

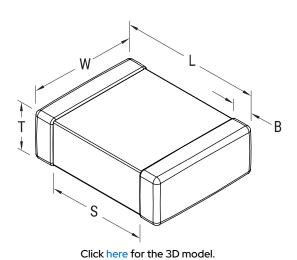
**Specifications** 

Insulation Resistance

Capacitance



ESD SMD Comm X7R, Ceramic, 0.22 uF, 5%, 100 VDC, X7R, SMD, MLCC, Temperature Stable, Electro Static Discharge, Class II, 1206



General Information	
Series	ESD SMD Comm X7R
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Electro Static Discharge, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Flexible Termination
Marking	No
AEC-Q200	No
Typical Component Weight	25 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
Chip Size	1206
L	3.3mm +/-0.4mm
W	1.6mm +/-0.35mm
T	0.9mm +/-0.20mm
S	1.5mm MIN
В	0.6mm +/-0.25mm

Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	5%
Voltage DC	100 VDC
ESD Level per AEC-Q200	25,000 V ESD Level
Dielectric Withstanding Voltage	250 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0 Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours

0.22 uF

4.5455 GOhms

Packaging Specifications	
Packaging	Bulk, Bag
Packaging Quantity	1

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