

CMH65R190SD

650V, 185mΩ typ., 23A N-Channel Super Junction Power MOSFET

General Description

The CMH65R190SD is power MOSFET using Cmos's advanced super junction technology. The resulting devices provide all benefits of a fast switching super junction MOSFET while offering an extremely fast and robust body diode. This combination of extremely low switching, commutation and conduction losses together with highest robustness make especially resonant switching applications more reliable, more efficient and lighter.

Features

- Multi-layer Epitaxial Chip Technology
- Low On-Resistance
- Ultra-fast body diode
- RoHS Compliant

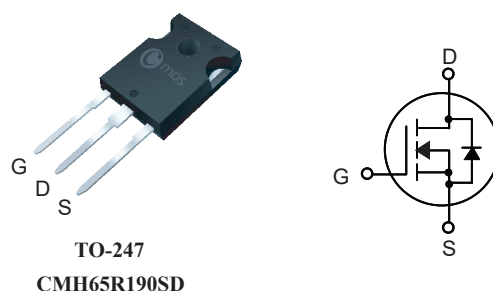
Product Summary

BVDSS	R _{DS(on)} max.	ID
650V	210mΩ	23A

Applications

- Charger
- Power Supply

TO-247 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	650	V
V _{GS}	Gate-Source Voltage	±30	V
I _D @T _C =25°C	Continuous Drain Current	23	A
I _D @T _C =100°C	Continuous Drain Current	15	A
I _{DM}	Pulsed Drain Current	92	A
EAS	Single Pulse Avalanche Energy (Note 1)	1215	mJ
P _D @T _C =25°C	Total Power Dissipation	250	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient	---	45	°C/W
R _{θJC}	Thermal Resistance Junction-case	---	0.50	°C/W

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Electrical Characteristics (T_J=25°C , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250μA	650	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V , I _D =10A	---	185	210	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250μA	3	---	5	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =650V , V _{GS} =0V	---	---	5	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±30V , V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =20V , I _D =20A	---	11	---	S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	1.0	---	Ω
Q _g	Total Gate Charge	V _{DD} =480V , I _D =10A V _{GS} =10V	---	41	---	nC
Q _{gs}	Gate-Source Charge		---	12	---	
Q _{gd}	Gate-Drain Charge		---	20	---	
T _{d(on)}	Turn-On Delay Time	V _{DS} =400V , I _D =10A R _G =25Ω , V _{GS} =10V	---	42.5	---	ns
T _r	Rise Time		---	58	---	
T _{d(off)}	Turn-Off Delay Time		---	105	---	
T _f	Fall Time		---	36	---	
C _{iss}	Input Capacitance	V _{DS} =45V , V _{GS} =0V , f=1MHz	---	1500	---	pF
C _{oss}	Output Capacitance		---	85	---	
C _{rss}	Reverse Transfer Capacitance		---	2	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V , Force Current	---	---	23	A
I _{SM}	Pulsed Source Current		---	---	92	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =10A , T _J =25°C	---	0.88	1.5	V
t _{rr}	Reverse Recovery Time	di/dt = 100A/μs	---	108	---	ns
Q _{rr}	Reverse Recovery Charge	V _{DS} =100V , I _{SD} =10A (Note 2,3)	---	0.54	---	μC

Note :

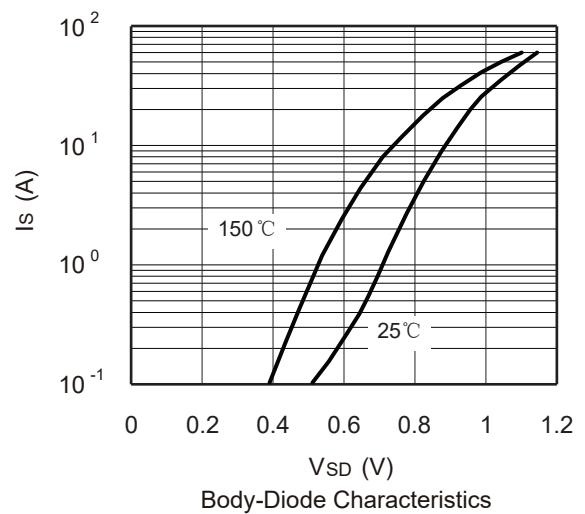
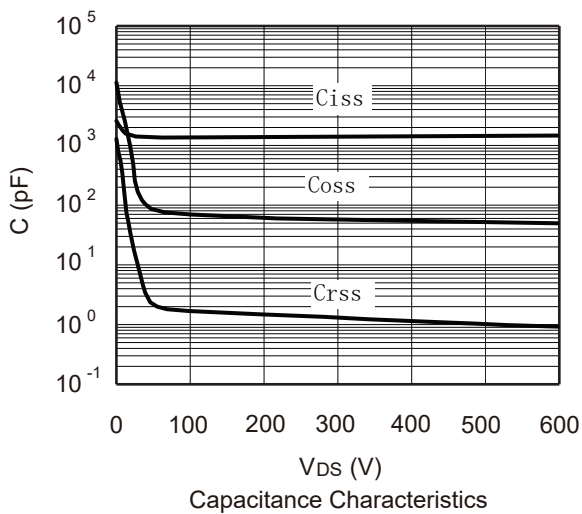
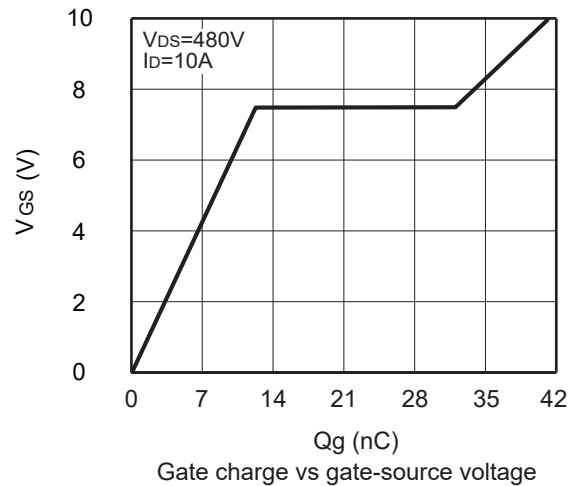
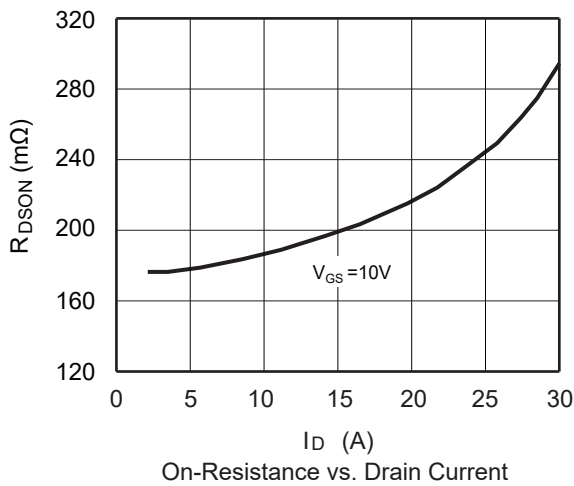
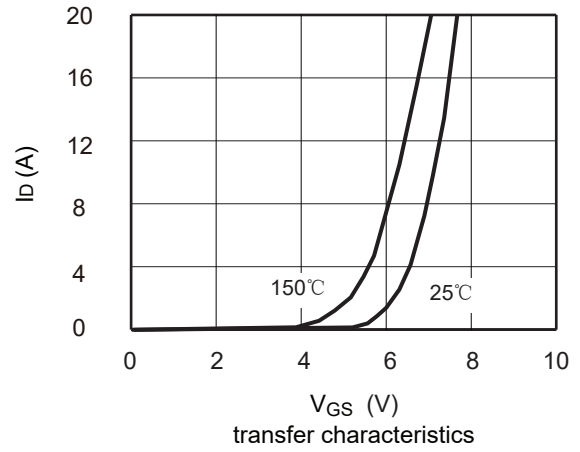
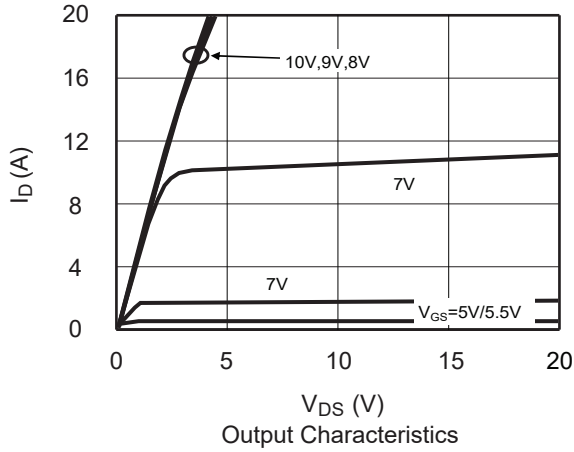
1. The EAS data shows Max. rating .The test condition is V_{DS}=80V , V_{GS}=10V , L=30mH , I_{AS}=9A.
2. Pulse test: Pulse width≤300us, Duty cycle≤2%.
3. Essentially independent of operating temperature typical characteristics.

This product has been designed and qualified for the consumer market.

CMOS assumes no liability for customers' product design or applications.

CMOS reserves the right to improve product design ,functions and reliability without notice.Please refer to the latest version of specification.

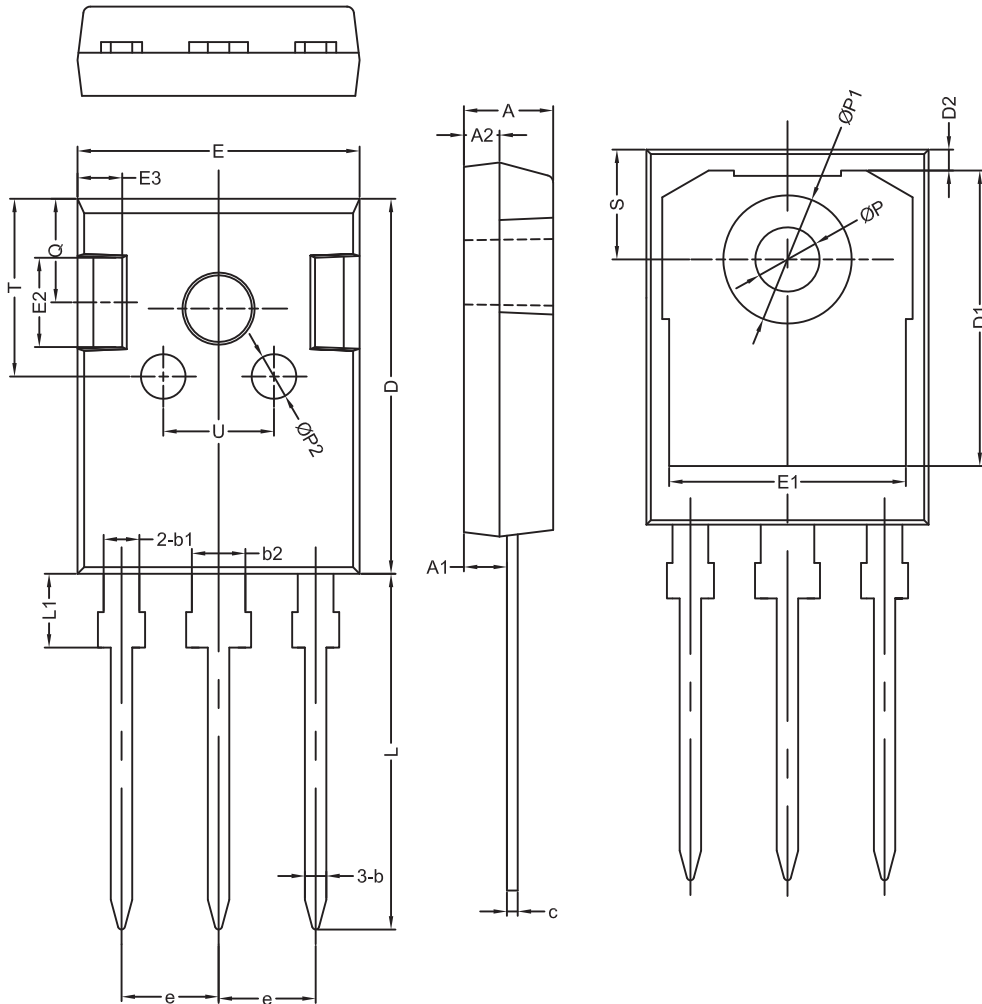
Typical Characteristics



Package Dimension

TO-247

Unit :mm



符号	机械尺寸/mm			符号	机械尺寸/mm		
	最小值	典型值	最大值		最小值	典型值	最大值
A	4.80	5.00	5.20	E2		5.00	
A1	2.21	2.41	2.61	E3		2.50	
A2	1.90	2.00	2.10	e		5.44	
b	1.10	1.20	1.35	L	19.42	19.92	20.42
b1		2.00		L1		4.13	
b2		3.00		P	3.50	3.60	3.70
c	0.55	0.60	0.75	P1		7.19	
D	20.80	21.00	21.20	P2		2.50	
D1		16.55		Q		5.80	
D2		1.20		S	6.05	6.15	6.25
E	15.60	15.80	16.0	T		10.00	
E1		13.30		U		6.20	