

## SIRA20DP-T1-RE3-VB Datasheet

Single-N Trench 20V DFN8(5X6) MOSFET

PRODU	PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $\Omega$ )	I <sub>D</sub> (A) <sup>a, e</sup>	Q <sub>g</sub> (Typ.)			
20	0.0017 at V <sub>GS</sub> = 4.5 V	150	64 nC			
20	0.0020 at V <sub>GS</sub> = 2.5 V	115	04110			

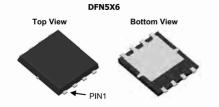
#### **FEATURES**

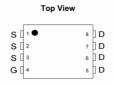
- TrenchFET® Power MOSFET
- 100 %  $R_g$  and UIS Tested

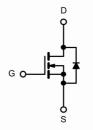
# **RoHS**

#### **APPLICATIONS**

- · OR-ing
- Server







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	<b>S</b> (T <sub>A</sub> = 25 °C, unle	ess otherwise	noted)	
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	20	V	
Gate-Source Voltage	V <sub>GS</sub>	± 12		
	T <sub>C</sub> = 25 °C		150 <sup>a, e</sup>	
Continuous Drain Current (T <sub>.I</sub> = 175 °C)	T <sub>C</sub> = 70 °C		115 <sup>e</sup>	1
Continuous Diain Current (1) = 175 C)	T <sub>A</sub> = 25 °C	. I <sub>D</sub>	55 <sup>b, c</sup>	A
	T <sub>A</sub> = 70 °C		49.8 <sup>b, c</sup>	1 ^
Pulsed Drain Current		I <sub>DM</sub>	350	1
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	70	
Single Pulse Avalanche Energy	L = 0.11111H	E <sub>AS</sub>	123	mJ
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	1	90 <sup>a, e</sup>	A
Continuous Source-Diain Diode Current	T <sub>A</sub> = 25 °C	. I <sub>S</sub>	3.13 <sup>b, c</sup>	1 ^
	T <sub>C</sub> = 25 °C	- P <sub>D</sub>	250 <sup>a</sup>	
Maximum Dayyar Disaination	T <sub>C</sub> = 70 °C		175	w
Maximum Power Dissipation	T <sub>A</sub> = 25 °C		3.75 <sup>b, c</sup>	] vv
	T <sub>A</sub> = 70 °C		2.63 <sup>b, c</sup>	
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	12	17	°C/W		
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	1.1	1.6	C/VV		

#### Notes:

- a. Based on T<sub>C</sub> = 25 °C.
  b. Surface mounted on 1" x 1" FR4 board.
  c. t = 10 s.
  d. Maximum under steady state conditions is 90 °C/W.
- e. Calculated based on maximum junction temperature. Package limitation current is 90 A.

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Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V, I}_{D} = 250 \mu\text{A}$	20			V
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = 250 μA		35		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	Ι <sub>D</sub> – 250 μΑ		- 7.5		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.5		2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
7 0	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V			1	μΑ
Zero Gate Voltage Drain Current		V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	90			Α
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 32 A		0.0017		Ω
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 29 A		0.0020		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 32 A		100		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			4975		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ = 12.5 V, $V_{GS}$ = 0 V, f = 1 MHz		1995		
Reverse Transfer Capacitance	C <sub>rss</sub>			990		
T	Qg	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 32 \text{ A}$		64		nC
Total Gate Charge				81.5		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 29 \text{ A}$		37		
Gate-Drain Charge	Q <sub>gd</sub>			33		
Gate Resistance	R <sub>q</sub> f = 1 MHz			1.4		Ω
Turn-On Delay Time	t <sub>d(on)</sub>			19	31	ns
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.555 $\Omega$		12	20	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 27 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		75	112	
Fall Time	t <sub>f</sub>			11	17	
Turn-On Delay Time	t <sub>d(on)</sub>			56	87	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_{L}$ = 0.625 $\Omega$		180	270	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 24 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		58	86	
Fall Time	t <sub>f</sub>			14	23	
Drain-Source Body Diode Characteristic	s					ļ.
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			150	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				350	A
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 22 A		0.7	1.0	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			52	78	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	1 - 00 A - 1944 - 400 A / - T - 05 00		70.2	105	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		27		
Reverse Recovery Rise Time	t <sub>b</sub>			25		ns

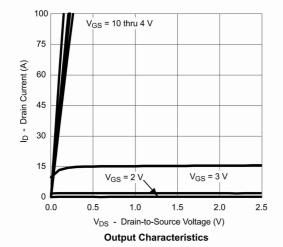
- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

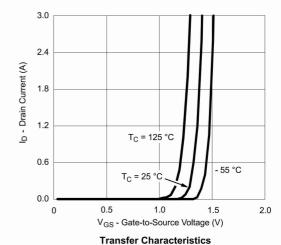
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

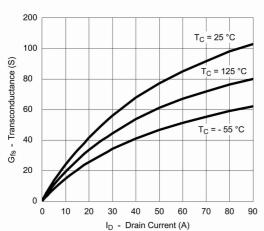
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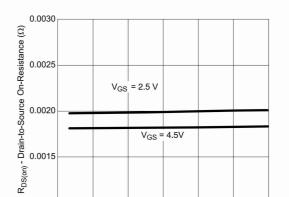


#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





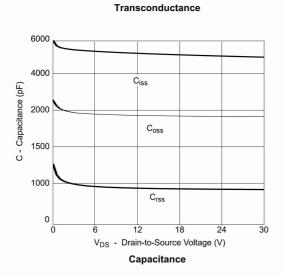




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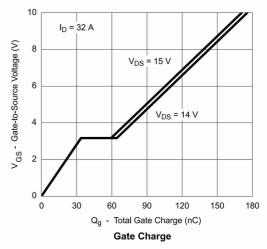
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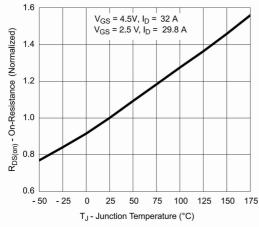
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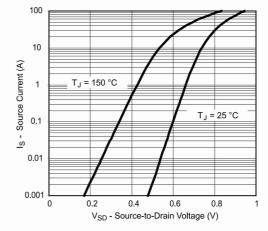
90





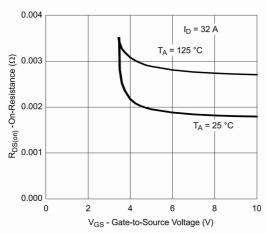
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

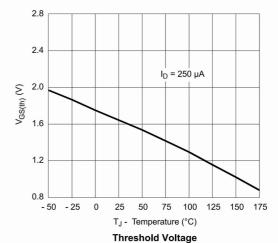




#### On-Resistance vs. Junction Temperature

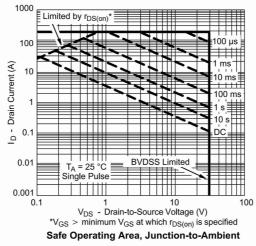






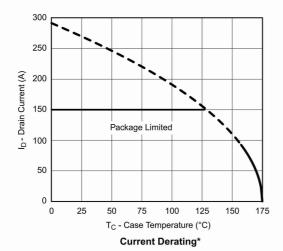
 $\rm R_{\rm DS(on)}$  vs.  $\rm V_{\rm GS}$  vs. Temperature

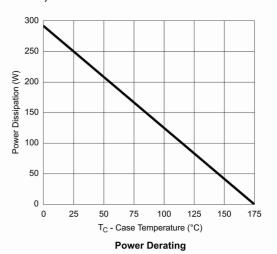




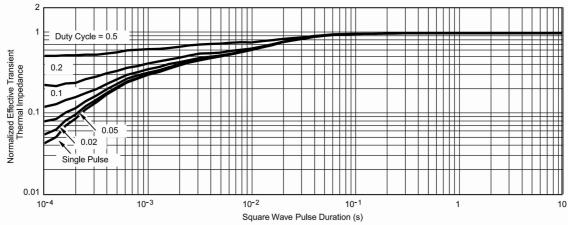


#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





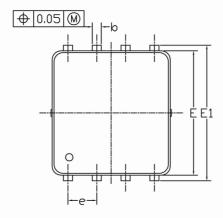
\* The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

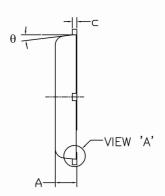


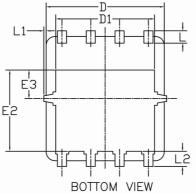
Normalized Thermal Transient Impedance, Junction-to-Case

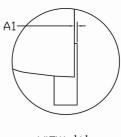


DFN5x6\_8L\_EP1\_P PACKAGE OUTLIN



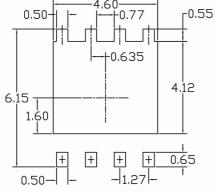






<u>VIEW 'A'</u> (SCALE 5:1)

## 



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
A	0.85	0.95	1.00	0.033	0.037	0.039	
A1	0.00		0.05	0.000		0.002	
b	0.30	0.40	0.50	0.012	0.016	0.020	
c	0. 15	0.20	0. 25	0.006	0.008	0.010	
D	5. 10	5. 20	5. 30	0. 201	0. 205	0. 209	
D1	4. 25	4. 35	4. 45	0. 167	0.171	0. 175	
E	5. 45	5. 55	5. 65	0. 215	0.219	0. 222	
E1	5. 95	6.05	6. 15	0. 234	0. 238	0. 242	
E2	3. 525	3.625	3. 725	0.139	0.143	0. 147	
E3	1. 175	1. 275	1.375	0.046	0.050	0.054	
e	1. 27 BSC			0.050 BSC			
L	0.45	0. 55	0.65	0.018	0.022	0.026	
L1	0		0.15	0		0.006	
L2	0.68 REF			0.027 REF			
θ	0°		10°	0°		10°	

NOTE UNIT: mm

- 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
- 2. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



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