

FDS4935BZ-VB Datasheet

Trench -30V Dual-P+P SOP8 MOSFET

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^{d, e}	Q _g (Typ.)
- 30	0.021 at V _{GS} = - 10 V	- 8.0	15 nC
	0.028 at V _{GS} = - 4.5 V	- 7.0	

FEATURES

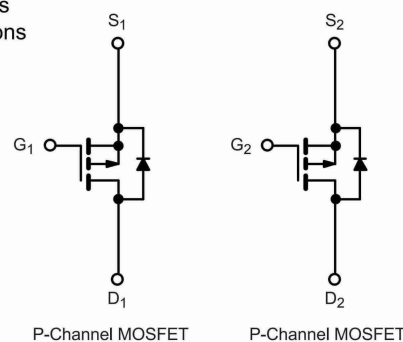
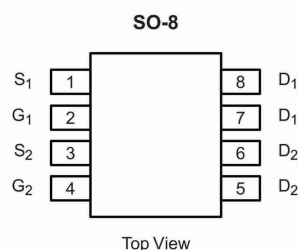
- Halogen-free
- TrenchFET® Power MOSFET
- 100 % UIS Tested



RoHS
COMPLIANT

APPLICATIONS

- Load Switches
 - Notebook PCs
 - Desktop PCs
 - Game Stations



ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	- 30	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current (T _J = 150 °C)	I _D	T _C = 25 °C	A
		T _C = 70 °C	
		T _A = 25 °C	
		T _A = 70 °C	
Pulsed Drain Current	I _{DM}	- 32 ^e	A
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	
		T _A = 25 °C	
Avalanche Current	I _{AS}	- 20	mJ
Single-Pulse Avalanche Energy	E _{AS}	20	
Maximum Power Dissipation	P _D	T _C = 25 °C	W
		T _C = 70 °C	
		T _A = 25 °C	
		T _A = 70 °C	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{a, c}	R _{thJA}	38	50	°C/W
Maximum Junction-to-Foot	R _{thJF}	20	25	

Notes:

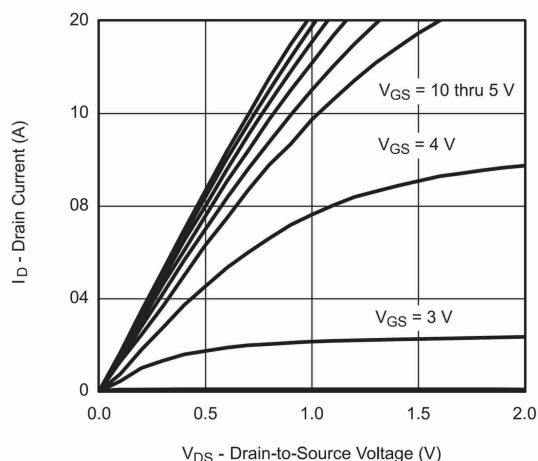
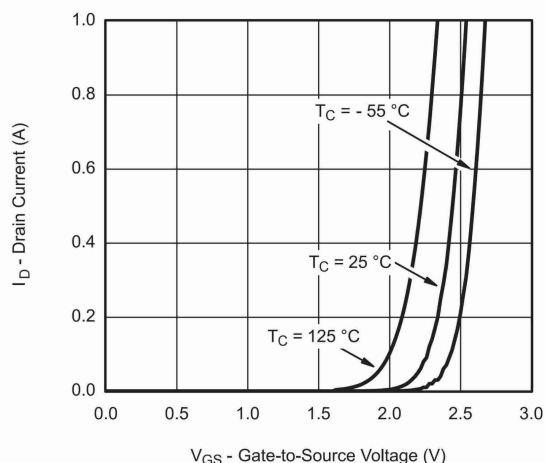
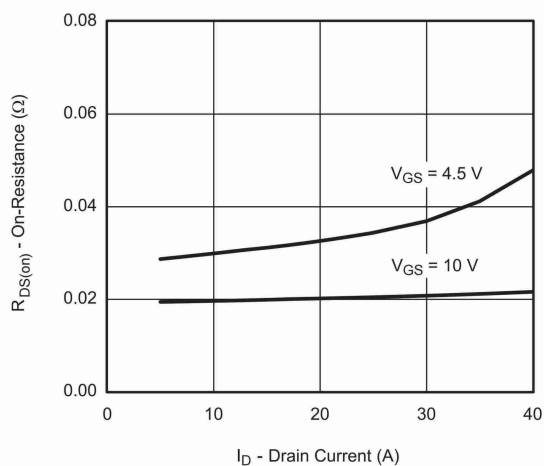
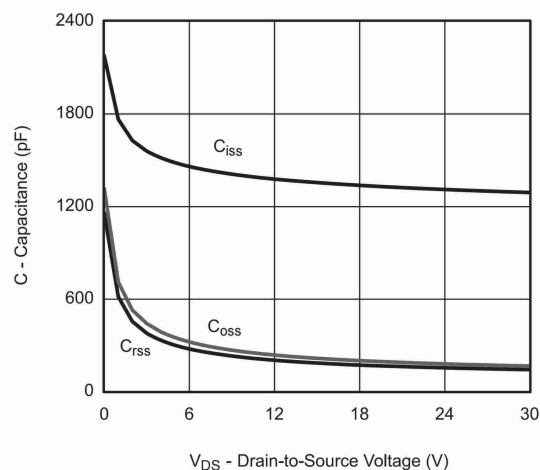
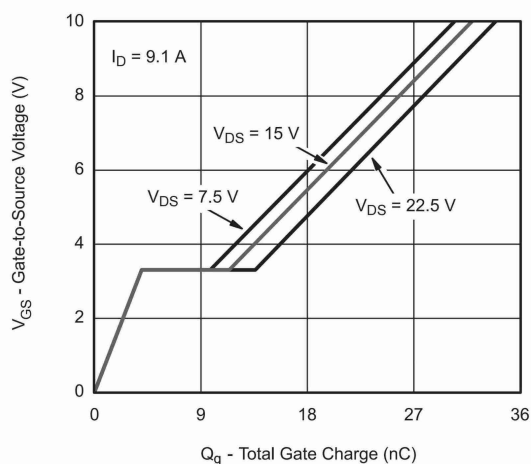
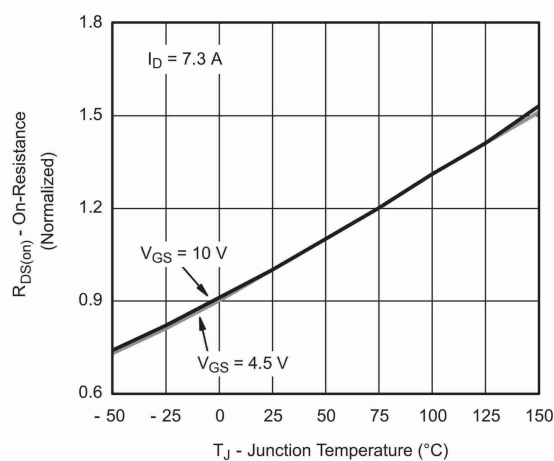
- Surface mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under Steady State conditions is 85 °C/W.
- Based on T_C = 25 °C.
- Limited by package.

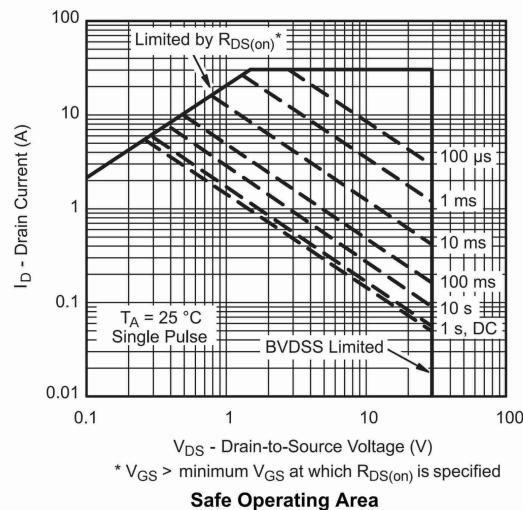
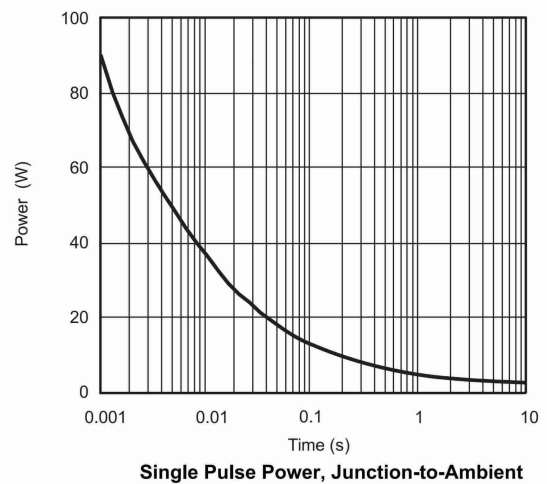
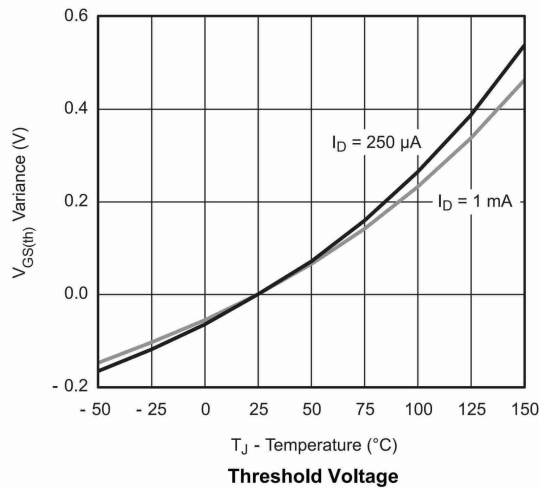
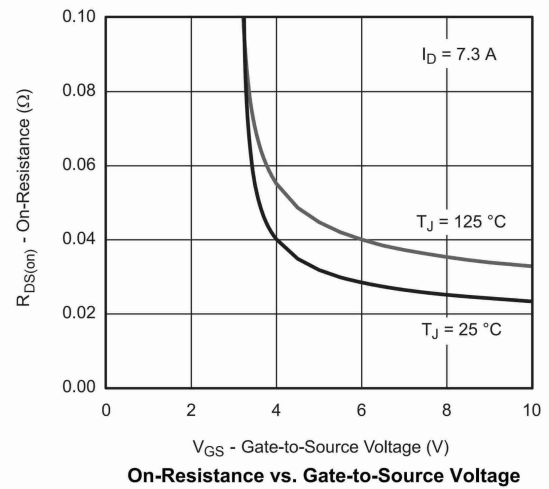
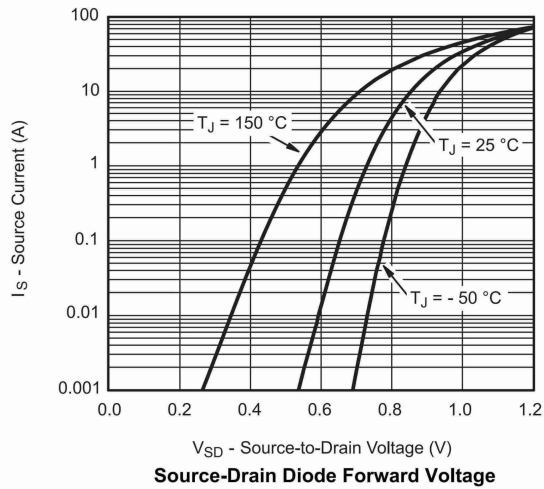
SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 30			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = - 250 μA		- 31		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			4.5		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 1.0		- 3.0	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V			- 1	μA
		V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C			- 5	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ - 10 V, V _{GS} = - 10 V	- 30			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 7.3 A		0.021		Ω
		V _{GS} = - 4.5 V, I _D = - 6.2 A		0.028		
Forward Transconductance ^a	g _{fs}	V _{DS} = - 10 V, I _D = - 9.1 A		23		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		1350		pF
Output Capacitance	C _{oss}			215		
Reverse Transfer Capacitance	C _{rss}			185		
Total Gate Charge	Q _g	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 9.1 A		32	50	nC
		V _{DS} = - 15 V, V _{GS} = - 4.5 V, I _D = - 9.1 A		15	25	
Gate-Source Charge	Q _{gs}			4		
Gate-Drain Charge	Q _{gd}			7.5		
Gate Resistance	R _g	f = 1 MHz		5.8		Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 15 V, R _L = 15 Ω I _D ≅ - 1 A, V _{GEN} = - 10 V, R _g = 1 Ω		10	15	ns
Rise Time	t _r			8	15	
Turn-Off DelayTime	t _{d(off)}			45	70	
Fall Time	t _f			12	25	
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 15 V, R _L = 15 Ω I _D ≅ - 1 A, V _{GEN} = - 4.5 V, R _g = 1 Ω		42	70	
Rise Time	t _r			35	60	
Turn-Off DelayTime	t _{d(off)}			40	70	
Fall Time	t _f			16	30	
Drain-Source Body Diode Characteristics						
Continous Source-Drain Diode Current	I _S	T _C = 25 °C			- 4.1	A
Pulse Diode Forward Current	I _{SM}				- 32	
Body Diode Voltage	V _{SD}	I _S = - 2 A, V _{GS} = 0 V		- 0.75	- 1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = - 2 A, dI/dt = 100 A/μs, T _J = 25 °C		34	60	ns
Body Diode Reverse Recovery Charge	Q _{rr}			22	40	nC
Reverse Recovery Fall Time	t _a			11		ns
Reverse Recovery Rise Time	t _b			23		

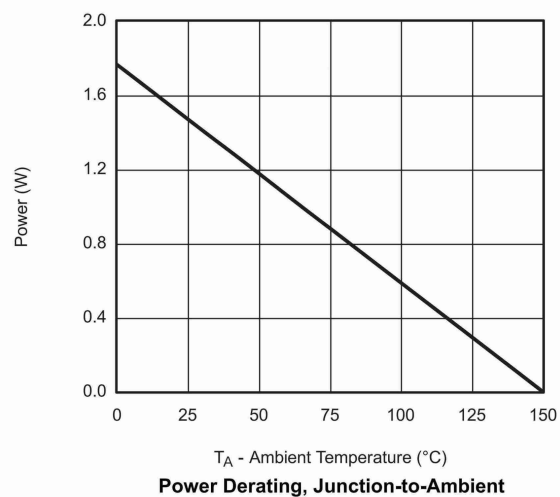
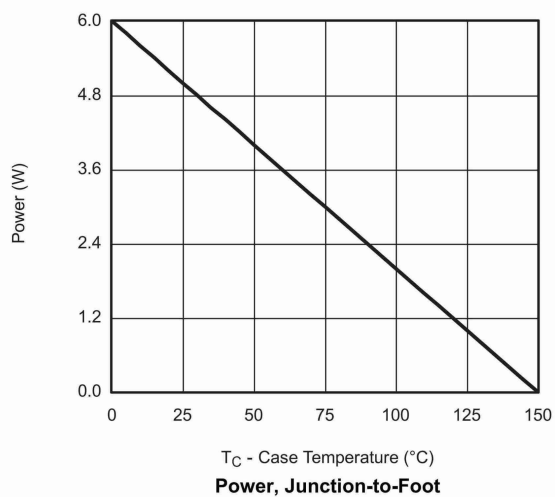
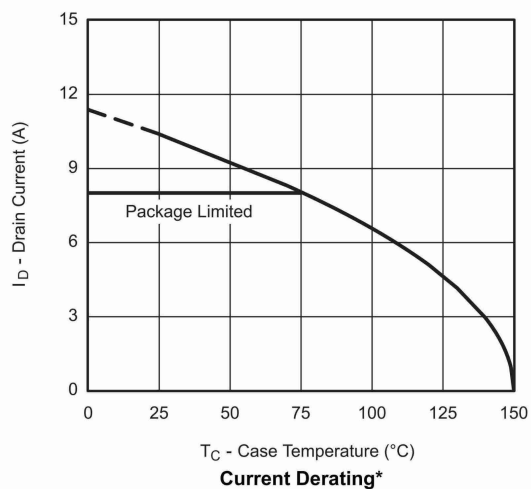
Notes:

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

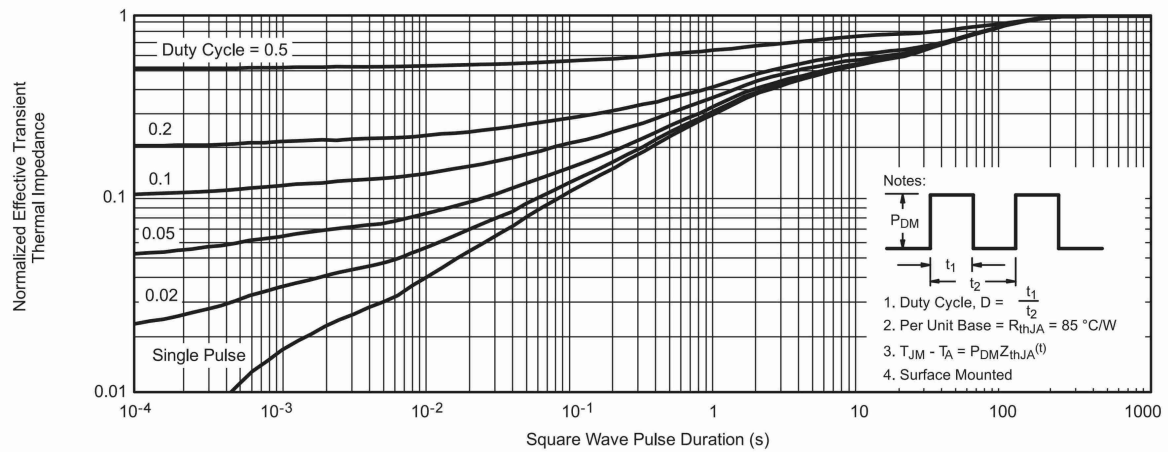
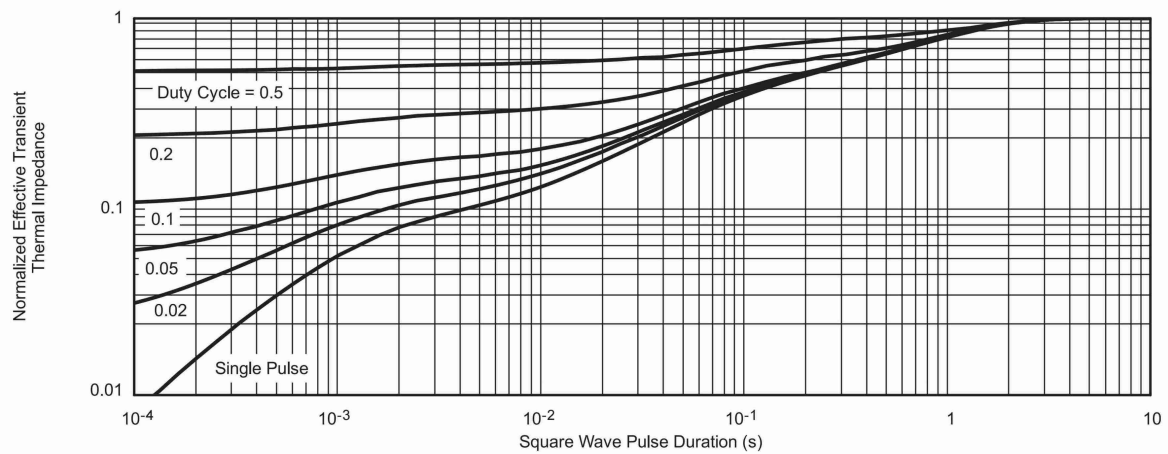
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Output Characteristics

Transfer Characteristics

On-Resistance vs. Drain Current

Capacitance

Gate Charge

On-Resistance vs. Junction Temperature

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted


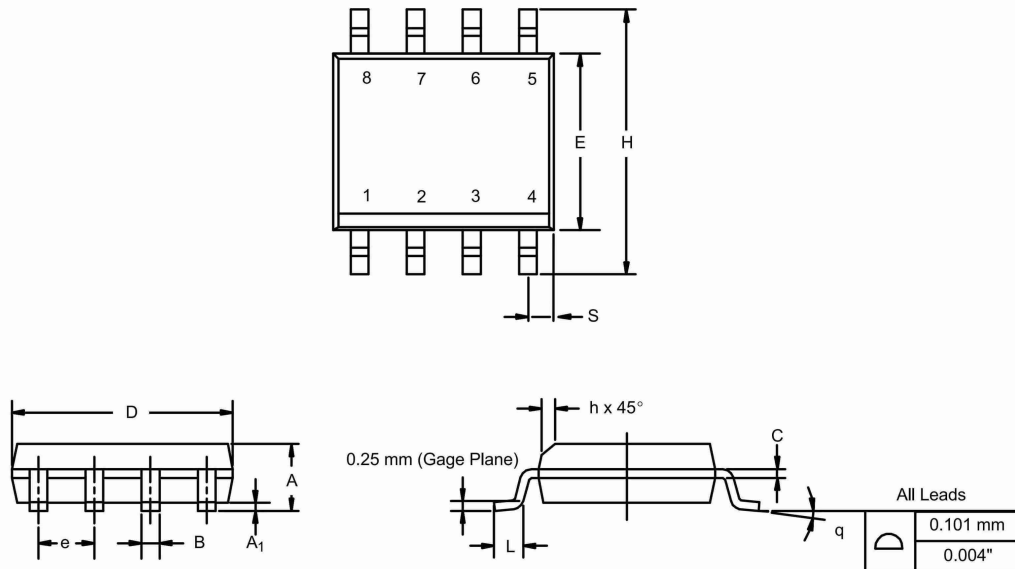
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted


* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Normalized Thermal Transient Impedance, Junction-to-Ambient

Normalized Thermal Transient Impedance, Junction-to-Foot

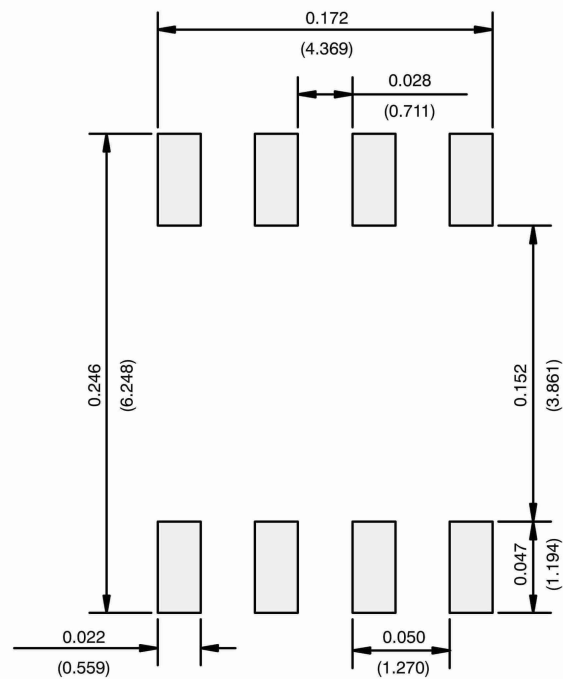
SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A ₁	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06				
DWG: 5498				

RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads
Dimensions in Inches/(mm)

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