

Features

- · High Blocking Voltage with Low On-Resistance
- · High Speed Switching with Low capacitances
- · Avalanche Ruggednes

Applications

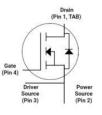
- · Solar Inverters
- · Switch Mode Power Supplies
- · Auxiliary power supplies
- · Smart meters

Ordering Part Number	Package	Marking	
G3R75MT12J	TO-263-7L	G3751KN	





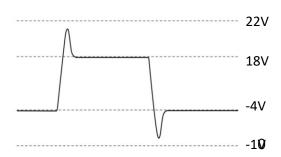
TO-263-7L Package



Maximum Ratings (Tc = 25 °C unless otherwise specifed)

Parameter	Symbol	Value	Unit
Drain-source voltage	V _D s	1200	V
Continuous drain current			
Tc = 25°C Tc = 100°C	lo	30 21	Α
Source current(Body Diode)			
Tc = 25°C Tc = 100°C	Is	30 21	А
Pulsed drain current (Tc = 25°C, tp limited by T _{jmax})	D pulse	80	Α
Avalanche energy, single pulse (L=10mH)	Eas	600	mJ
Gate-Source voltage	Vgs	-4/+18	V
Gate-Source voltage(dynamic,Absolute maximum values)	VGSmax	-8/+22	V
Power dissipation (Tc = 25°C)	Ptot	136	W
Operating junction and storage temperature	Tj, Tstg	-55+175	°C

• Example of acceptable Vgs waveform





Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, junction – case. Max	RthJC	1.1	°C/W
Thermal resistance, junction – ambient. Max	RthJA	40	0/11

Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	Symbol Value Unit	Test Condition				
		min.	typ.	max.	J Cilit	Tool Condition
Static Characteristics		•	•			
Drain-source breakdown voltage	V_{DSS}	1200	-	-	V	V _{GS} =0V, I _D =100uA
Gate threshold voltage	V _{GS(th)}	2.3	2.8	3.6	V	$V_{DS}=V_{GS},I_{D}=5mA$
						V _{DS} =1200V,V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	-	1	10	μA	T _C =25°C
		-	5	-		T _C =175°C
Gate-source leakage current	I _{GSS}	-		100	nA	V _{GS} =18V,V _{DS} =0V
						V _{GS} =18V, I _D =20A,
Drain-source on-state resistance	R _{DS(on)}	-	75	85	mΩ	T _J =25°C
		-	125	-		T _J =175°C
Transconductance	g _{fs}	-	10	-	S	V_{DS} =20 V , I_{D} =20 A
Dynamic Characteristics						
Input Capacitance	C _{iss}	-	920	-		$V_{DS} = 1000V$ $V_{GS} = 0V$
Output Capacitance	C _{oss}	-	57	-] pF	V _{GS} – UV T _J = 25°C
Reverse Transfer Capacitance	C _{rss}	-	3.9	-		V _{AC} = 25mV f = 1MHz
Gate Total Charge	Q_{G}	-	40	-		V _{DS} = 800V
Gate-Source charge	Q_{gs}	-	7	-	nC	V _{GS} = -4/18V
Gate-Drain charge	Q_{gd}	-	19	-		I _D = 20A
Turn-On Switching Energy	E _{ON}	-	320	-		
Turn-Off Switching Energy	E _{OFF}	-	49	-	μJ	V _{DD} = 800V
Turn-on delay time	t _{d(on)}	-	19	-		$V_{GS} = -4/+15V$
Rise time	t _r	-	21	-		$I_D = 20A$ $R_G = 0\Omega$
Turn-off delay time	t _{d(off)}	-	15	-	ns	L = 120uH
Fall time	t _f	-	17	-		
Gate resistance	R _G	-	1.5	-	Ω	V _{AC} = 25mV, f=1MHz



Body Diode Characteristics

			Value			
Parameter	Symbol	min.	typ.	max.	Unit	Test Condition
Body Diode Forward Voltage	V_{SD}	-	4.2	-	V	V _{GS} =-4V,I _{SD} =10A, T _J =25°C
, <u>-</u>	35	-	3.8	-		V _{GS} =-4V,I _{SD} =10A, T _J =175°C
Body Diode Forward Current	I _{SD}	-	-	30		
Pulsed Body Diode Forward Current	I _{SDM}	-	-	89	А	V _{GS} =-4V,T _J =25°C
Reverse Recovery Time	t _{rr}	-	39.6	-	ns	
Reverse Recovery Charge	Q _{rr}	-	141.1	-	nC	V 000V
Reverse Recovery Energy	E _{REC}	-	62.9	-	uJ	$V_R = 800V,$ $V_{GS} = -4V$ $I_D = 20A$
Peak Reverse Recovery Current	I _{rrm}	-	6.2	-	Α	$I_D = 20A$ di/dt = 700A/µS $I_A = 25^{\circ}C$
Charge Time	t _A	-	9.9	-	ns	1j 25 C
DisCharge Time	t _B	-	29.7	-	ns	
Reverse Recovery Time	t _{rr}	-	45.4	-	ns	
Reverse Recovery Charge	Q _{rr}	-	397	-	nC	V 900V
Reverse Recovery Energy	E _{REC}	-	180.1	-	uJ	$V_R = 800V,$ $V_{GS} = -4V$ $I_D = 20A$
Peak Reverse Recovery Current	I _{rrm}	-	13.8	-	Α	I _D = 20A di/dt = 700A/μS T _J = 175°C
Charge Time	t _A	-	30.8	-	ns	., 1100
DisCharge Time	t _B	-	14.9	-	ns	



Typical Performance Characteristics

Fig 1. Output Characteristic (T_J=-40°C)

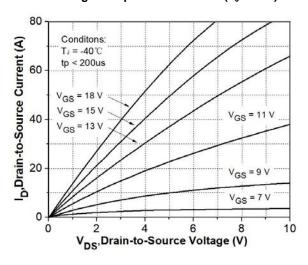


Fig 3. Output Characteristic (T_J=175℃)

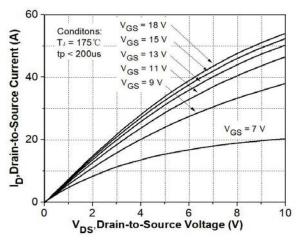


Fig 5: Rds(on) vs. Temperature

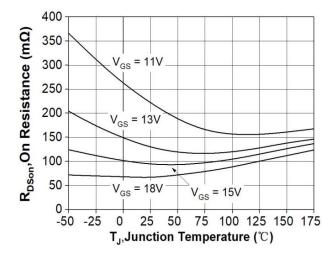


Fig 2. Output Characteristic (T_J=25℃)

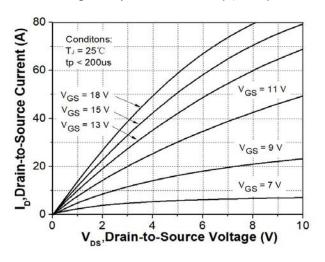


Fig 4: Rdson Vs Ids Characteristic

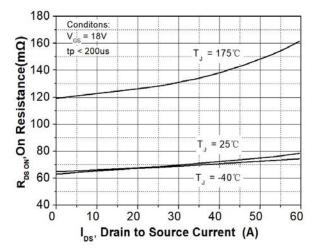


Fig 6: Transfer Characteristic

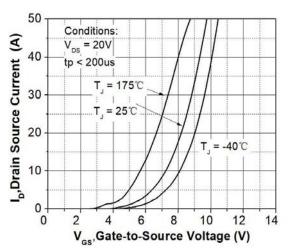




Fig 7: Body-diode Characteristic (T_J=-40°C)

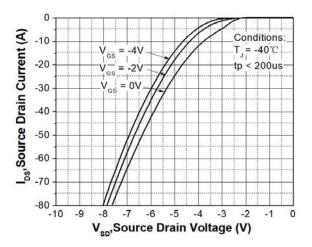


Fig 9: Body-diode Characteristic (T_J=175℃)

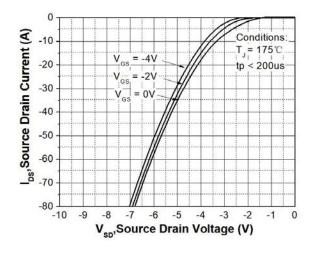


Fig 11: Gate Charge Characteristics

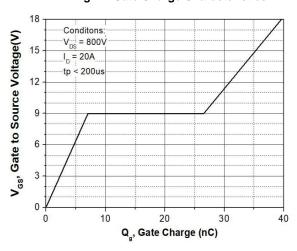


Fig 8: Body-diode Characteristic (T_J=25℃)

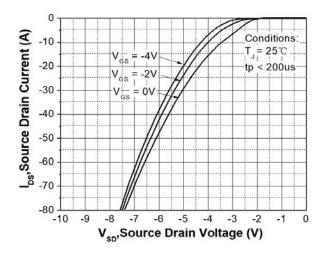


Fig 10: V_{TH} Vs T_J Temperature Characteristic

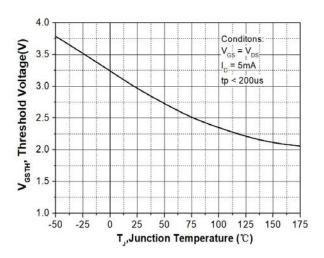


Fig 12: 3rd Quadrant Characteristic(T_J=-40°C)

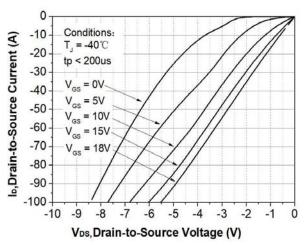




Fig 13: 3rd Quadrant Characteristic(T_J=25°C)

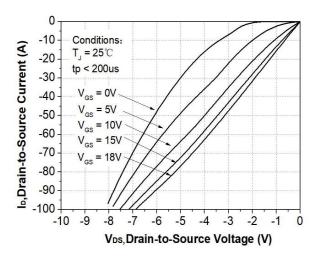


Fig 15: Capacitance Characteristic

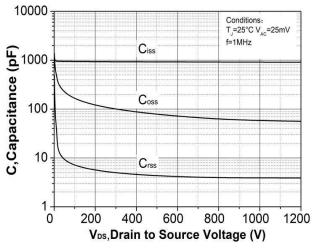


Fig 17: Transient Thermal Impedance

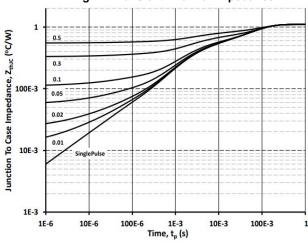


Fig 14: 3rd Quadrant Characteristic(T_J=175°C)

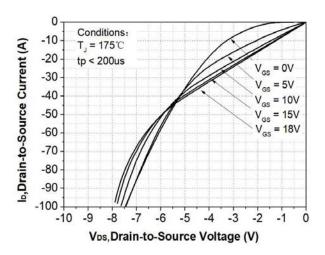
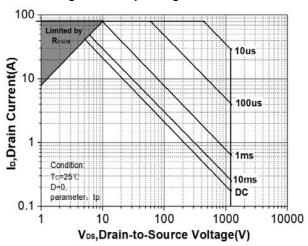


Fig 16: Safe Operating Area





Test Circuit Schematic

Figure A. Definition of switching times

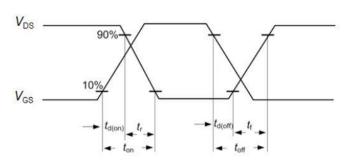


Figure B. Dynamic test circuit

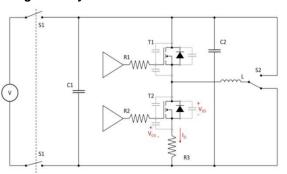


Figure C. Definition of body diodeswitching characteristics

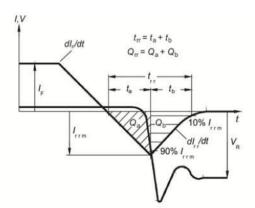
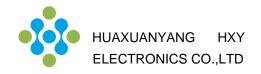
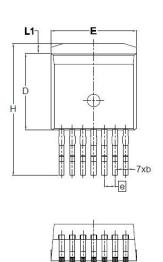


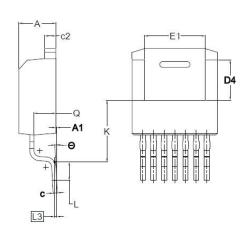
Figure C. Definition of diode switching characteristics



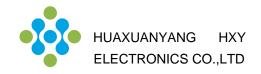
Package Dimensions

Package TO-263-7L





SYMBOL	DIMENSIONS				
	MIN.	NOM.	MAX.		
Α	4.30	4.40	4.50		
.A1	0.00	0.10	0.25		
b	0.50	0.60	0.70		
С	0.45	0.50	0.60		
c2	1.20	1.30	1.40		
D	8,93	9.08	9.23		
D4	4.65	4.80	4.95		
E	10.08	10.18	10.28		
E1	6.82	7.22	7.62		
е		1.27 BSC			
н	15.00	15.70	16.00		
K	6	7.30			
L	1.90	2.20	2.50		
L1	1.00	1.20	1.40		
L3	0.25 BSC				
Q	2.45	2.60	2.75		
.⊚:	0°	3°	7°		



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