

General Description

The AGM628DM1 combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

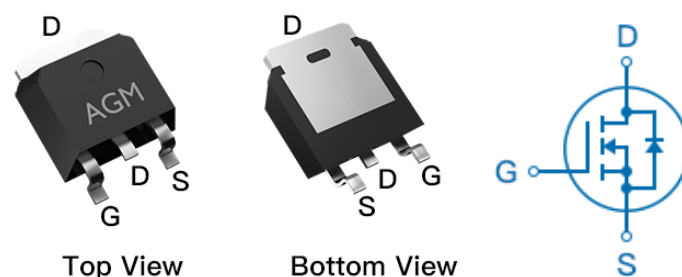
Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
60V	31mΩ	20A

TO-252 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM628D	AGM628DM1	TO-252	330mm	16mm	2500

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	60	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25°C) (Note 1)	20	A
	Drain Current-Continuous(Tc=100°C)	16	A
IDM (pluse)	Drain Current-Pulsed (Note 2)	80	A
PD	Maximum Power Dissipation(Tc=25°C)	40	w
	Maximum Power Dissipation(Tc=100°C)	20	w
EAS	Avalanche energy (Note 3)	100	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 175	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) ¹	---	50	°C/W
RθJC	Thermal Resistance Junction-Case ¹	---	3.7	°C/W

Table 3. Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	60	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=60V,VGS=0V	--	--	1.0	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	1.2	1.7	2.2	V
gFS	Forward Transconductance	VDS=5V,ID=5A	--	10	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=15A	--	31	38	mΩ
		VGS=4.5V, ID=10A	--	33	45	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=30V,VGS=0V, F=100 kHz	--	1010	--	pF
Coss	Output Capacitance		--	115	--	pF
Crss	Reverse Transfer Capacitance		--	105	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	3.3	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=10V,VDS=30V RGEN=2.5Ω	--	6.1	--	nS
tr	Turn-on Rise Time		--	2.5	--	nS
td(off)	Turn-Off Delay Time		--	15	--	nS
tf	Turn-Off Fall Time		--	2.3	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=30V, ID=15A	--	24	--	nC
Qgs	Gate-Source Charge		--	3.5	--	nC
Qgd	Gate-Drain Charge		--	5.1	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	20	A
VSD	Forward on Voltage	VGS=0V,IS=15A	--	--	1.2	V
trr	Reverse Recovery Time	Is=15A , dI/dt=100A/μs , TJ=25℃	--	36	--	ns
Qrr	Reverse Recovery Charge		--	53	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: T_J=25°C

Typical electrical and thermal characteristics:

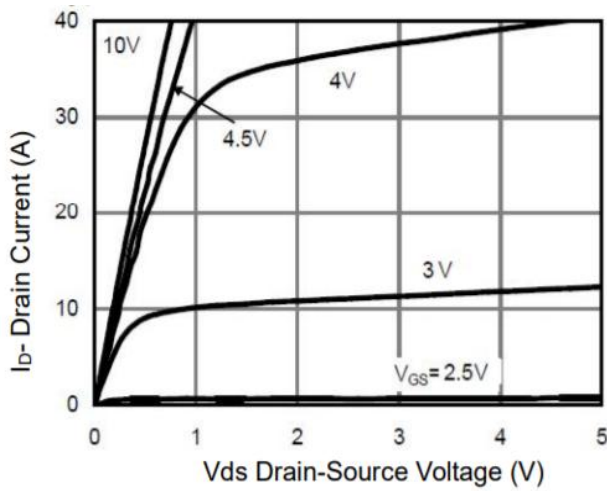


Figure 1: Typical Output Characteristics

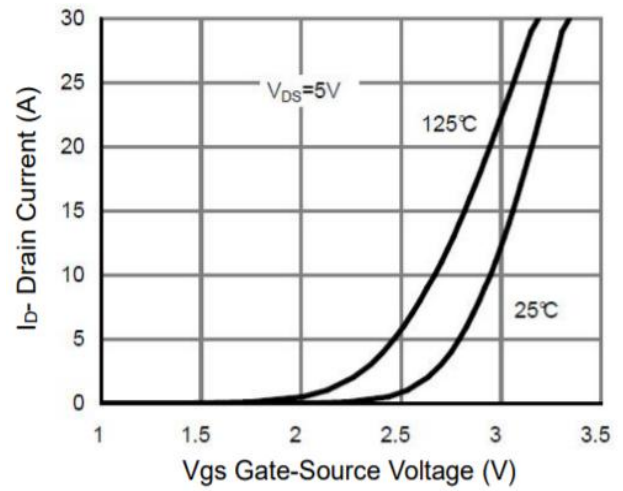


Figure 2: Typical Transfer Characteristics

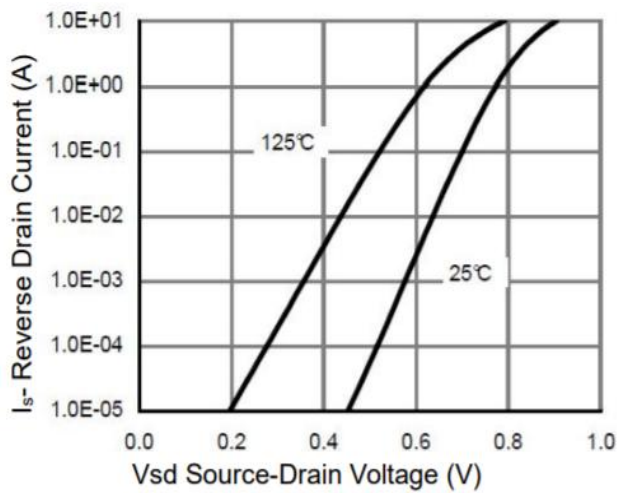


Figure 3: Body-Diode Characteristics

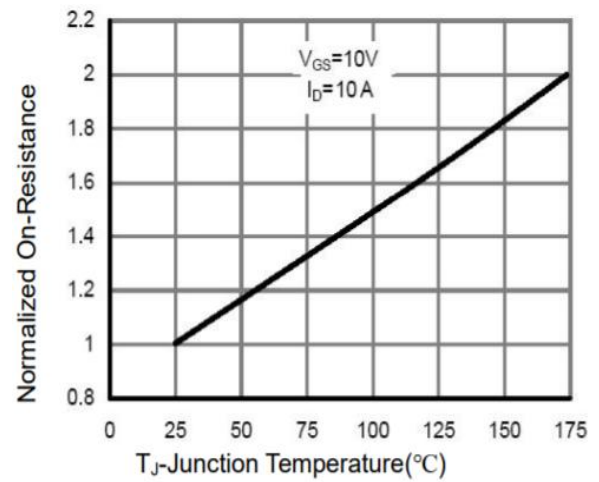


Figure 4: On-Resistance vs. Junction Temperature

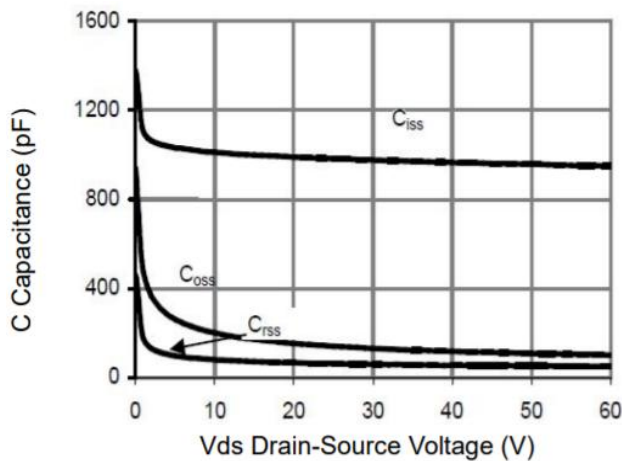


Figure 5: Capacitance Characteristics

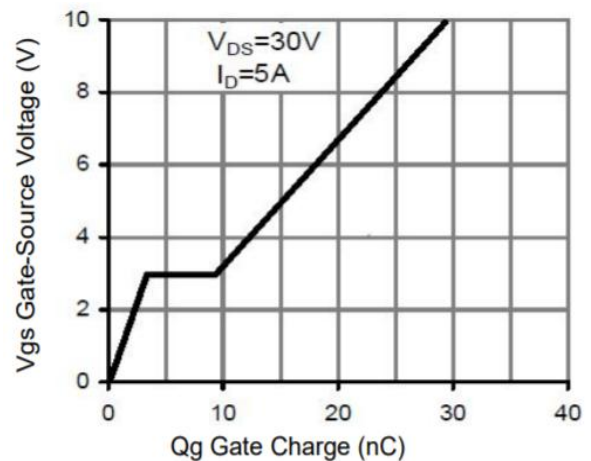


Figure 6: Gate-Charge Characteristics

Typical electrical and thermal characteristics:

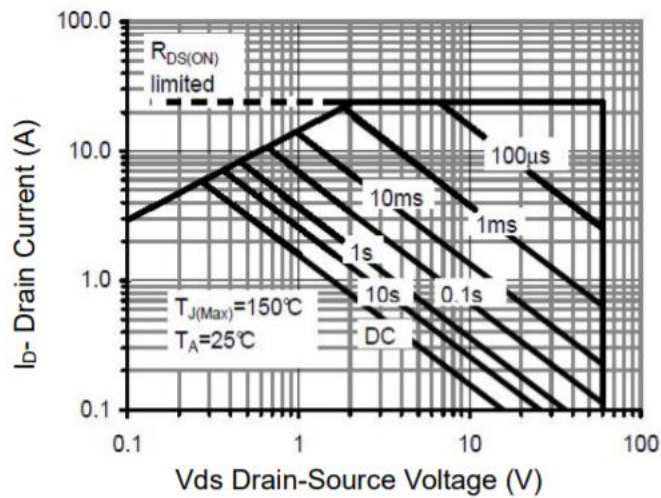


Figure 7: Maximum Forward Biased Safe Operating Area

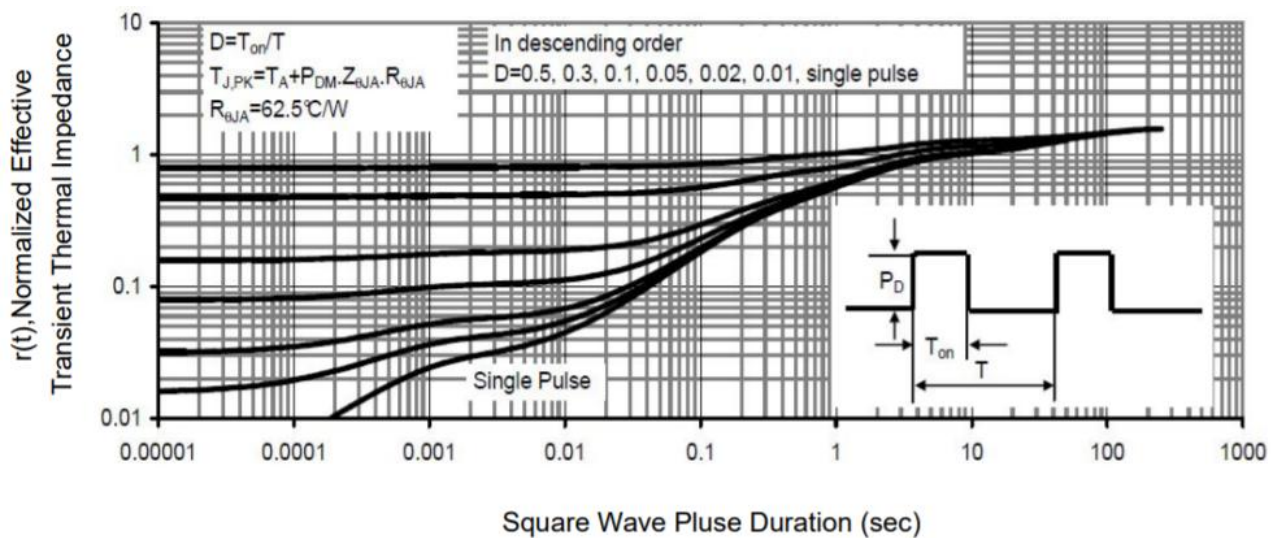
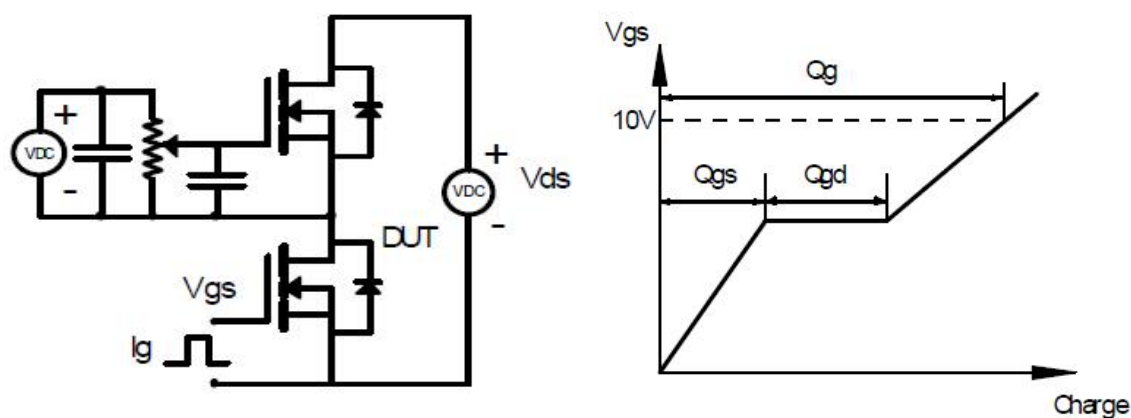


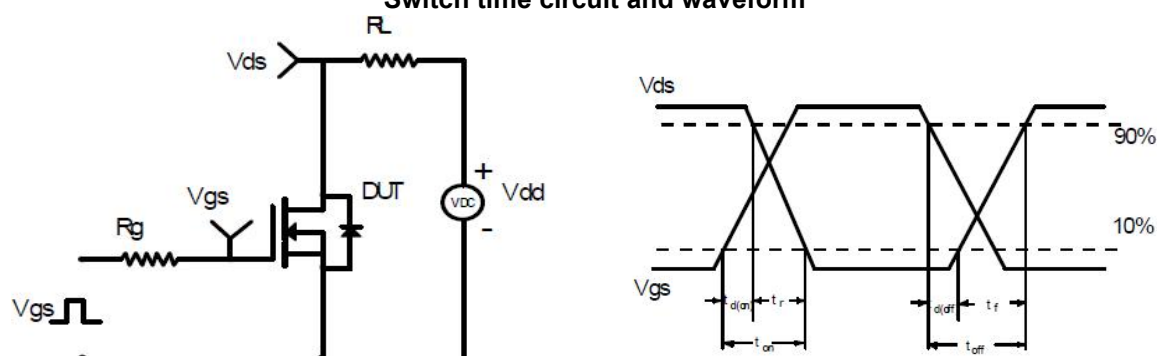
Figure 8: Normalized Maximum Transient Thermal Impedance

Test circuits and Waveforms:

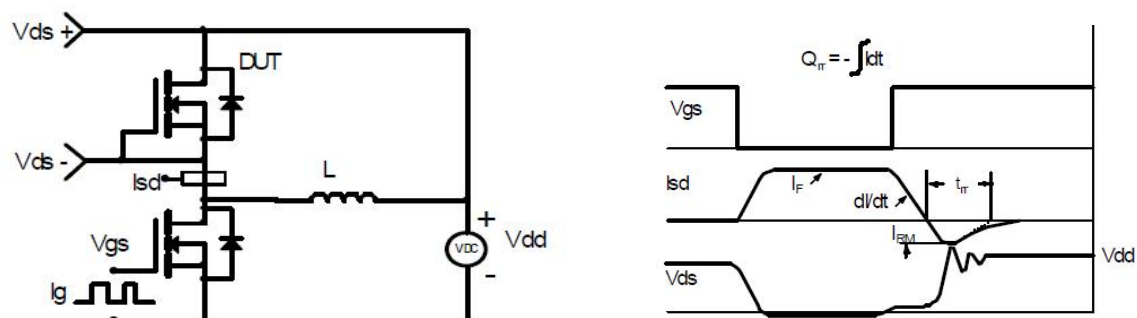
Gate charge circuit and waveform



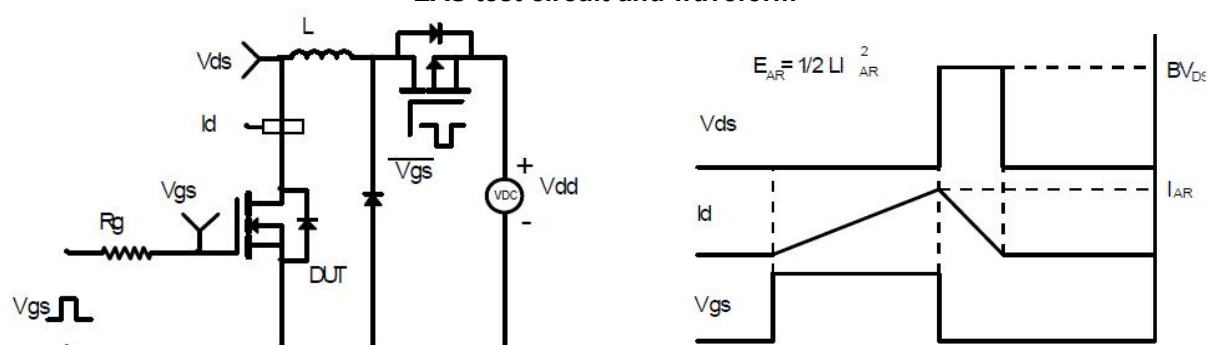
Switch time circuit and waveform



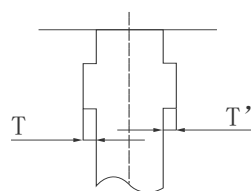
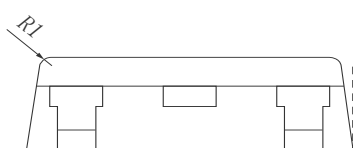
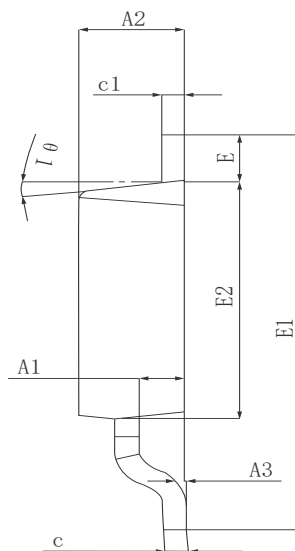
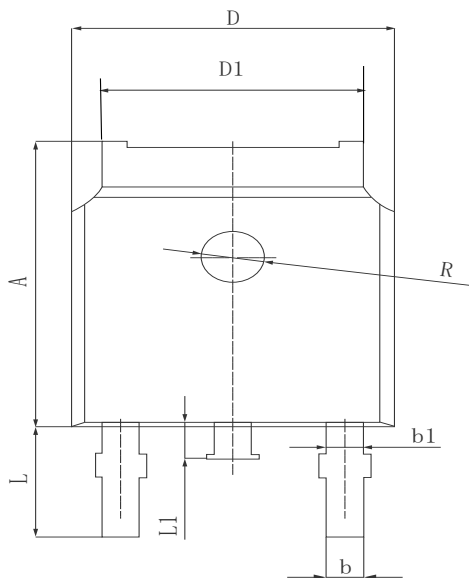
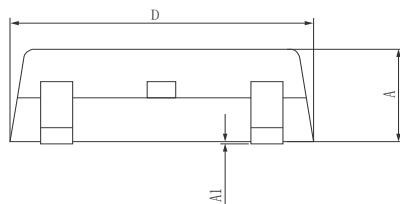
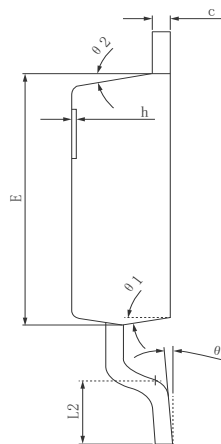
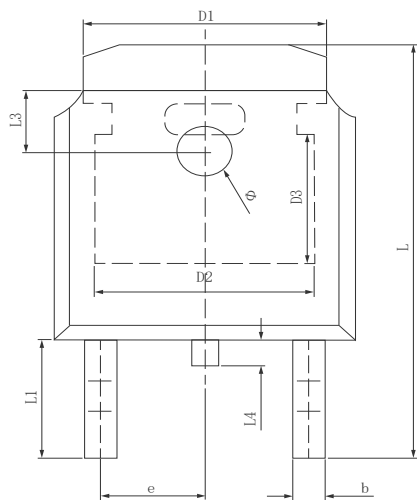
Reverse recovery test circuit and waveform



EAS test circuit and waveform



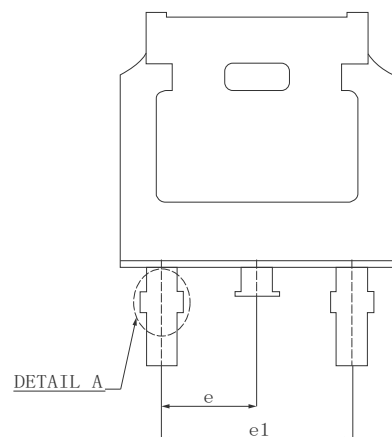
Dimensions (TO-252)



0 ≤ T, T' ≤ 0.12
DETAIL A

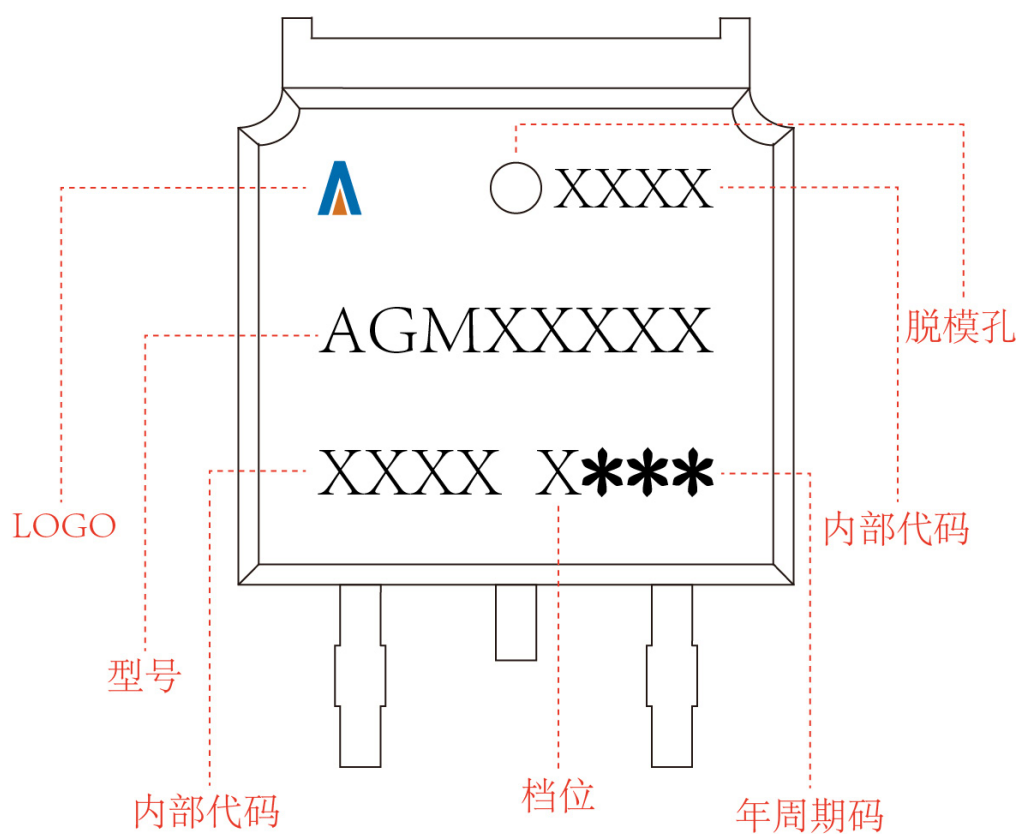
SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
c (电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	4.826 REF		
D3	3.166 REF		
E	6.000	6.100	6.200
e	2.286 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.888 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.600	0.800	1.000
Φ	1.100	1.200	1.300
θ	0°		8°
θ1	9° TYP		
θ2	9° TYP		

SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	7.050	7.100	7.150
A1	0.960	1.010	1.060
A2	2.250	2.300	2.350
A3	0.000	0.050	0.100
b	0.760REF.		
b1	1.000REF.		
c	0.508REF.		
c1	0.508REF.		
D	6.550	6.600	6.650
D1	5.220	5.320	5.420
E	0.950	1.000	1.050
E1	9.700	9.900	10.100
E2	6.050	6.100	6.150
e	2.286BSC		
e1	4.572REF.		
L	2.650	2.800	2.950
L1	0.700	0.800	0.900
θ1	7° REF.		
R	1.300REF.		
R1	0.250REF.		



TO-252

Marking Instructions:



Disclaimer:

The information provided in this document is believed to be accurate and reliable. However, Shenzhen Core Control Source Electronics Technology Co., Ltd. does not assume any responsibility for the following consequences. Do not consider the use of such information or use beyond its scope.

The information mentioned in this document may be changed at any time without notice.

The products and information provided in this document do not infringe patents. Shenzhen Core Control Source Electronics Technology Co., Ltd. assumes no responsibility for any infringement of any other rights of third parties. The result of using such products and information.

This document is the second version issued on April 10th, 2024. This document replaces all previously provided information.

 It is a registered trademark of Shenzhen Core Control Source Electronics Technology Co., Ltd.

Copyright © 2017 Shenzhen Core Control Source Electronics Technology Co., Ltd. all rights reserved.