

N-Channel 30V MOSFET

E030N5P2CL1

V_{DS} (V)	$R_{DS(on),max}$ (m Ω)	I_D (A)
30V	5.2 @ $V_{GS} = 10V$	90

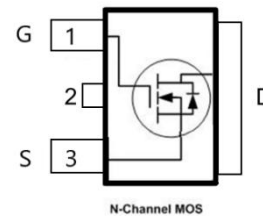
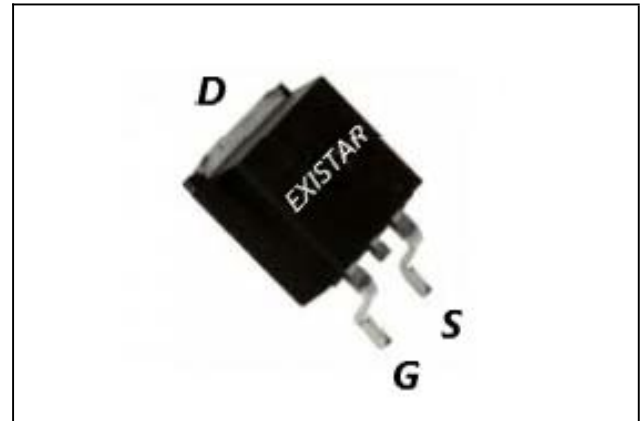
Features

- Low $R_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

Applications

- DC/DC conversion
- Power switch
- PD charger
- Moto driver

TO-252



Package And Ordering Information

Ordering code	Package	Marking
E030N5P2CL1	TO-252	E030N5P2CL1

Ordering Information

Package	Units/ Reel	Reels/ Inner Box	Units/ Inner Box
TO-252	2500	1	2500

Key Performance Parameters

Parameter	Value	Unit
V _{DS} , min @ T _j (max)	30	V
I _D , pulse	360	A
R _{DS(ON)} , max @ V _{GS} =10V	5.2	mΩ
Q _g	34	nC

Absolute Maximum Ratings at T_j=25°C Unless Otherwise Noted

Parameter		Symbol	Limit	Unit
Drain-source voltage		V _{DS}	30	V
Gate-source voltage		V _{GS}	±20	
Continuous drain current	T _C =25°C	I _D	90	A
	T _C =100°C		57	
Pulsed drain current		I _{D,pulse}	360	
Avalanche energy, single pulse		E _{AS}	169	mJ
Power dissipation	T _C =25°C	P _D	66	W
	T _C =100°C		26	
Operating junction and storage temperature range		T _J , T _{stg}	-55 To 175	°C

Thermal Characteristics

Parameter		Symbol	Max.	Unit
Thermal resistance, junction-to-case	Steady state	R _{θJC}	1.9	°C/W
Thermal resistance, junction-to-ambient	Steady state	R _{θJA}	-	

Electrical Characteristics at T_j=25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test conditions
Static						
Drain to source breakdown voltage	V _{(BR)DSS}	30			V	V _{GS} = 0, I _D = 250 μA
Gate-source threshold voltage	V _{GS(th)}	1		2.5	V	V _{DS} = V _{GS} , I _D = 250 μA
Gate-body leakage	I _{GSS}			±100	nA	V _{DS} = 0 V, V _{GS} = ±20 V
Zero gate voltage drain current	I _{DSS}			1	μA	V _{DS} = 30 V, V _{GS} = 0 V
Drain-source on-resistance	R _{DS(on)}		3.9	5.2	mΩ	V _{GS} = 10 V, I _D = 20 A
Drain-source on-resistance	R _{DS(on)}		6	8	mΩ	V _{GS} = 4.5 V, I _D = 15 A
Forward transconductance	g _{fs}		24		S	V _{DS} = 5 V, I _D = 20 A

Gate resistance	Rg		1.52		Ω	f=1MHz
Gate Charge						
Total gate charge	Qg		34		nC	VDS = 15 V, ID = 20 A, VGS = 10 V
Gate-source charge	Qgs		6.5			
Gate-drain charge	Qgd		7.5			
Dynamic						
Turn-on delay time	td(on)		7		ns	VDS = 15 V, VGS = 10 V, RL = 0.75 Ω, RGEN = 6 Ω
Rise time	tr		14			
Turn-off delay time	td(off)		34			
Fall time	tf		11			
Input capacitance	Ciss		1776		pF	VDS=15 V, VGS = 0 V, f = 1.0MHz
Output capacitance	Coss		183			
Reverse transfer capacitance	Crss		167			
Body Diode						
Diode forward voltage	VSD			1.2	V	VGS = 0 V, IF = 20 A
Reverse recovery time	trr		10		ns	Is =20 A, di/dt = 100 A/μs
Reverse recovery charge	Qrr		1.7		nC	

Electrical Characteristics Diagrams

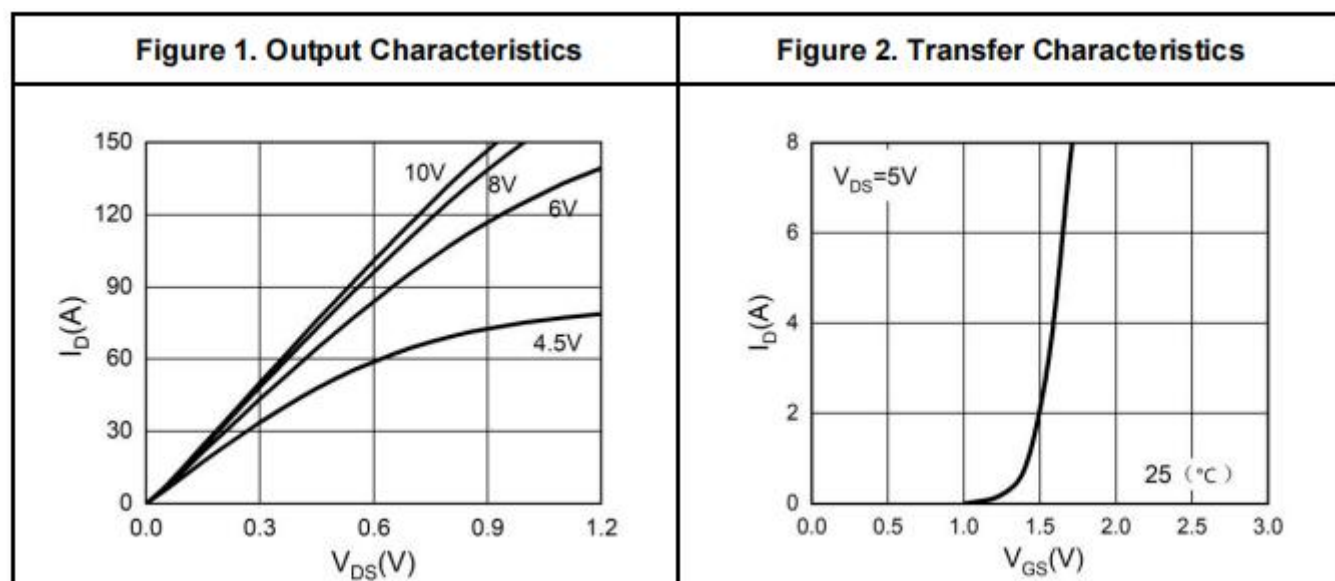


Figure 3. Power Dissipation

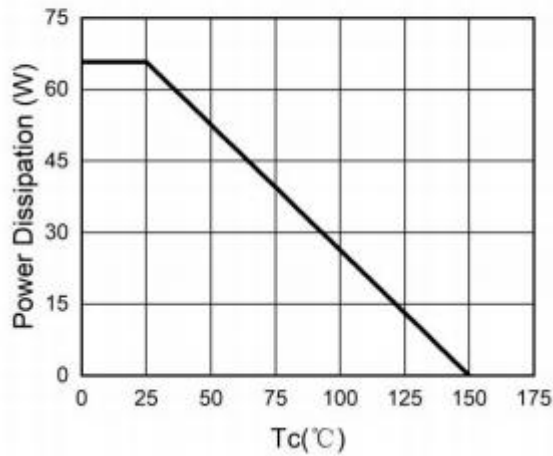


Figure 4. Drain Current

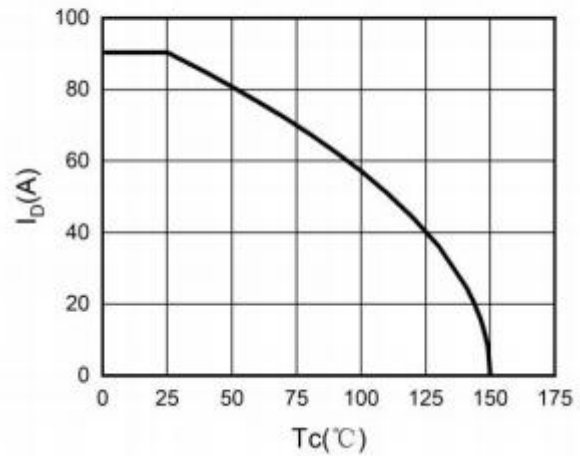


Figure 5. BV_{DS} vs Junction Temperature

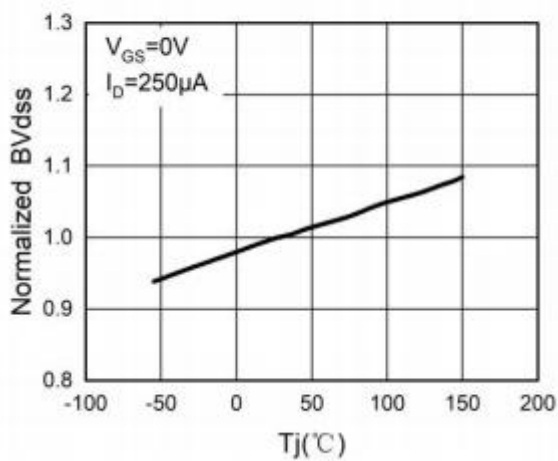


Figure 6. $R_{DS(ON)}$ vs Junction Temperature

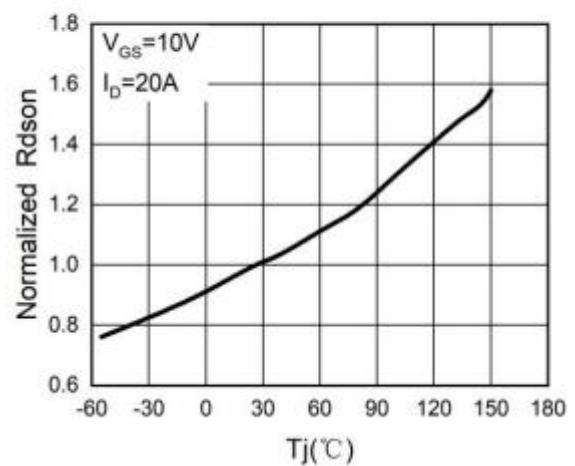
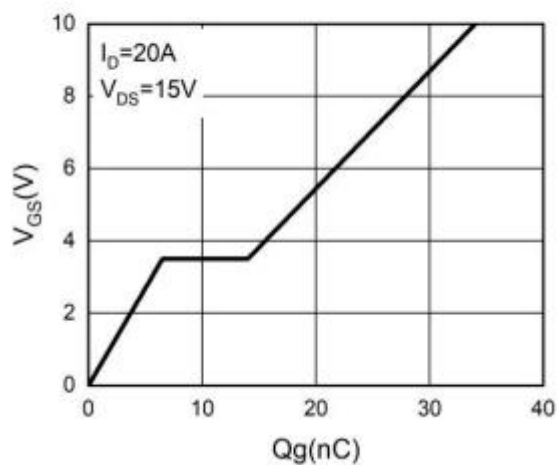
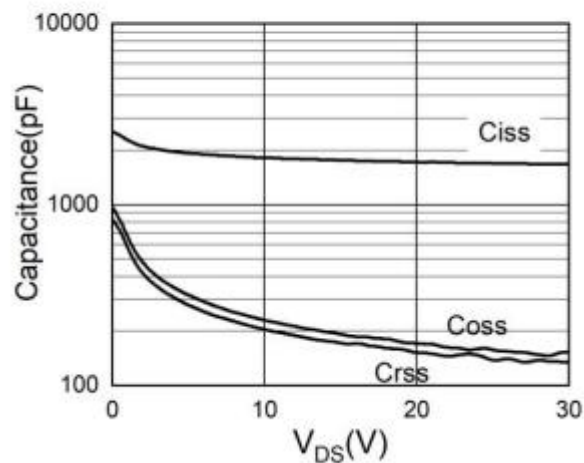
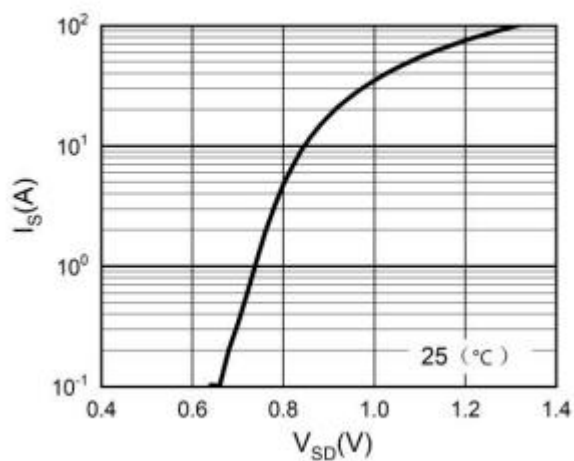
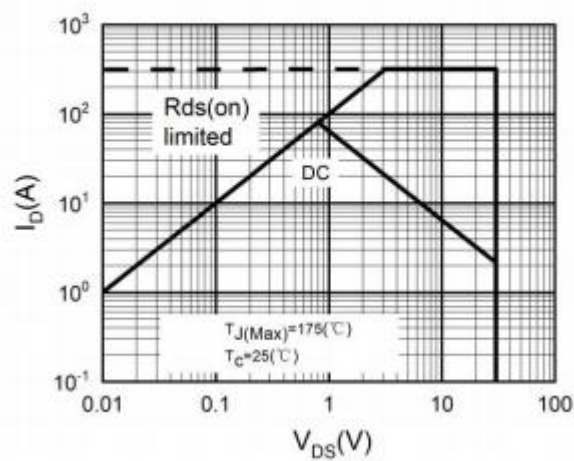
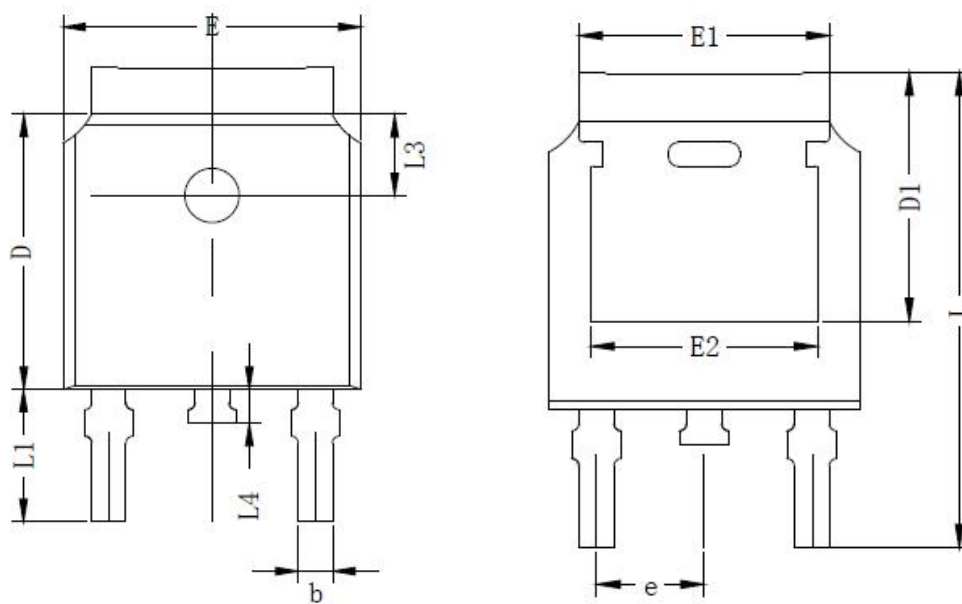


Figure 7. Gate Charge Waveforms

Figure 8. Capacitance

Figure 9. Body-Diode Characteristics

Figure 10. Maximum Safe Operating Area


Package Outline Dimensions



SYMBOL	MIN	NOM	MAX
A	2.10	2.30	2.50
A1	0.97	1.07	1.17
A2	0.00	—	0.12
b	0.66	0.76	0.86
c	0.45	0.51	0.60
D	5.90	6.10	6.30
D1	5.10	5.30	5.45
E	6.40	6.60	6.80
E1	5.10	5.33	5.45
E2	4.63	4.83	5.03
L	9.90	10.10	10.30
L1	2.74	2.94	3.14
L2	1.40	1.50	1.70
L3	1.65	1.80	1.95
L4	0.60	0.80	1.00
e	2.286BSC		
θ	5°	7°	10°
θ1	0°	—	3°

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