

General Description

The AGM4008LL combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

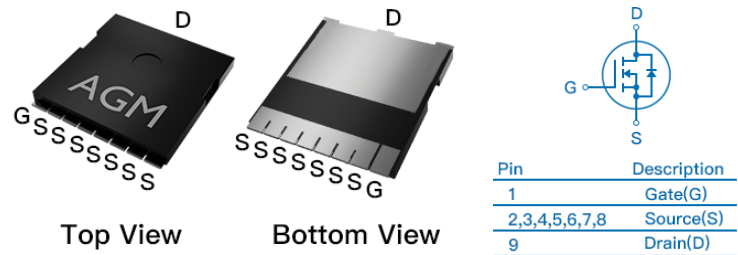
Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
40V	0.63mΩ	500A

TOLL Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM4008LL	AGM4008LL	TOLL	330mm	25mm	2000

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	40	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25°C) (Note 1)	500	A
	Drain Current-Continuous(Tc=100°C)	335	A
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 2)	2000	A
PD	Maximum Power Dissipation(Tc=25°C)	428	w
	Maximum Power Dissipation(Tc=100°C)	214	w
EAS	Avalanche energy (Note 3)	1600	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 175	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) ¹	---	60	°C/W
RθJC	Thermal Resistance Junction-Case ¹	---	0.35	°C/W

Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	40	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=40V,VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	1.2	--	2.4	V
gFS	Forward Transconductance	VDS=5V,ID=20A	--	68	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=50A	--	0.63	0.95	mΩ
		VGS=4.5V, ID=20A	--	0.88	1.5	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=20V,VGS=0V, F=1MHZ	--	8369	--	pF
Coss	Output Capacitance		--	5013	--	pF
Crss	Reverse Transfer Capacitance		--	896	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	2.8	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=10V,VDS=20V, ID=50A,RGEN=3Ω	--	24	--	nS
tr	Turn-on Rise Time		--	106.1	--	nS
td(off)	Turn-Off Delay Time		--	120	--	nS
tf	Turn-Off Fall Time		--	112	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=20V, ID=50A	--	221	--	nC
Qgs	Gate-Source Charge		--	45	--	nC
Qgd	Gate-Drain Charge		--	38	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	500	A
VSD	Forward on Voltage	VGS=0V,IS=50A	--	0.8	1.2	V
trr	Reverse Recovery Time	IF=50A , dI/dt=100A/μs , TJ=25℃	--	77	--	ns
Qrr	Reverse Recovery Charge		--	135	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C , VDD=20V,Vgs=10V,ID=80A,L=0.5mH,RG=25ohm

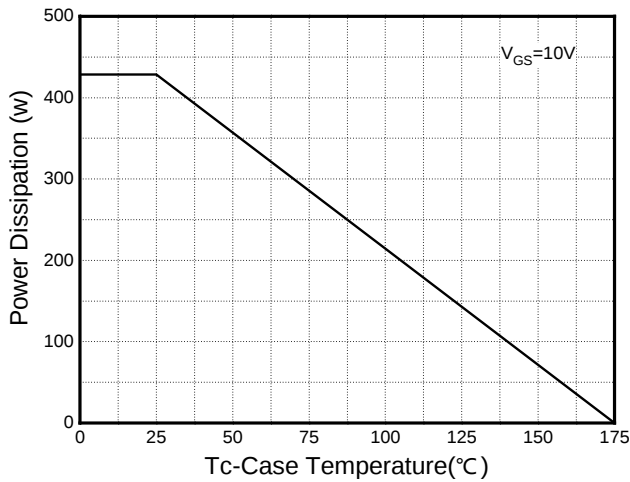


Figure 3: Safe Operation Area

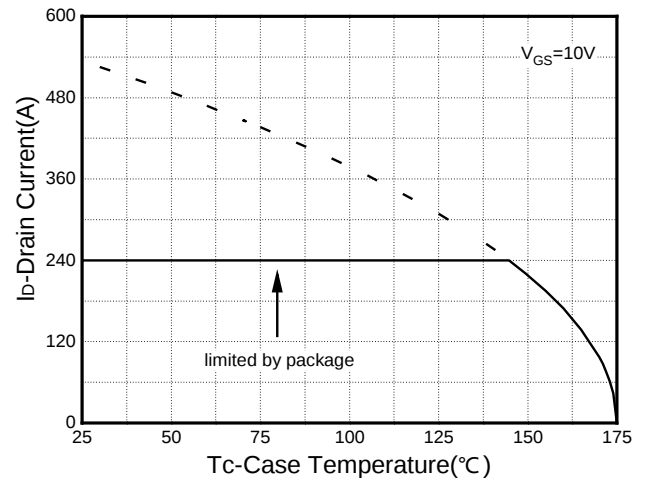


Figure 4: Thermal Transient Impedance

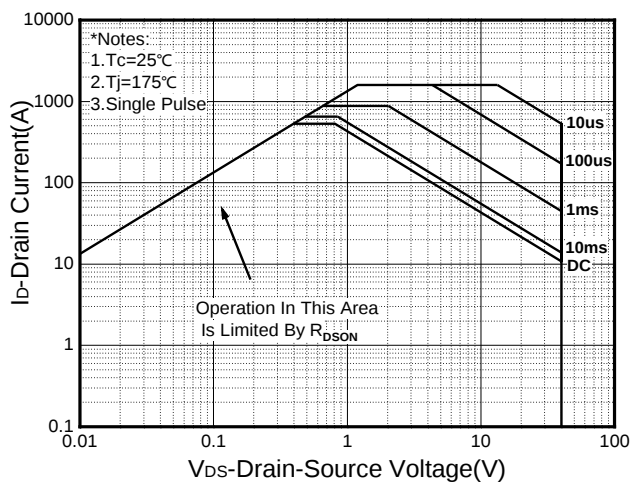


Figure 5: Output Characteristics

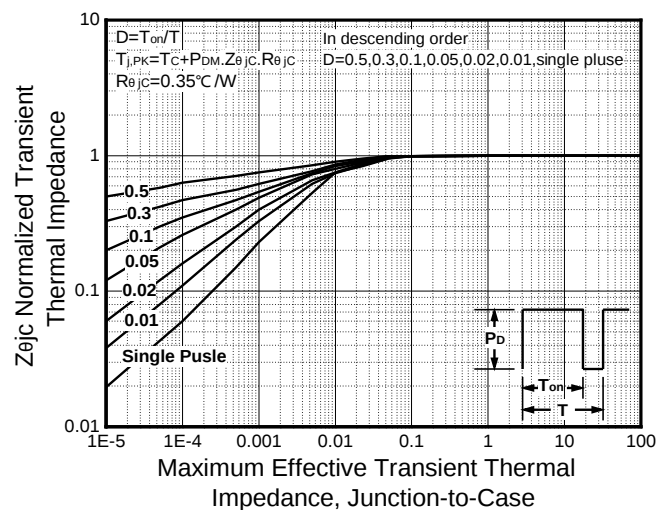
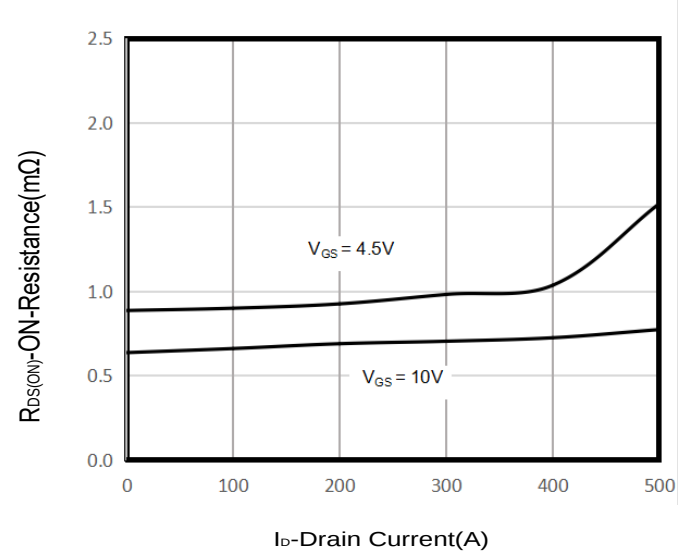
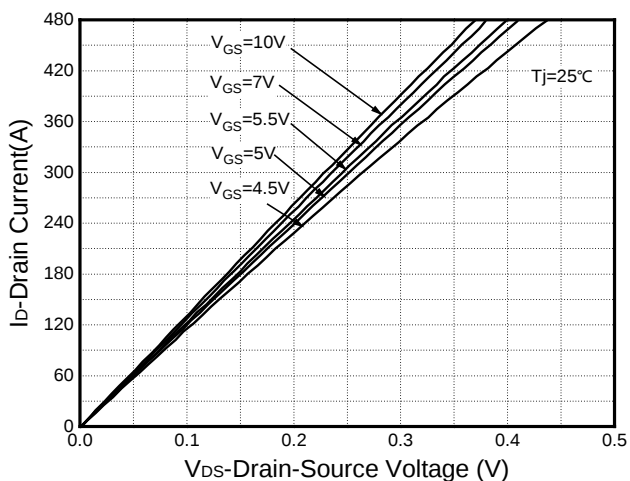


Figure 6: Drain-Source On Resistance



Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

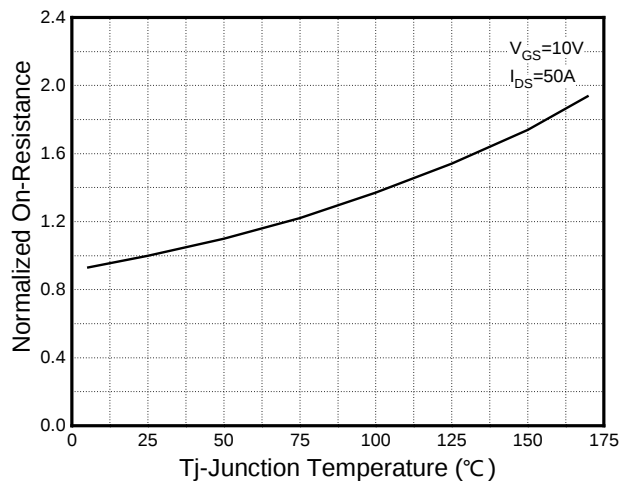


Figure 8: Source-Drain Diode Forward

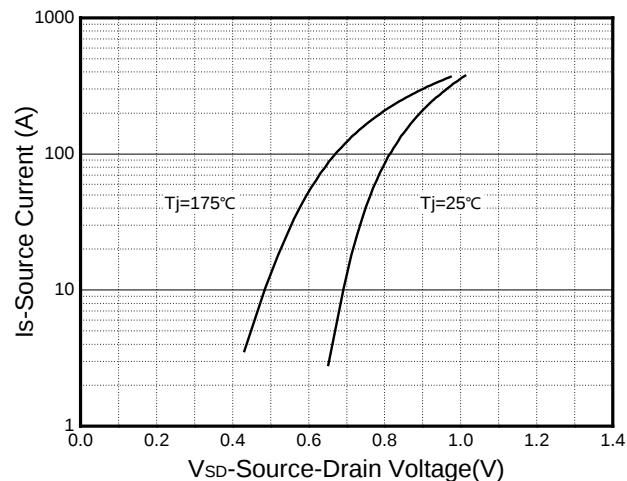


Figure 9: Capacitance Characteristics

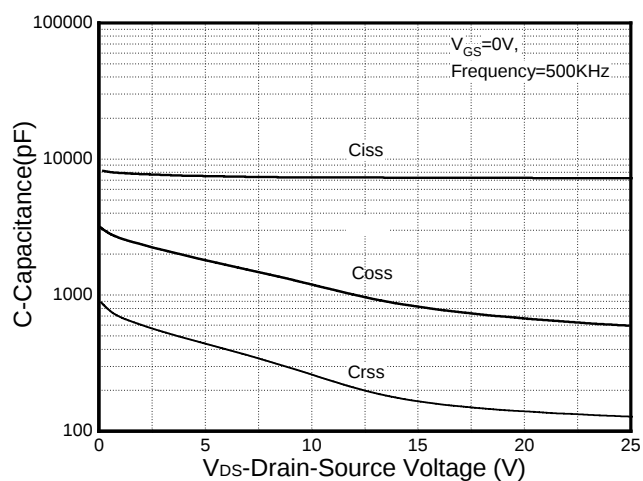
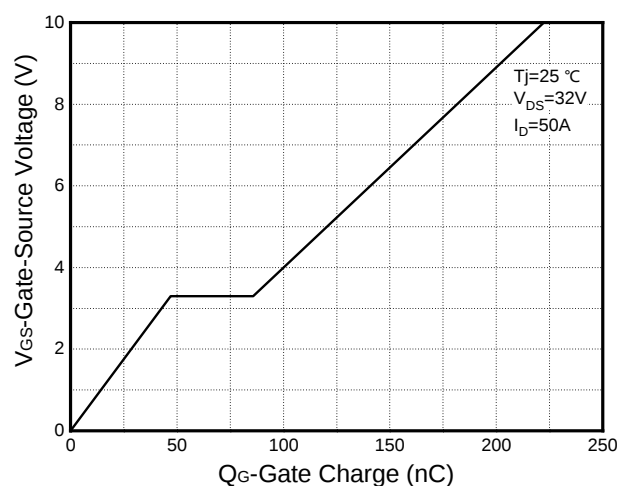
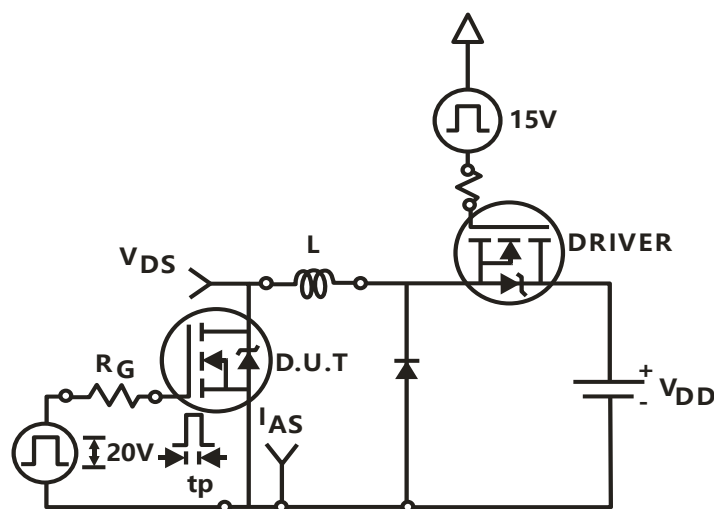


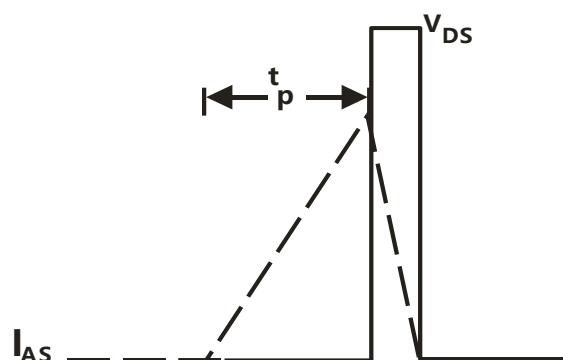
Figure 10: Gate Charge Characteristics



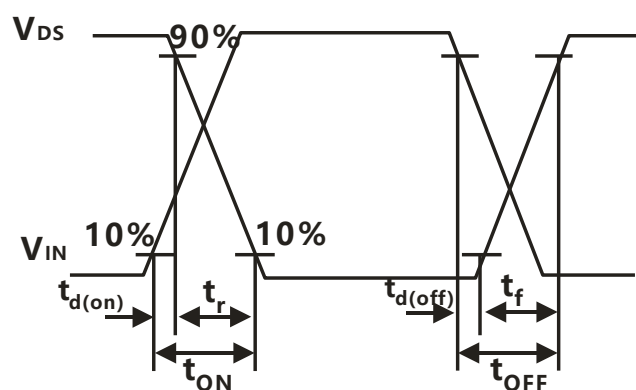
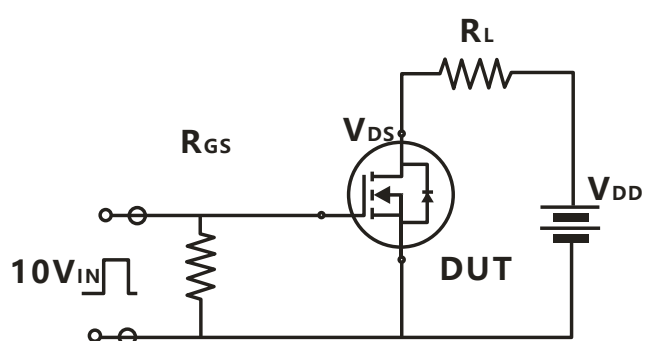
Avalanche Test Circuit



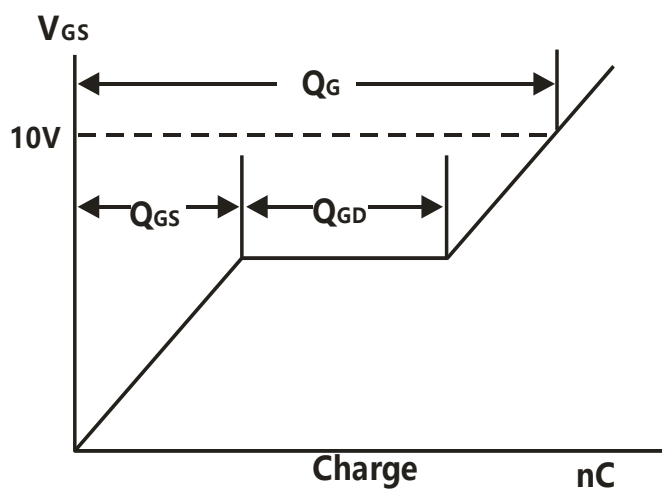
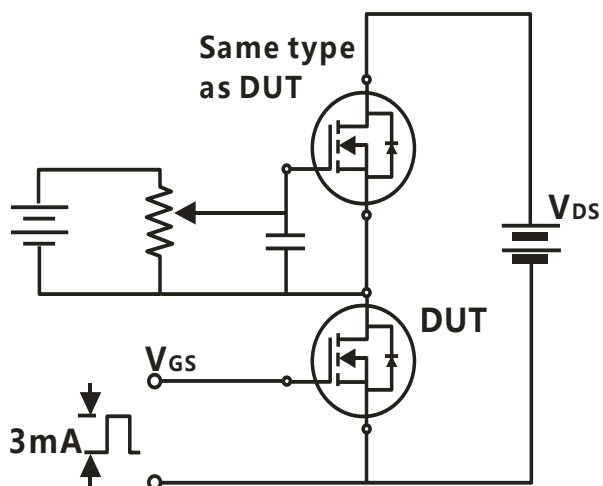
$$E_{AS} = \frac{1}{2} L I_{AS}^2$$



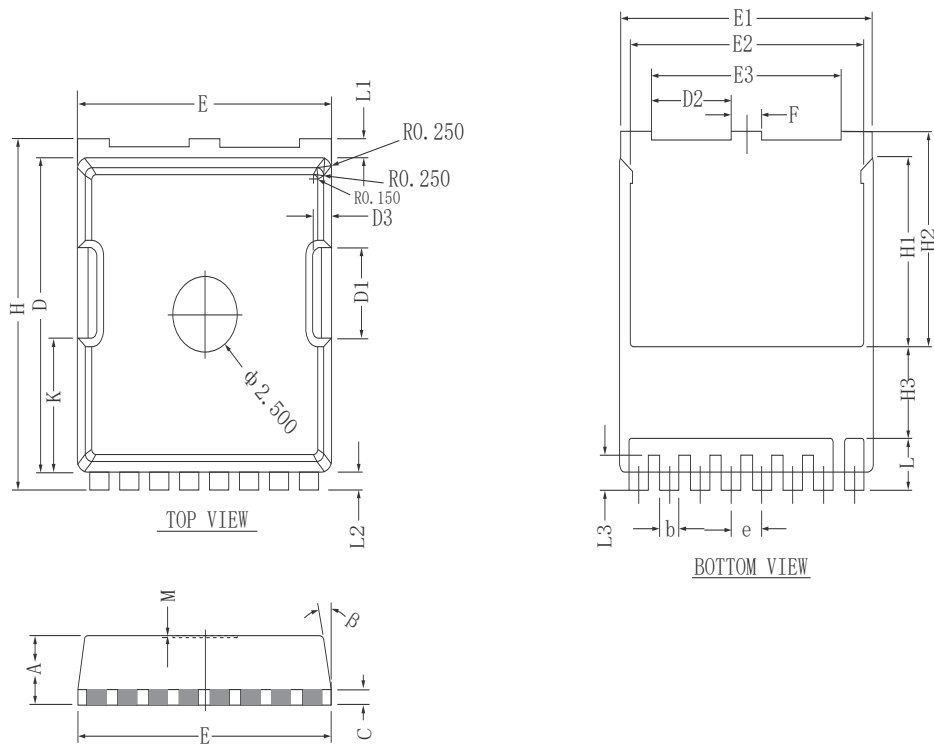
Switching Time Test Circuit



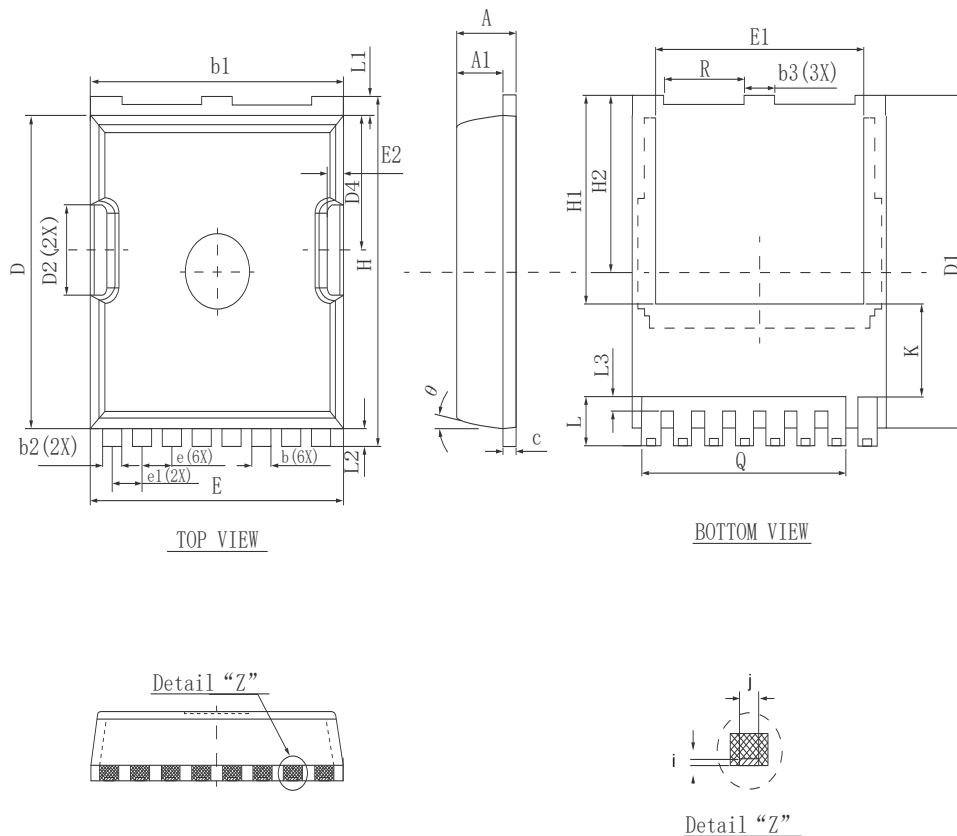
Gate Charge Test Circuit



Dimensions (TOLL)



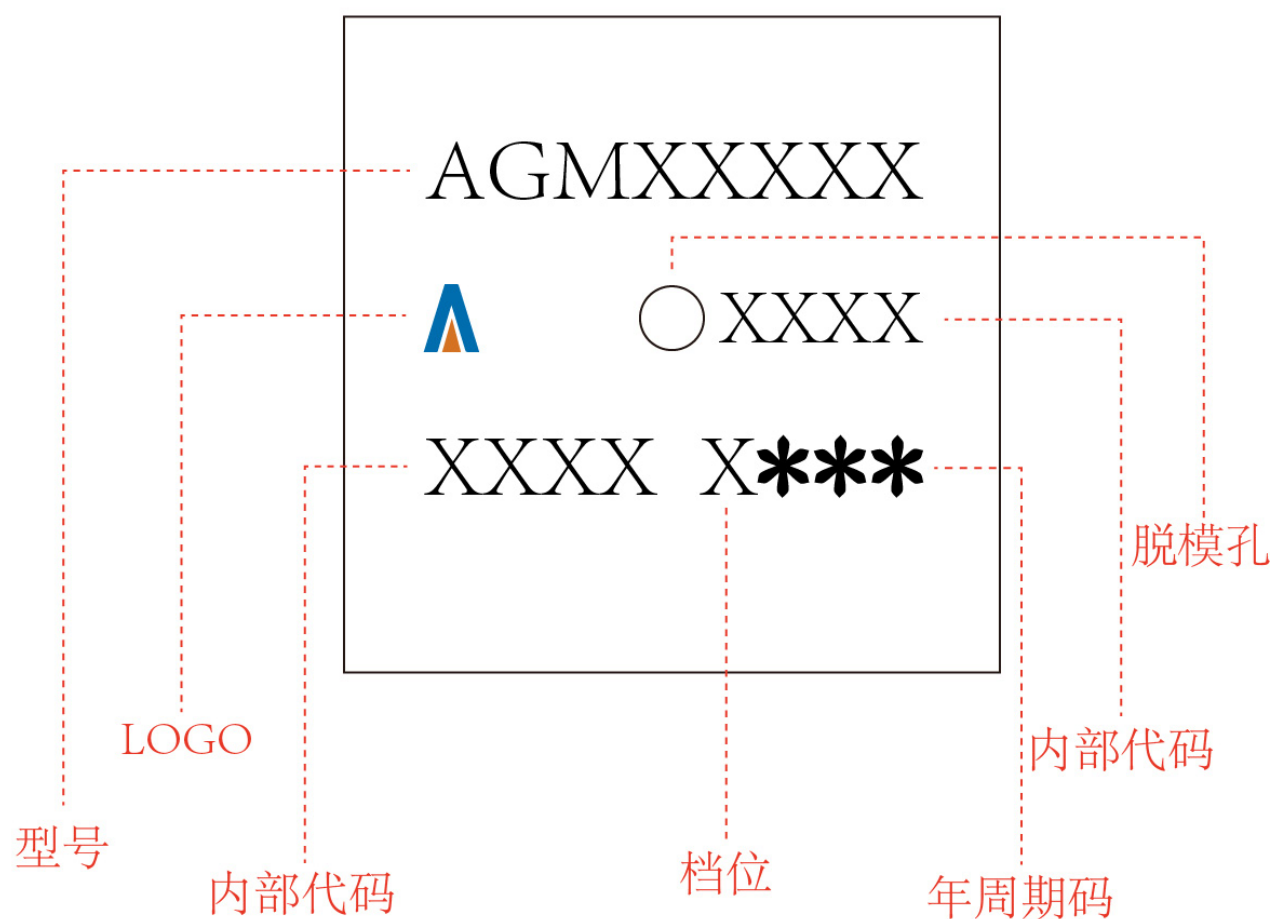
Symbols	Millimeters		
	MIN.	NOM.	MAX.
A	2.20	2.30	2.40
b	0.65	0.75	0.85
C	0.508 REF		
D	10.25	10.40	10.55
D1	2.85	3.00	3.15
D2	2.95	3.10	3.25
D3	0.75 REF		
E	9.75	9.90	10.05
E1	9.65	9.80	9.95
E2	8.95	9.10	9.25
E3	7.25	7.40	7.55
e	1.20 BSC		
F	1.05	1.20	1.35
H	11.55	11.70	11.85
H1	6.03	6.18	6.33
H2	6.85	7.00	7.15
H3	3.00 BSC		
L	1.55	1.70	1.85
L1	0.55	0.70	0.85
L2	0.45	0.60	0.75
L3	1.00	1.15	1.30
M	0.08 REF		
β	8°	10°	12°
K	4.25	4.40	4.55



SYMBOL	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.200	2.300	2.400
A1	1.700	1.800	1.900
b	0.600	0.700	0.800
b1	9.700	9.800	9.900
b2	0.650	0.750	0.850
b3	1.100	1.200	1.300
c	0.400	0.500	0.600
D	10.300	10.400	10.500
D1	11.000	11.100	11.200
D2	3.200	3.300	3.400
D4	4.470	4.570	4.670
E	9.800	9.900	10.000
E1	8.000	8.100	8.200
E2	0.500	0.600	0.700
e	1.200 BSC		
e1	1.225 BSC		
H	11.600	11.700	11.800
H1	6.950 BSC		
H2	5.900 BSC		
i	0.100 REF.		
j	0.350 REF.		
K	3.100 REF.		
L	1.550	1.650	1.750
L1	0.600	0.700	0.800
L2	0.500	0.600	0.700
L3	0.400	0.500	0.600
Q	7.950 REF.		
R	3.000	3.100	3.200
θ	10° REF.		

TOLL

Marking Instructions:



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