

• General Description

The AGM60P85E combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

• Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

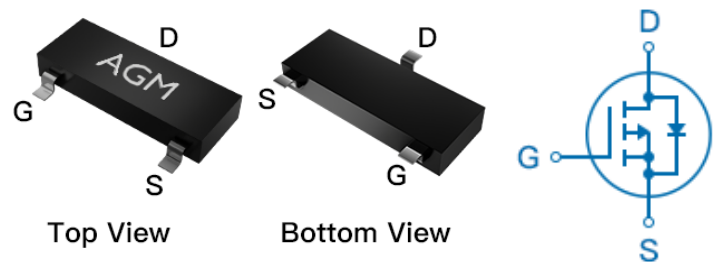
• Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
-60V	80mΩ	-3A

SOT23-3 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
60P85	AGM60P85E	SOT23-3	178mm	8mm	3000

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	-60	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(TA=25°C) (Note 1)	-3.0	A
	Drain Current-Continuous(TA=100°C)	-1.8	A
IDM (pluse)	Drain Current-Pulsed (Note 2)	-12	A
PD	Maximum Power Dissipation(TA=25°C)	1.25	w
	Maximum Power Dissipation(TA=100°C)	0.5	w
EAS	Avalanche energy (Note 3)	10	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) ¹	---	100	°C/W

Table 2. P-Channel Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=-250μA	-60	-72	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=-60V,VGS=0V	--	--	-1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=-250μA	-1.2	-1.6	-2.2	V
gFS	Forward Transconductance	VDS=-5V,ID=-2A	--	10	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=-10V, ID=-3A	--	80	97	mΩ
		VGS=-4.5V, ID=-2A	--	97	126	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=-30V,VGS=0V, F=1MHZ	--	1450	--	pF
Coss	Output Capacitance		--	48	--	pF
Crss	Reverse Transfer Capacitance		--	35	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	--	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=-10V,VDS=-30V, RL=3Ω,RGEN=3Ω	--	9.7	--	nS
tr	Turn-on Rise Time		--	5.5	--	nS
td(off)	Turn-Off Delay Time		--	29	--	nS
tf	Turn-Off Fall Time		--	6	--	nS
Qg	Total Gate Charge	VGS=-10V, VDS=-30V, ID=-10A	--	23.7	--	nC
Qgs	Gate-Source Charge		--	2.1	--	nC
Qgd	Gate-Drain Charge		--	7.2	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	-3.0	A
VSD	Forward on Voltage	VGS=0V,IS=-3A	--	--	-1.2	V
trr	Reverse Recovery Time	IF=-3A , dl/dt=100A/μs ,	--	34	--	ns
Qrr	Reverse Recovery Charge	TJ=25℃	--	37	--	nc

Notes 1.The maximum current rating is package limited.

Notes2.Repetitive Rating: Pulse width limited by maximum junction temperature Notes

3.EAS condition: T_J=25°C

Figure 1. Output Characteristics

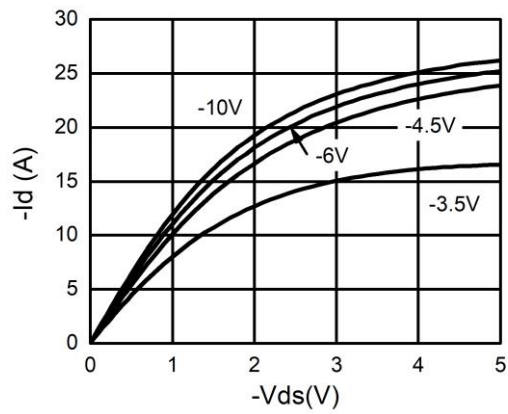


Figure 2. Transfer Characteristics

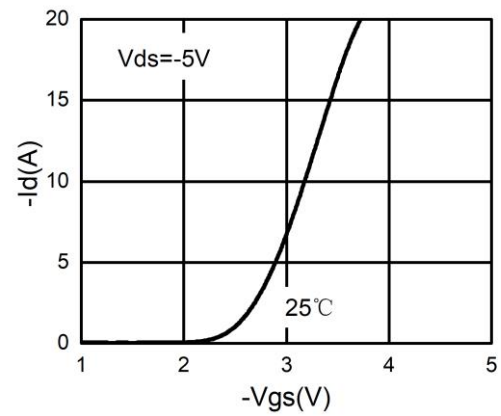


Figure 3. Power Dissipation

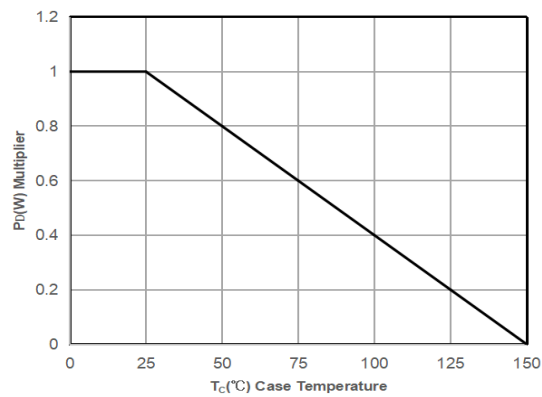


Figure 4. Drain Current

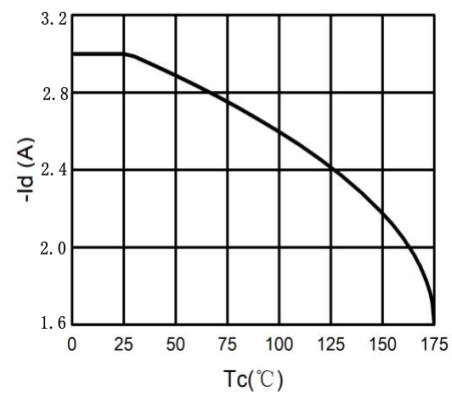
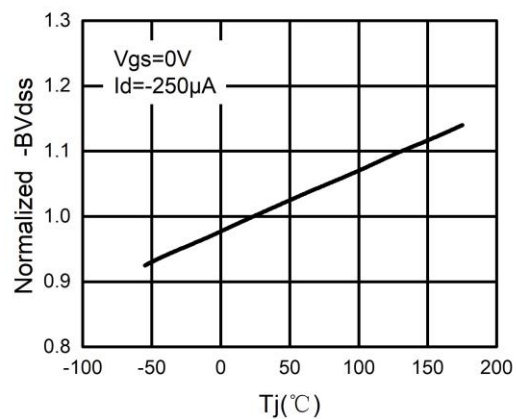
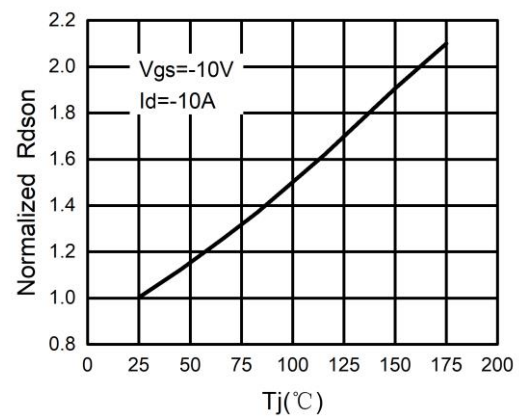

Figure 5. BV_{DSS} vs Junction Temperature

Figure 6. $R_{DS(ON)}$ vs Junction Temperature


Figure 7. Gate Charge Waveforms

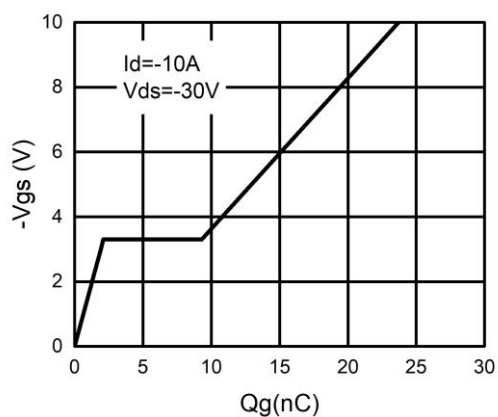


Figure 8. Capacitance

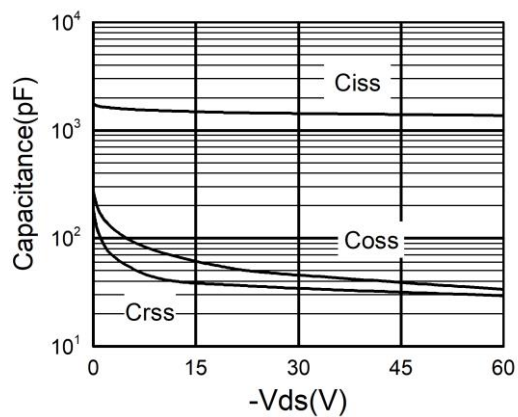


Figure 9. Body-Diode Characteristics

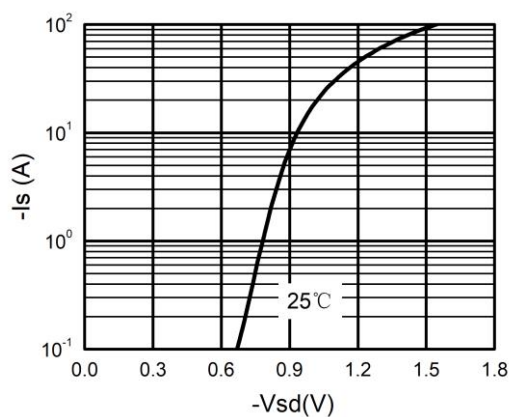
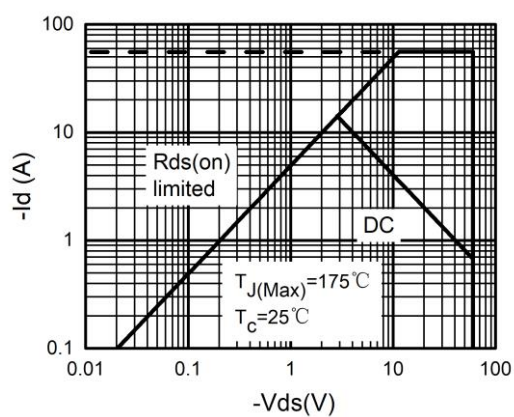
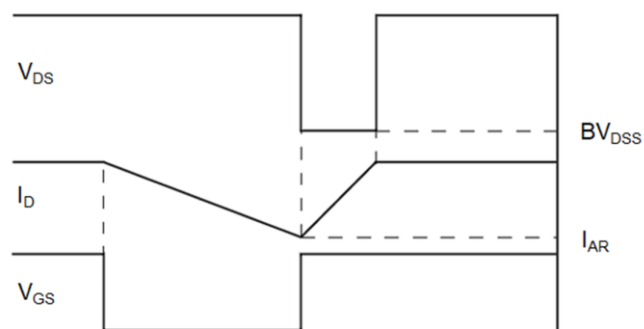
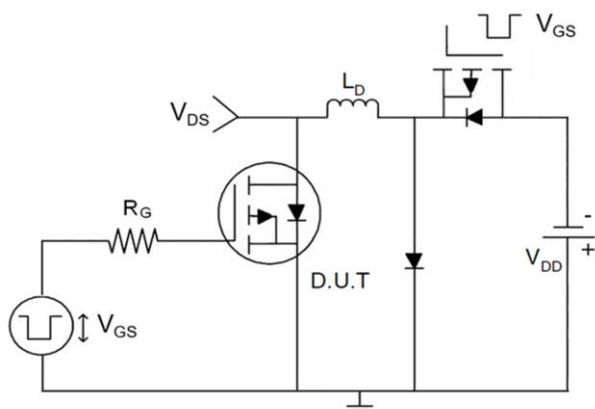


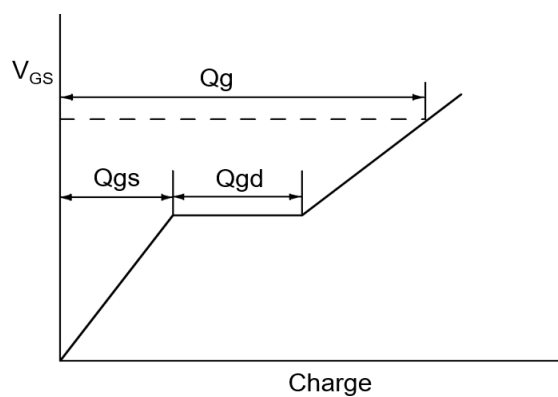
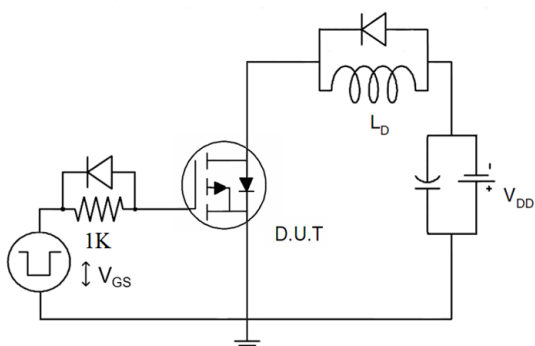
Figure 10. Maximum Safe Operating Area



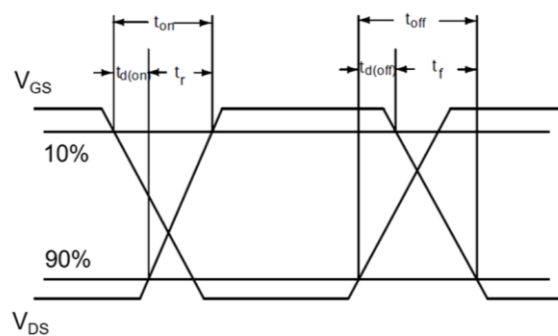
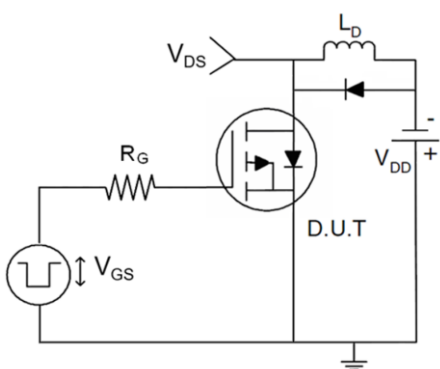
1) E_{AS} Test Circuits



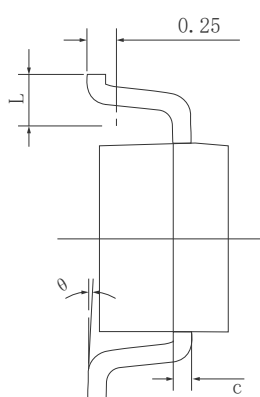
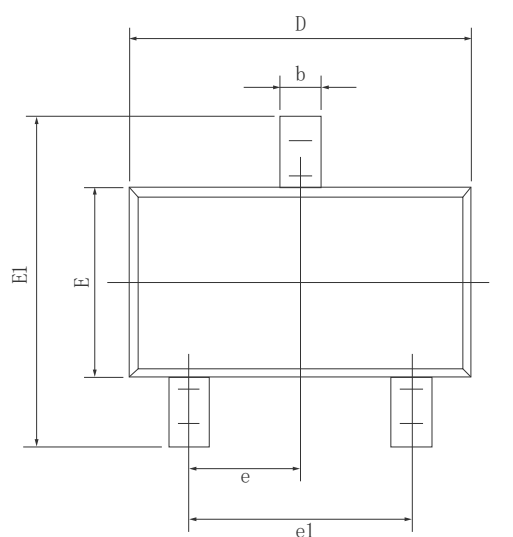
2) Gate Charge Test Circuit



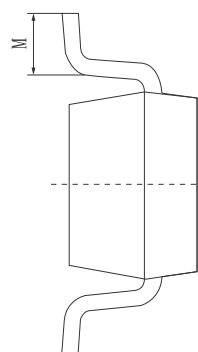
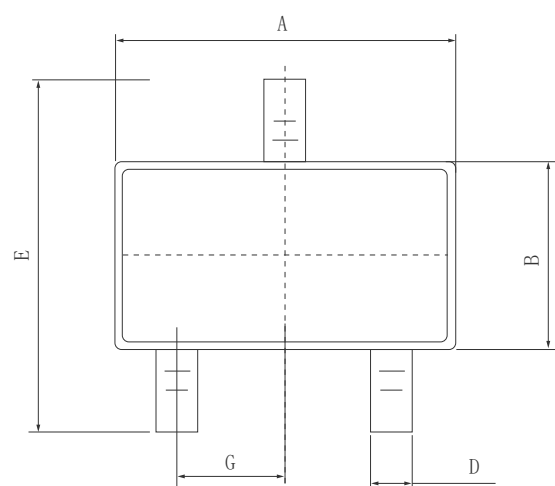
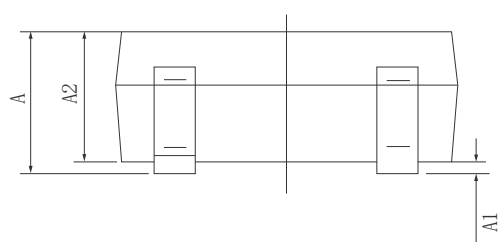
3) Switch Time Test Circuit



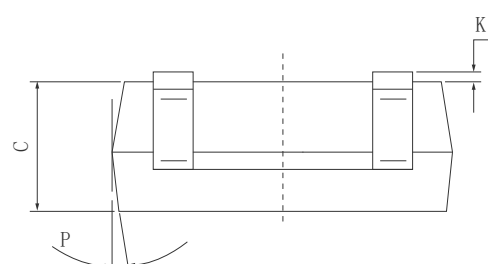
Dimensions (SOT23-3)



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	—	—	1.25
A1	0.03	—	0.10
A2	1.05	1.10	1.15
b	0.30	0.35	0.40
c	0.13	—	0.17
D	2.87	2.92	2.97
E	1.55	1.60	1.65
E1	2.70	2.85	3.00
e	0.95 BSC.		
e1	1.80	—	2.00
L	0.35	0.45	0.55
θ	0°	—	8°

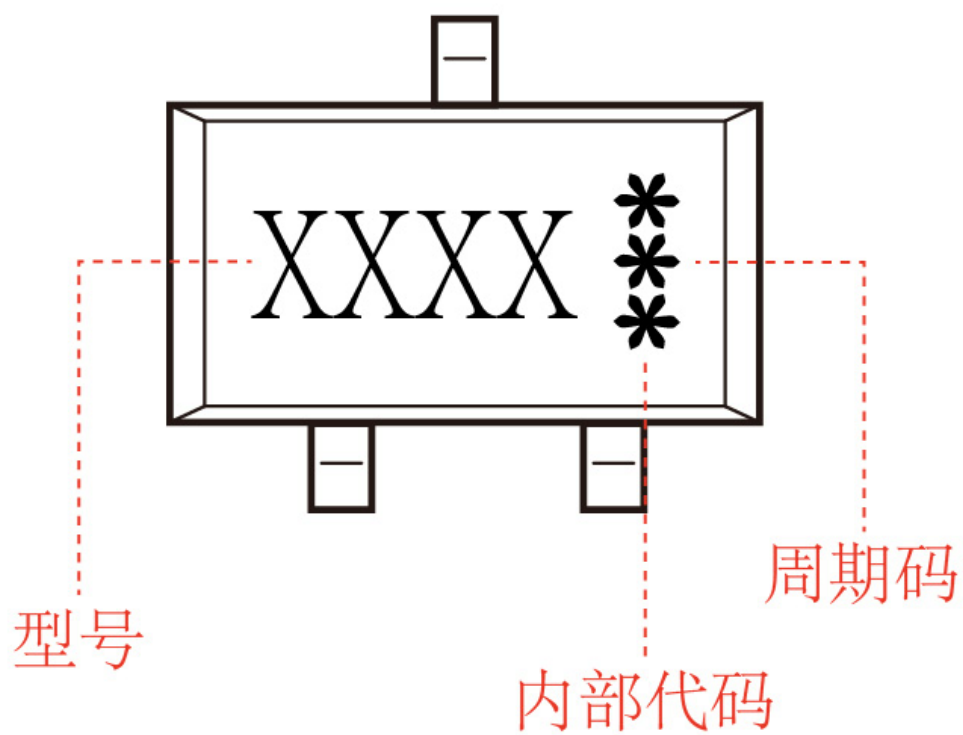


DIM	MILLIMETERS
A	2.82~3.02
B	1.60±0.10
C	1.10±0.05
D	0.40±0.10
E	2.65~2.95
G	0.95typ
K	0.00~0.10
M	0.20MIN
P	9±2°



SOT23-3

Marking Instructions:



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