

### RHBS602 THRU RHBS610

#### SINGLE PHASE GLASS PASSIVATED BRIDGE RECTIFIERS

## **Features**

- ◆ Surface mount bridge, small package;
- ♦ Ideal for printed circuit boards;
- Glass passivated chip junction;
- ◆ High forward current capability up to 6.0A;
- ♦ High surge current capability;
- High heat dissipation capability;
- ◆ Low profile package;
- Low forward voltage drop;
- ◆ Plastic package has Underwrites Laboratory Flammability Classification 94V-0;

# **Mechanical Data**

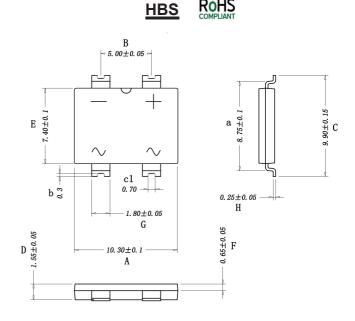
Case: JEDEC HBS Molded plastic body

Mounting Position: Any

High temperature soldering guaranteed: Solder Reflow

260 ℃,10seconds

Polarity: As marked on body



Dimensions in inches and (millimeters)

# **Maximum Ratings And Electrical Characteristics**

Ratings at 25°C ambient temperature unless otherwisespecified.

Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

| Parameter   | SYMBOLS             | MDD<br>RHBS602 | MDD<br>RHBS604 | MDD<br>RHBS606 | MDD<br>RHBS608 | MDD<br>RHBS610 | UNITS |
|---|---------------------|----------------|----------------|----------------|----------------|----------------|-------|
| Marking Code  |                     |                |                |                |                |                |       |
| Maximum repetitive peak reverse voltage   | Vrrm                | 200            | 400            | 600            | 800            | 1000           | V     |
| Maximum RMS voltage   | VRMS                | 140            | 280            | 420            | 560            | 700            | V     |
| Maximum DC blocking voltage   | VDC                 | 200            | 400            | 600            | 800            | 1000           | V     |
| Maximum average forward rectified current at T <sub>A</sub> =25°C                                       | I <sub>F(AV)</sub>  | 6.0            |                |                |                |                | А     |
| Peak forward surge current,<br>8.3ms single half sine-wave superimposed on<br>rated load (JEDEC Method) | I <sub>FSM</sub>    | 170            |                |                |                |                | А     |
| Maximum instantaneous forward voltage drop per diode at 6A  | V <sub>F</sub>      | 1.3            |                |                |                |                | V     |
| Maximum DC reverse current Ta=25°C at rated DC blocking voltage Ta=125°C                                | I <sub>R</sub>      | 5<br>100       |                |                |                |                | μΑ    |
| Maxumum reverse recovery time(1)  | Trr                 | 1              | 50             | 250            | 50             | 00             | ns    |
|   | R <sub>eJA</sub>    | 72             |                |                |                |                |       |
| Typical thermal resistance  | R <sub>eJC</sub>    | 14             |                |                |                |                | °C/W  |
|   | R <sub>eJL</sub>    | 12             |                |                |                |                |       |
| Operating junction and Storage Temperature Range  | $T_{j}$ , $T_{STG}$ | -55 to +150    |                |                |                |                | °C    |

Note1: IF=0.5A,IR=1.0A,Irr=0.25A

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# **Ratings And Characteristic Curves**

(TA = 25<sup>°</sup>C unless otherwise noted)

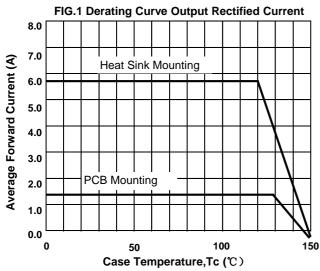
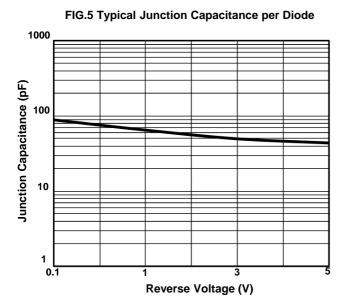
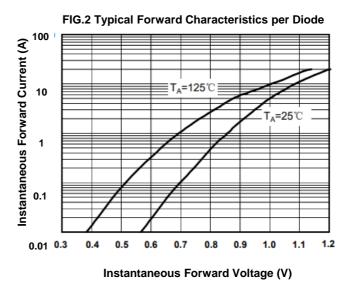


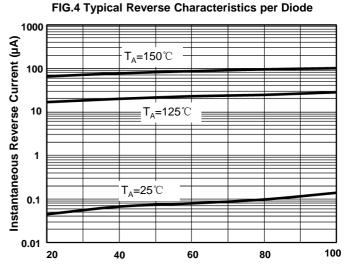
FIG.3 Maximum Non-Repetitive Peak Forward Surge
Current per Diode

170
8.3ms single half
sine-wave

1 10
Number of Cycles at 60Hz







Percent of Rated Peak Reverse Voltage (%)



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