

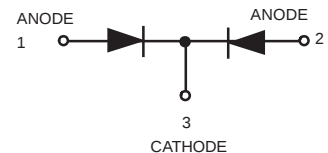
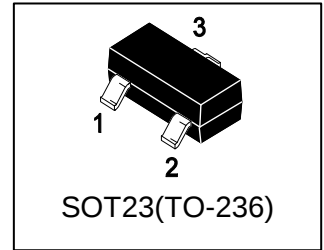
LDAN202KLT1G

S-LDAN202KLT1G

Dual Serise Switching Diodes

1. FEATURES

- Ultra high speed switching
- Suitable for high packing density layout.
- High reliability.
- We declare that the material of product compliance with RoHS requirements and Halogen Free.
- S- prefix for automotive and other applications requiring unique site and control change requirements; AEC-Q101 qualified and PPAP capable.



2. DEVICE MARKING AND RESISTOR VALUES

Device	Marking	Shipping
LDAN202KLT1G	N	3000/Tape&Reel
LDAN202KLT3G	N	10000/Tape&Reel

3. MAXIMUM RATINGS(Ta = 25°C)

Parameter	Symbol	Limits	Unit
Reverse Voltage	VR	80	V
Forward Current	IO	100	mA
Peak Forward Surge Current	IFSM	300	mA

4. THERMAL CHARACTERISTICS

Parameter	Symbol	Limits	Unit
Total Device Dissipation FR- 5 Board, (Note 1) TA = 25°C Derate above 25°C	PD	225 1.8	mW mW/°C
Thermal resistance from junction to ambient	RθJA	556	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) TA = 25°C Derate above 25°C	PD	300 2.4	mW mW/°C
Thermal resistance from junction to ambient	RθJA	417	°C/W
Junction and Storage Temperature	TJ , Tstg	-55~+150	°C

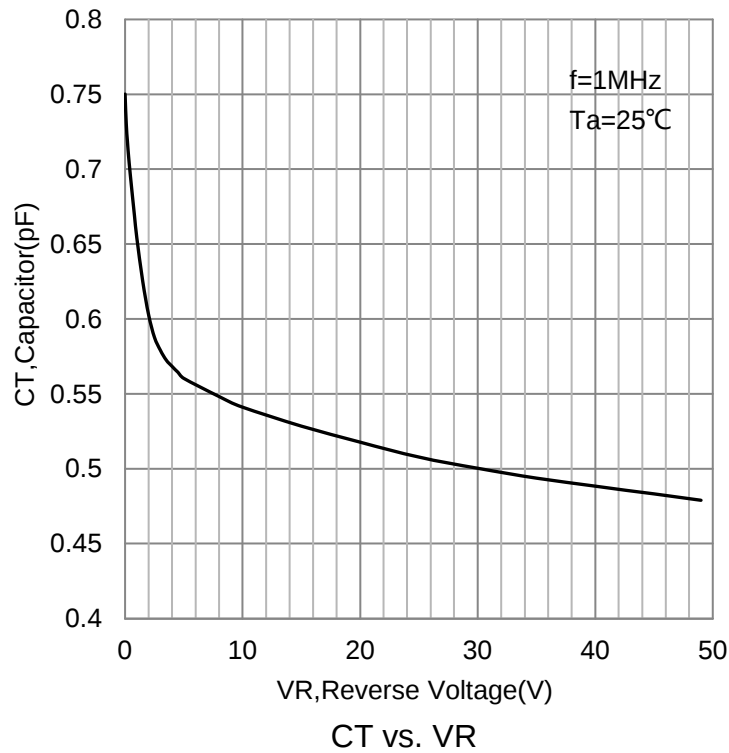
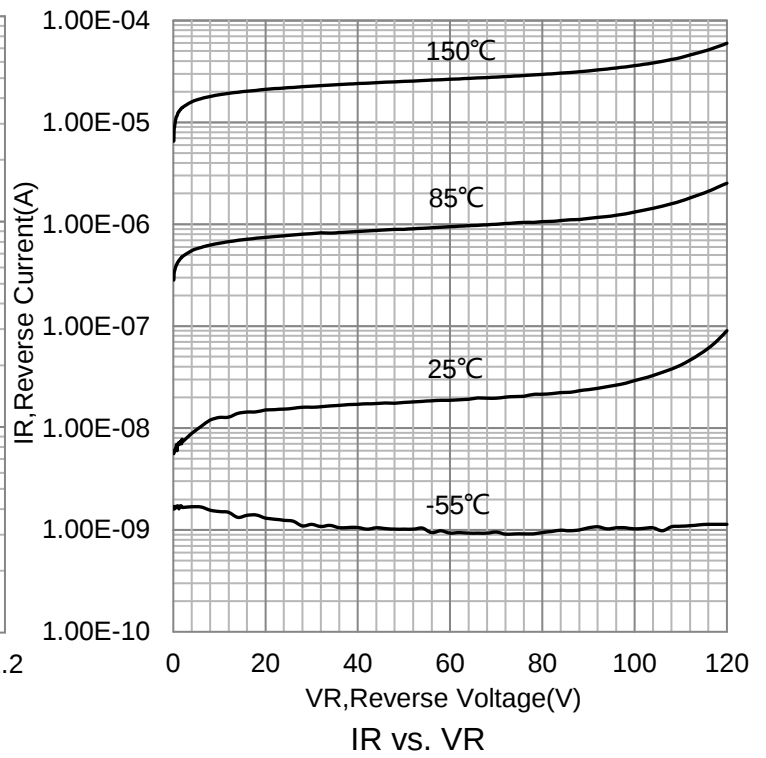
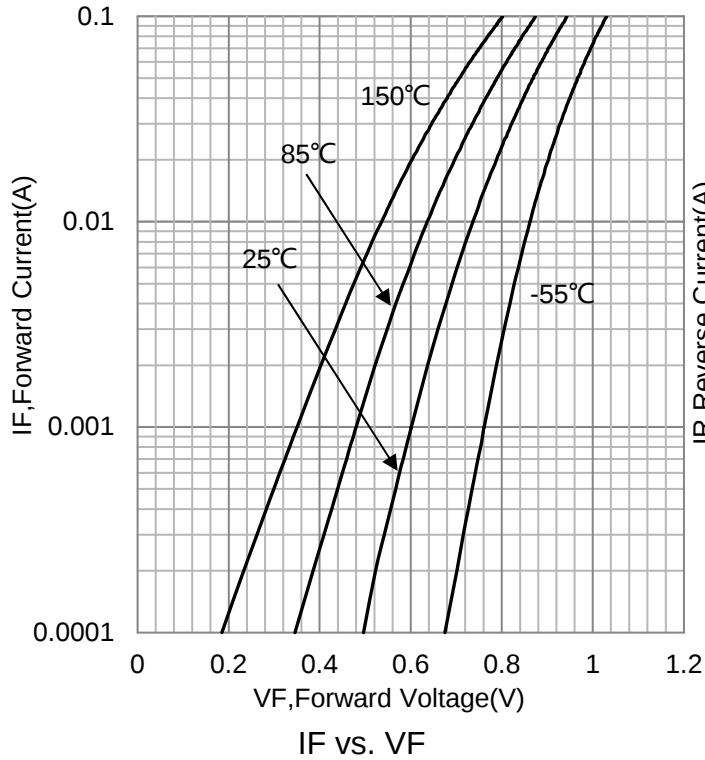
1. FR-5 = 1.0 x 0.75 x 0.062 in.

2. Alumina = 0.4 x 0.3 x 0.024 in. 99.5% alumina.

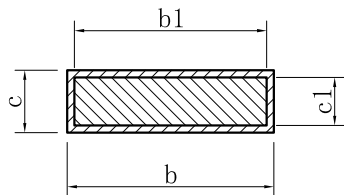
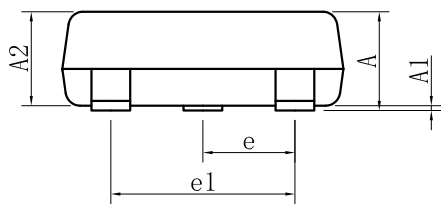
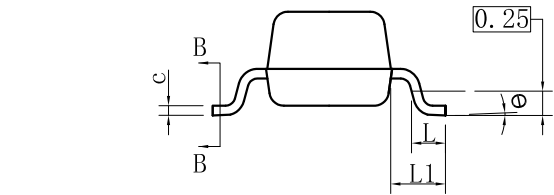
5. ELECTRICAL CHARACTERISTICS (Ta= 25°C)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Forward voltage (IF = 100mA)	VF	-	-	1.2	V
Reverse Current (VR = 70V)	IR	-	-	0.1	μA
Capacitance between terminals (f =1MHz)	CT	-	-	3.5	pF
Reverse recovery time (VR= 6V,IF=5 mA)	Trr	-	-	4	ns

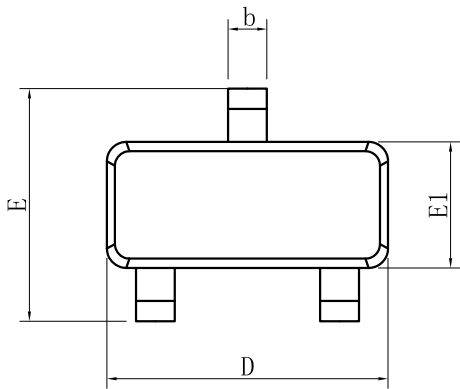
6.ELECTRICAL CHARACTERISTICS CURVES



7.OUTLINE AND DIMENSIONS



SECTION B-B

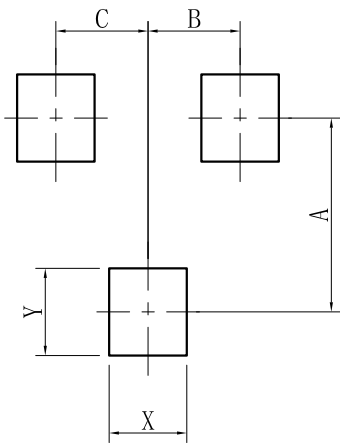


SOT23			
DIM	MIN	NOR	MAX
A	0.89	-	1.12
A1	0.01	-	0.10
A2	0.88	0.95	1.02
b	0.30	-	0.50
b1	0.30	0.40	0.45
c	0.08	-	0.20
c1	0.08	0.10	0.16
D	2.80	2.90	3.04
E	2.10	-	2.64
E1	1.20	1.30	1.40
e	0.95BSC		
e1	1.90BSC		
L	0.40	0.46	0.60
L1	0.54REF		
θ	0°	-	8°
All Dimensions in mm			

GENERAL NOTES

- 1.Top package surface finish Ra0.4±0.2um
- 2.Bottom package surface finish Ra0.7±0.2um
- 3.Side package surface finish Ra0.4±0.2um

8.SOLDERING FOOTPRINT



SOT-23	
DIM	(mm)
X	0.80
Y	0.90
A	2.00
B	0.95
C	0.95

DISCLAIMER

- Curve guarantee in the specification. The curve of test items with electric parameter is used as quality guarantee. The curve of test items without electric parameter is used as reference only.
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