Features

- The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- Idea for printed circuit board
- Open Junction chip
- Low reverse leakage
- High forward surge current capability
- High temperature soldering guaranteed 250°C/10 seconds at terminals

0.20±0.01 0.10±0.05 0.10±0.05 0.10±0.05 0.10±0.05 0.10±0.05 0.10±0.05 0.10±0.05 0.10±0.05 0.10±0.05

DO-214AA/SMB

Mechanical Data

Case: JEDEC DO-214AA/SMB molded plastic body Terminals: Solderable per MIL-STD-750,Method 2026 Polarity: Polarity symbol marking on body Mounting

Position: Any

Weight: 0.005ounce, 0.138grams

4. 50 ± 0. 05 4. 50 ± 0. 05 90 ° 0 + 0. 05 90 ° 0 + 0. 05

5.25±0.10

Dimiensions in inches and (milimeters)

Maximum Ratings And Elect Characteristics

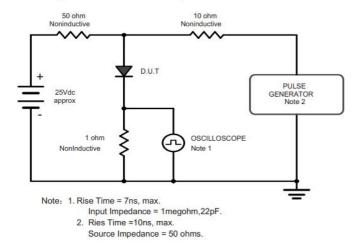
Ratings at 25 C ambient temperature unless otherwise specified. Single phase half-wave 60Hz,resistive or inductive load, for capacitive load current derate by 20%.

Type Number	SYMBOL	ES3JB	unit
Maximum Recurrent Peak Reverse Voltage	$V_{ m RRM}$	600	V
Maximum RMS Voltage	$V_{ m RMS}$	420	V
Maximum DC blocking Voltage	$V_{ m DC}$	600	V
Maximum Average Forward Rectified Current .at TA =55°C	$I_{ m F(AV)}$	3	A
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC method)	I _{FSM}	90.0	A
Maximum Forward Voltage at 1.5A DC	$V_{ m F}$	1.7	V
Maximum DC Reverse Current @T _A =25°C	7	5.0	μΑ
At rated DC blocking voltage @T _A =100°C	I _R	100.0	μΑ
Typical Junction Capacitance (Note1)	Cj	40	pF
Maximum reverserecovery tme (Note2)	trr	35	ns
Typical Thermal Resistance (Note 3)	$R_{ m (JA)}$	40/16	°C /W
Storage Temperature	T _{STG}	-55 to +150	°C
Operation Junction Temperature	$T_{ m J}$	-55 to +150	°C

Note: 1. Pulse Test with PW=300µsec,2% Duty Cycle.

2. Mounted on P.C.Board with 5.0mm2(.013mm thick)copper pad areas.

Fig.1 Reverse Recovery Time Characteristic And Test Circuit Diagram





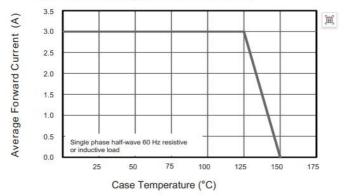


Fig.4 Typical Forward Characteristics

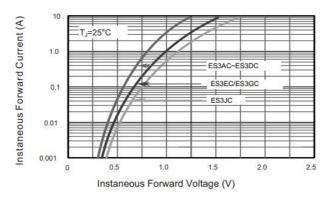
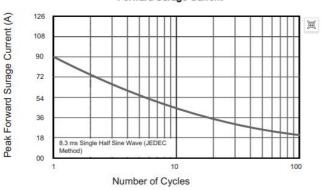


Fig.6 Maximum Non-Repetitive Peak Forward Surage Current



The curve above is for reference only.

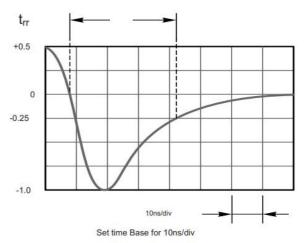


Fig.3 Typical Reverse Characteristics

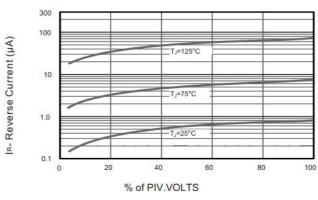


Fig.5 Typical Junction Capacitance

