

General Description

The WSD220N06DN56 uses advanced SGT II technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 10V. This device is suitable for use as a Battery protection or in other Switching application.

The WSD220N06DN56 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

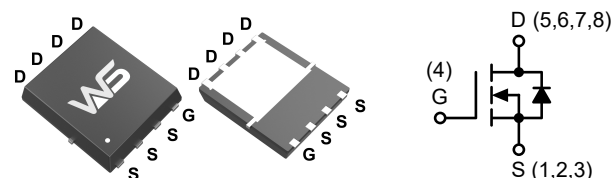
Product Summary

BV_{DSS}	$R_{DS(ON)}$	I_D
60V	1.0m Ω	220A

Applications

- Battery protection
- Uninterruptible Power Supply(UPS)

DFN5X6-8L Pin Configuration



Absolute Maximum Ratings ($T_C=25^{\circ}\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^{\circ}\text{C}$	Continuous Drain Current ^{1,6}	220	A
$I_D@T_C=100^{\circ}\text{C}$	Continuous Drain Current ^{1,6}	158	A
I_{DM}	Pulsed Drain Current ²	1340	A
E_{AS}	Single Pulse Avalanche Energy ³	580	mJ
I_{AS}	Avalanche Current	47	A
$P_D@T_C=25^{\circ}\text{C}$	Power Dissipation ⁴	231	W
T_{STG}	Storage Temperature Range	-55 to 150	$^{\circ}\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^{\circ}\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient ¹	---	55	$^{\circ}\text{C/W}$
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case ¹	---	0.65	$^{\circ}\text{C/W}$

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=250\mu A$	60	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ⁴	$V_{GS}=10V$, $I_D=20A$	---	1.0	1.3	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu A$	2.0	2.9	4.0	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=60V$, $V_{GS}=0V$, $T_J=25^{\circ}\text{C}$	---	---	1.0	μA
		$V_{DS}=60V$, $V_{GS}=0V$, $T_J=100^{\circ}\text{C}$	---	---	100	
I_{GSS}	Gate-Source Leakage Current	$V_{DS}=0V$, $V_{GS}=\pm 20V$	---	---	± 100	nA
g_{fs}	Forward Transconductance ⁴	$V_{GS}=10V$, $I_D=20A$	---	62	---	S
Q_g	Total Gate Charge	$V_{DS}=30V$, $V_{GS}=10V$, $I_D=20A$	---	102	---	nC
Q_{gs}	Gate-Source Charge		---	24.6	---	
Q_{gd}	Gate-Drain Charge		---	28.2	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=30V$, $V_{GS}=10V$, $R_G=3\Omega$, $I_D=20A$	---	15.6	---	ns
T_r	Rise Time		---	29	---	
$T_{d(off)}$	Turn-Off Delay Time		---	63	---	
T_f	Fall Time		---	51	---	
C_{iss}	Input Capacitance	$V_{DS}=30V$, $V_{GS}=0V$, $f=1.0\text{MHz}$	---	5990	---	pF
C_{oss}	Output Capacitance		---	2257	---	
C_{rss}	Reverse Transfer Capacitance		---	86	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
I_S	Continuous Source Current	$T_C=25^{\circ}\text{C}$	---	---	378	A
V_{SD}	Diode Forward Voltage ⁴	$V_{GS}=0V$, $I_S=20A$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=20A$, $di/dt=100A/\mu s$	---	80	---	ns
Q_{rr}	Reverse Recovery Charge		---	114	---	μC

Note:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
2. The data tested by pulsed , pulse width .The E_{AS} data shows Max. rating .
3. The power dissipation is limited by 175 $^{\circ}\text{C}$ junction temperature
4. E_{AS} condition: $T_J=25^{\circ}\text{C}$, $V_{DD}=48V$, $V_G=10V$, $R_G=25\Omega$, $L=0.1\text{mH}$, $I_{AS}=55A$
5. The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

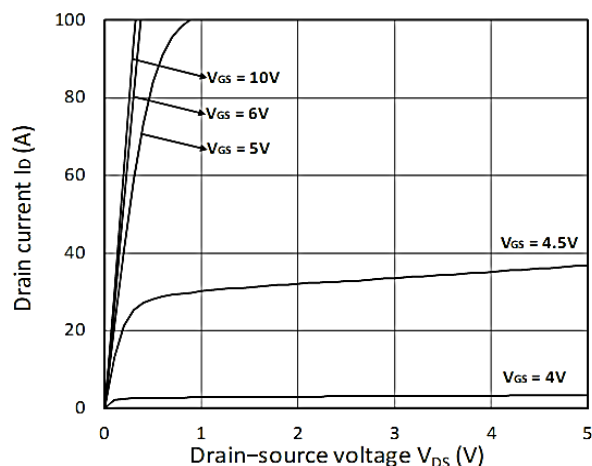


Figure 1. Output Characteristics

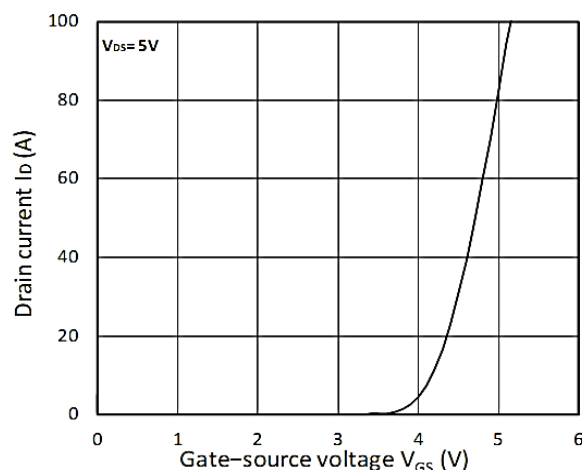


Figure 2. Transfer Characteristics

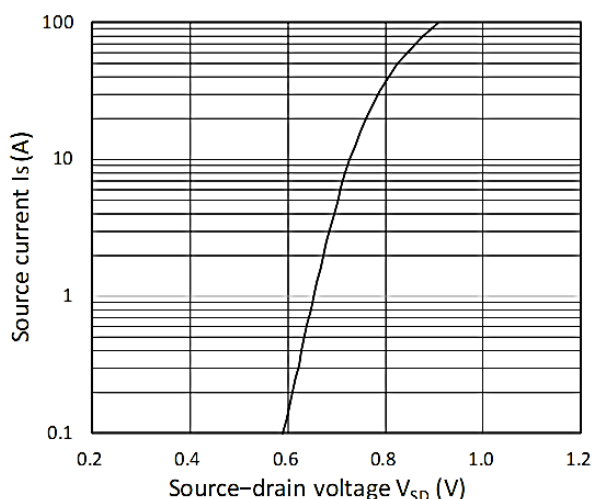


Figure 3. Forward Characteristics of Reverse

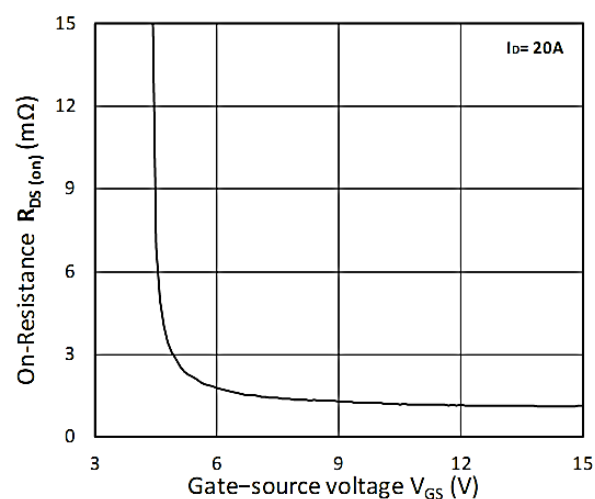


Figure 4. R_DS(ON) vs. V_GS

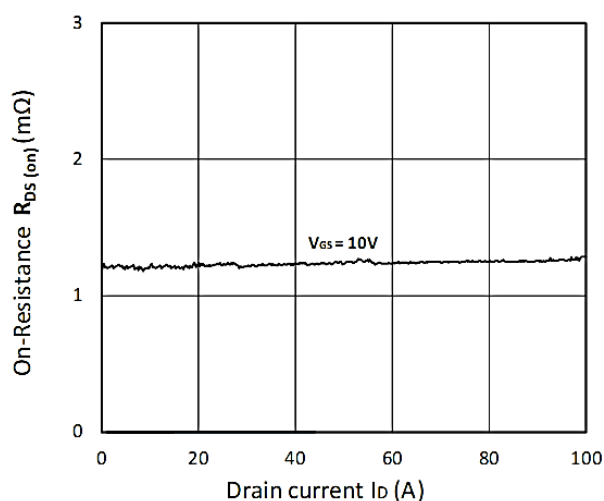


Figure 5. R_DS(ON) vs. I_D

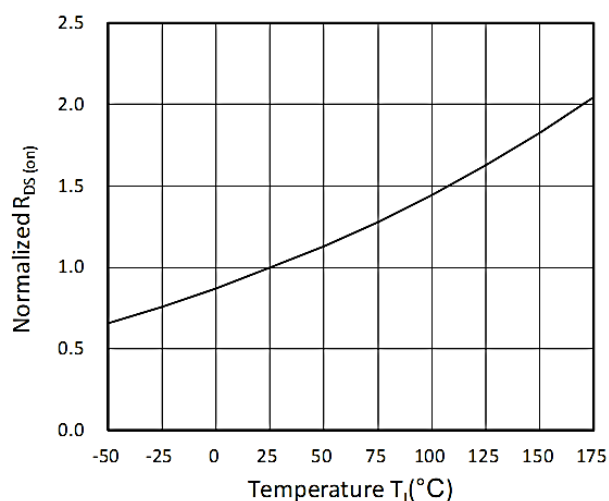
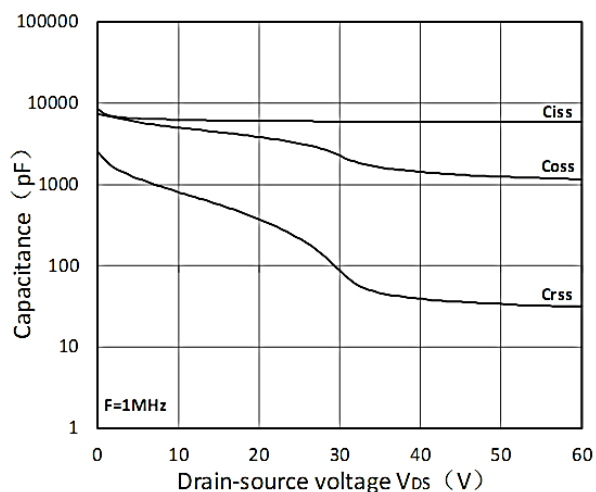
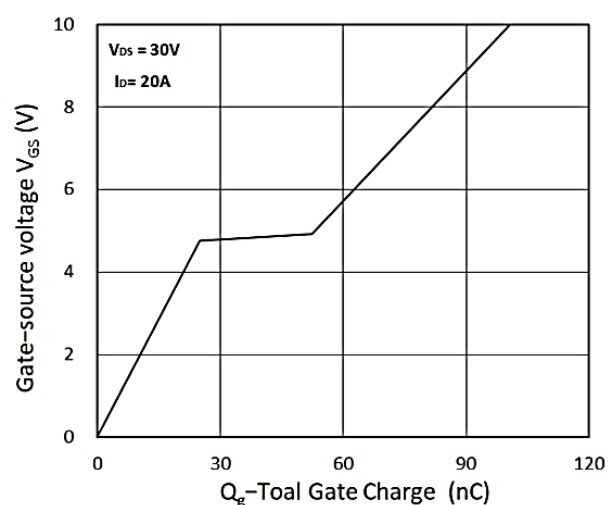
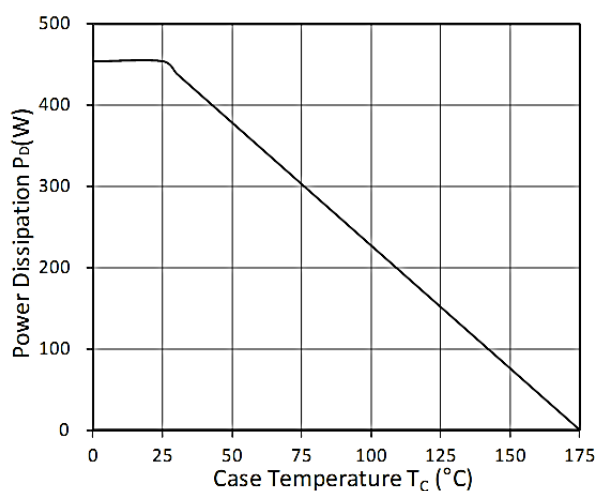
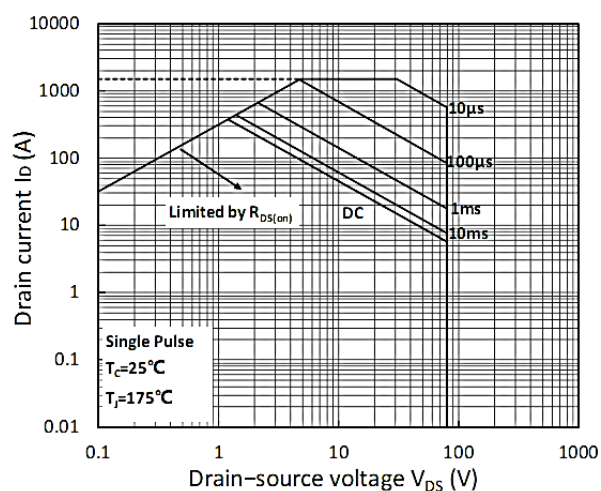
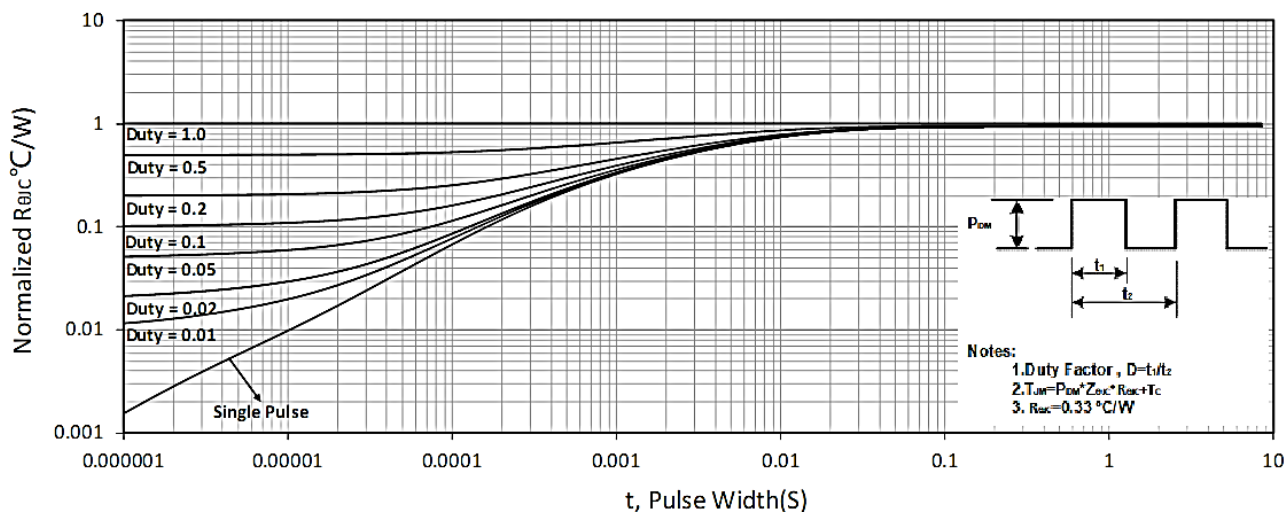
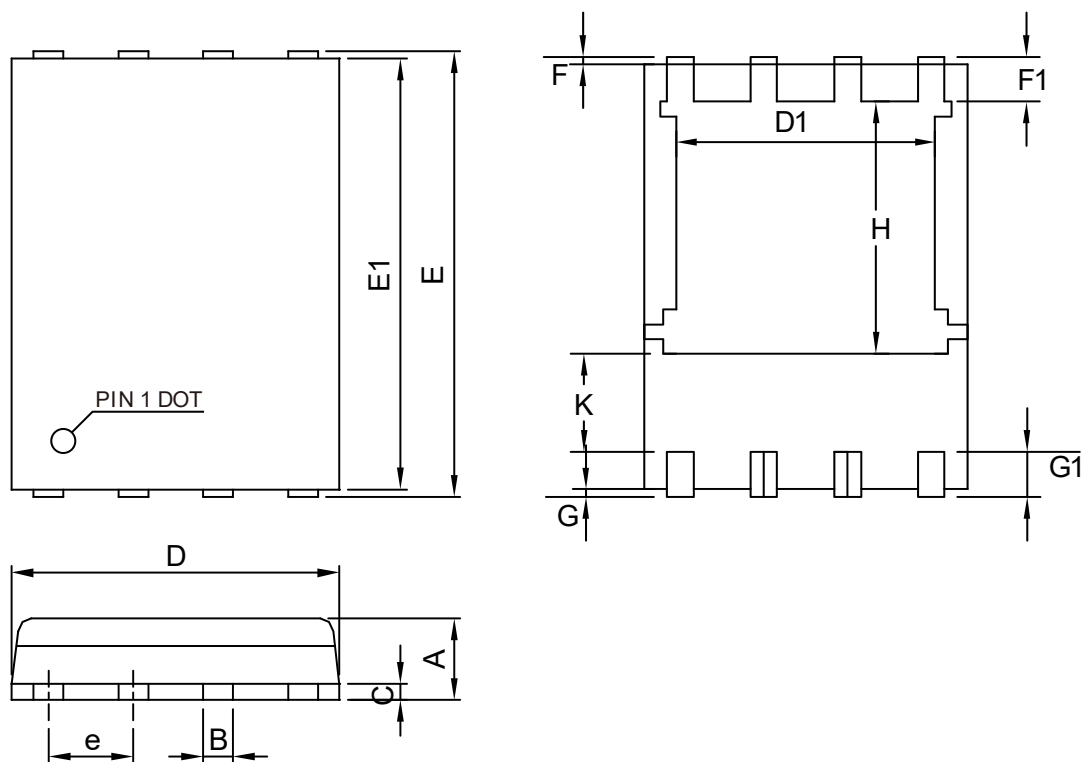


Figure 6. Normalized R_DS(on) vs. Temperature

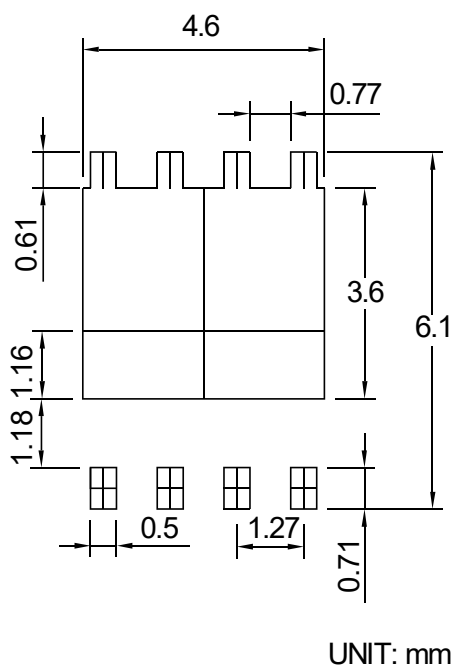
Typical Characteristics (Cont.)

Figure 7. Capacitance Characteristics

Figure 8. Gate Charge Characteristics

Figure 9. Power Dissipation

Figure10. Safe Operating Area

Figure 11. Normalized Maximum Transient Thermal Impedance

Packaging information



SYMBOL	DFN5X6-8L			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.90	1.20	0.035	0.047
B	0.30	0.51	0.012	0.020
C	0.19	0.25	0.007	0.010
D	4.80	5.30	0.189	0.209
D1	4.00	4.40	0.157	0.173
E	5.90	6.20	0.232	0.244
E1	5.50	5.80	0.217	0.228
e	1.27 BSC		0.050 BSC	
F	0.05	0.30	0.002	0.012
F1	0.35	0.75	0.014	0.030
G	0.05	0.30	0.002	0.012
G1	0.35	0.75	0.014	0.030
H	3.34	3.90	0.131	0.154
K	0.762	-	0.033	-

RECOMMENDED LAND PATTERN



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