

**N-Channel MOSFET** 

#### **General Description**

The WSK280N06G6 uses advanced technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 10V.

This device is suitable for use as a Battery protection or in other Switching application.

#### **Features**

- 100% E<sub>AS</sub> Guaranteed
- Green Device Available

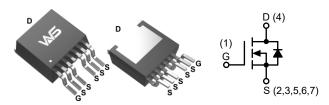
### **Product Summery**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
60V	2.1mΩ	280A

## **Applications**

- Battery protection
- UPS

### **TO-263-6L Pin Configuration**



## **Absolute Maximum Ratings** (T<sub>C</sub>=25°C, Unless Otherwise Noted)

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current <sup>1,6</sup>	280	
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current 1,6	248	А
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	240	
E <sub>AS</sub>	Single Pulse Avalanche Energy <sup>3</sup>	101	mJ
I <sub>AS</sub>	Avalanche Current	55	А
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	168	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	C

#### **Thermal Data**

Symbol	Parameter	Rating	Units
$R_{ heta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	0.89	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	1.5	C/VV



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# **Electrical Characteristics** (T<sub>J</sub>=25°C, Unless Otherwise Noted)

Symbol	Parameter	r Conditions		Тур.	Max.	Units
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250μA	60			V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>4</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =20A		2.1	3.2	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu A$	2.0	2.8	4.0	V
	Busin Course Lordon Course	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C			1.0	μΑ
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V, T <sub>J</sub> =100°C			100	
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA
g <sub>fs</sub>	Forward Transconductance <sup>4</sup>	V <sub>DS</sub> =5V , I <sub>D</sub> =20A		78		S
$R_{g}$	Gate Resistance	f=1.0MHz		2.2		Ω
$Q_g$	Total Gate Charge			72.5		
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =30V , V <sub>GS</sub> =10V , I <sub>D</sub> =20A		19.5		nC
Q <sub>gd</sub>	Gate-Drain Charge			14		
T <sub>d(on)</sub>	Turn-on Delay Time			26.5		
T <sub>r</sub>	Rise Time $V_{DD}$ =30V , $V_{GS}$ =10V ,			15		
$T_{d(off)}$	Turn-off Delay Time	$R_G=3\Omega$ , $I_D=20A$		73		ns
T <sub>f</sub>	Fall Time			18		
C <sub>iss</sub>	Input Capacitance			5245		
C <sub>oss</sub>	Output Capacitance $V_{DS}$ =30V , $V_{GS}$ =0V , $f$ =1.0MHz			1090		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			25		

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
Is	Continuous Source Current	T <sub>C</sub> =25°C			125	Α
$V_{SD}$	Diode Forward Voltage <sup>4</sup>	I <sub>S</sub> =20A , V <sub>GS</sub> =0V			1.2	V
t <sub>rr</sub>	Reverse Recovery Time	L =20.4 di/dt=100.4/up		25		ns
Q <sub>rr</sub>	Reverse Recovery Charge	l <sub>F</sub> =20A , di/dt=100A/μs		90		nC

#### Note:

- 1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2. The data tested by pulsed, pulse width. The  $\,{\sf E}_{\sf AS}\,$  data shows Max. rating.
- 3. The power dissipation is limited by 150°C junction temperature.
- 4.  $E_{AS}$  condition:  $T_J = 25^{\circ}C,\ V_{DD} = 48V,\ V_{GS} = 10V,\ R_G = 25\Omega,\ L = 0.1mH,\ I_{AS} = 55A$
- 5. The data is theoretically the same as  $\ensuremath{I_D}$  and  $\ensuremath{I_{DM}}$ , in real applications , should be limited by total power dissipation.



# **Typical Characteristics**

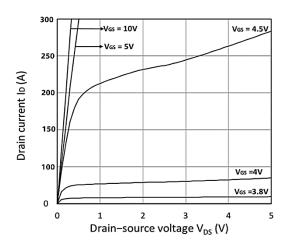


Figure 1. Output Characteristics

(V) SI 10

0.1

0.2

0.4

0.6

0.8

1.0

1.2

Source-drain voltage V<sub>SD</sub> (V)

Figure 3. Forward Characteristics of Reverse

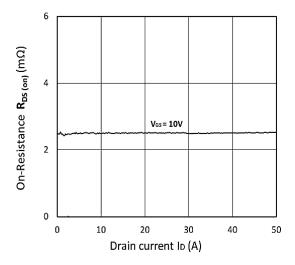


Figure 5. R DS(ON) vs. ID

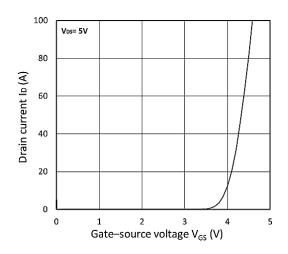


Figure 2. Transfer Characteristics

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Figure 4. RDS(ON) vs. VGS

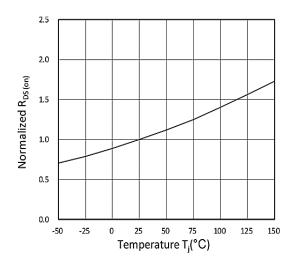


Figure 6. Normalized R DS(on) vs. Temperature



# **Typical Characteristics (Cont.)**

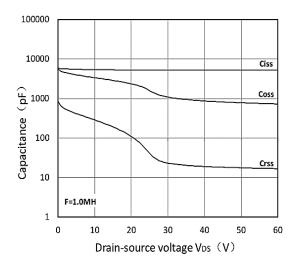


Figure 7. Capacitance Characteristics

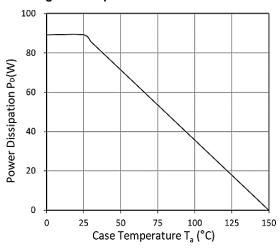


Figure 9. Power Dissipation

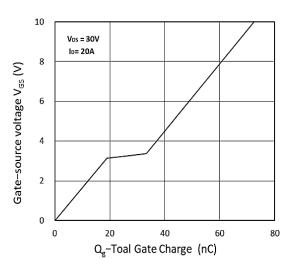


Figure 8. Gate Charge Characteristics

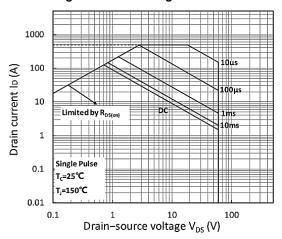


Figure 10. Safe Operating Area

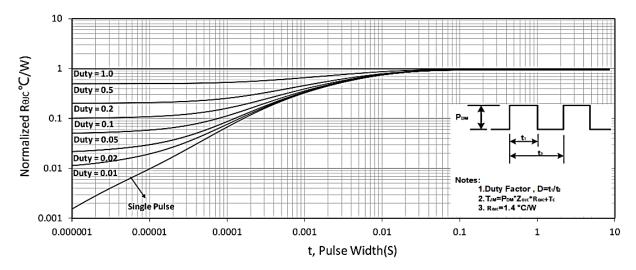
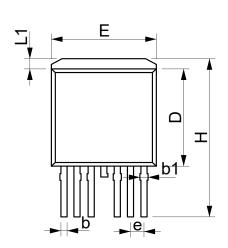


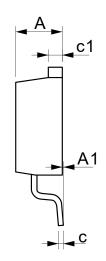
Figure 11. Normalized Maximum Transient Thermal Impedance

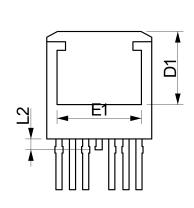


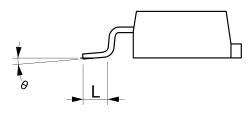
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# **Packaging information**









CVMDOL	MILLIMETERS		INCHES	
SYMBOL	MIN.	MAX.	MIN.	MAX.
Α	4.25	4.55	0.167	0.179
A1	0.01	0.25	0.000	0.010
b	0.50	0.70	0.020	0.028
b1	0.60	0.84	0.024	0.033
С	0.40	0.60	0.016	0.024
c1	1.20	1.40	0.047	0.055
D	9.05	9.45	0.356	0.372
D1	6.90	9.00	0.272	0.354
E	9.80	10.20	0.386	0.402
E1	7.25	9.00	0.285	0.354
е	1.27 BSC		0.05 BSC	
Н	14.65	15.35	0.577	0.604
L	2.40	3.00	0.094	0.118
L1	0.80	1.20	0.031	0.047
L2	0.85	1.15	0.330	0.045
θ	2°	8°	2°	8°



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