

SuperMOS –TO-252 45V BV_{DSS}, 16mΩ R_{DS(on)}, N-channel MOSFET

1. Description

The ESD4186 is N-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent R_{DS(ON)} with low gate charge. Device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product ESD4186 is Pb-free.

2. Features

- 45V, R_{DS(ON)}=16mΩ(TYP.) @V_{GS}=10V
R_{DS(ON)}=21mΩ(TYP.) @V_{GS}=4.5V
- Use trench MOSFET technology
- High density cell design for low R_{DS(on)}
- Material: Halogen free
- Reliable and rugged
- Avalanche Rated
- Low leakage current

3. Applications

- PWM applications
- Load switch
- Power management in portable/desktop PCs
- DC/DC conversion

100% UIS TESTED

4. Ordering Information

Part Number	Package	Marking	Material	Packing	Quantity per reel	Flammability Rating	Reel Size
ESD4186	TO-252	ESD4186/lot	Halogen free	Tape & Reel	2,500 PCS	UL 94V-0	13 inches

Table-1 Ordering information

5. Pin Configuration and Functions

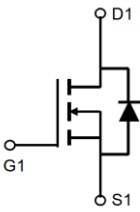
Pin	Function	Outline	Circuit Diagram
1	Gate		
2	Drain		
3	Source		

Table-2 Pin configuration

6. Specification

Absolute Maximum Rating & Thermal Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		BV_{DSS}	45	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C=25^{\circ}\text{C}$	I_D	36	A
	$T_C=75^{\circ}\text{C}$		28	
Maximum Power Dissipation	$T_C=25^{\circ}\text{C}$	P_D	42	W
	$T_C=75^{\circ}\text{C}$		25	
Pulsed Drain Current ^a		I_{DM}	144	A
Avalanche Current, Single Pulsed ^b		I_{AS}	19	A
Avalanche Energy, Single Pulsed ^b		E_{AS}	54	mJ
Operating Junction Temperature		T_J	150	$^{\circ}\text{C}$
Lead Temperature		T_L	260	$^{\circ}\text{C}$
Storage Temperature Range		T_{stg}	-55 to 150	$^{\circ}\text{C}$

Thermal resistance ratings

Single Operation					
Parameter		Symbol	Typical	Maximum	Unit
Junction-to-Case Thermal Resistance ^a	$t \leq 10 \text{ s}$	$R_{\theta JC}$	2.5	3	$^{\circ}\text{C/W}$

Note:

a: Surface mounted on FR4 Board using 1 square inch pad size, 1oz copper

b: $T_J=25^{\circ}\text{C}$, $V_{DD}=45\text{V}$, $V_G=10\text{V}$, $L=0.3\text{mH}$, $R_g=25\Omega$

Electrical Characteristics

At TA = 25°C unless otherwise specified

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	45			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =45V, V _{GS} =0V			1.0	uA
Gate-to-source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250uA	1.0	1.4	1.8	V
Drain-to-source On-resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A		16	21	mΩ
		V _{GS} =4.5V, I _D =15A		21	29	
Forward Trans conductance	g _{FS}	V _{DS} =5.0V, I _D =20A			80	S
CHARGES, CAPACITANCES AND GATE RESISTANCE						
Input Capacitance	C _{ISS}	V _{GS} =0V, f=1MHz, V _{DS} =20V		980		pF
Output Capacitance	C _{OSS}			130		
Reverse Transfer Capacitance	C _{RSS}			80		
Total Gate Charge	Q _{G(TOT)}	V _{GS} =10V, V _{DS} =20V, I _D =20A		17		nC
Gate-to-Source Charge	Q _{GS}			2.5		
Gate-to-Drain Charge	Q _{GD}			4.5		
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{d(ON)}	V _{GS} =10V, V _{DS} =20V, R _L =1.5Ω, R _{GEN} =3Ω		6.5		ns
Rise Time	t _r			17.6		
Turn-Off Delay Time	t _{d(OFF)}			19.8		
Fall Time	t _f			16.6		
BODY DIODE CHARACTERISTICS						
Forward Voltage	V _{SD}	V _{GS} =0V, I _S =1.0A	0.45		1.5	V

7. Typical Characteristic

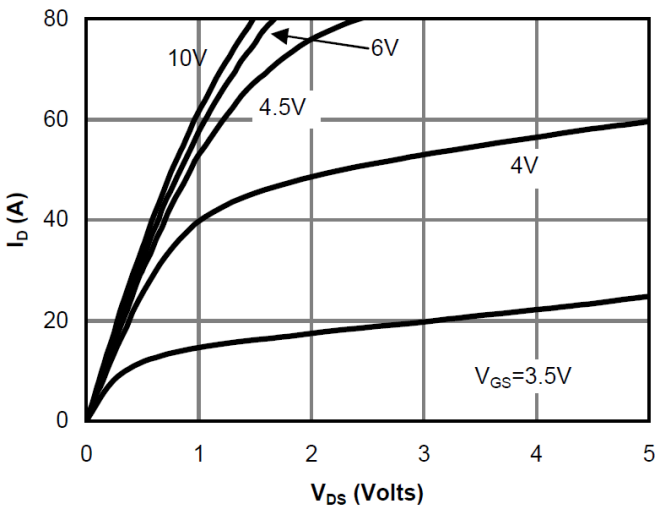


Fig 1: On-Region Characteristics

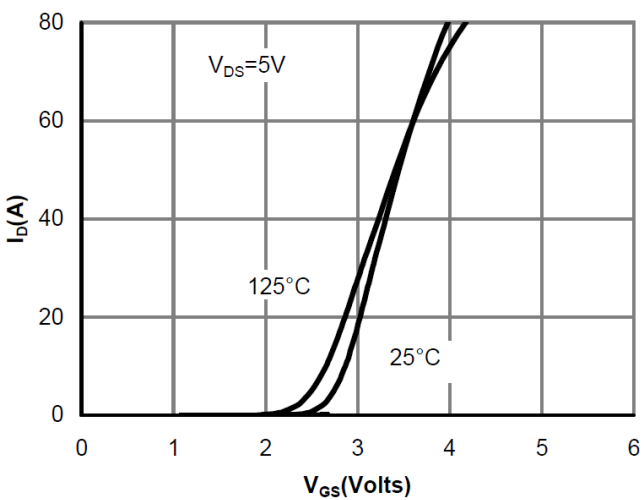


Figure 2: Transfer Characteristics

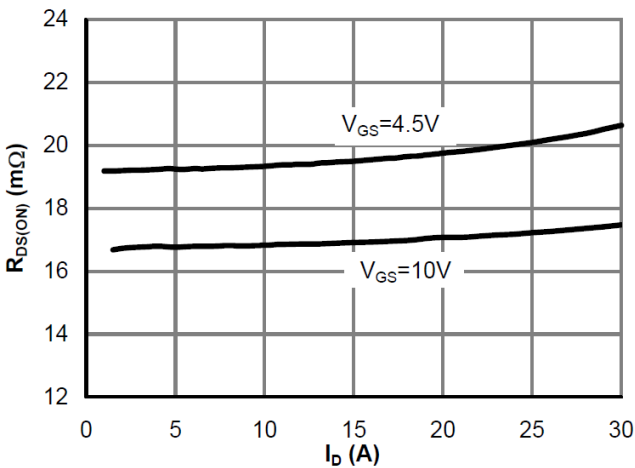


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

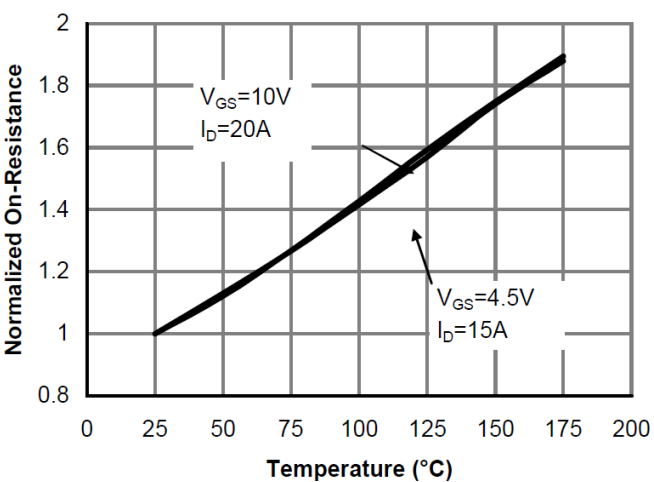


Figure 4: On-Resistance vs. Junction Temperature

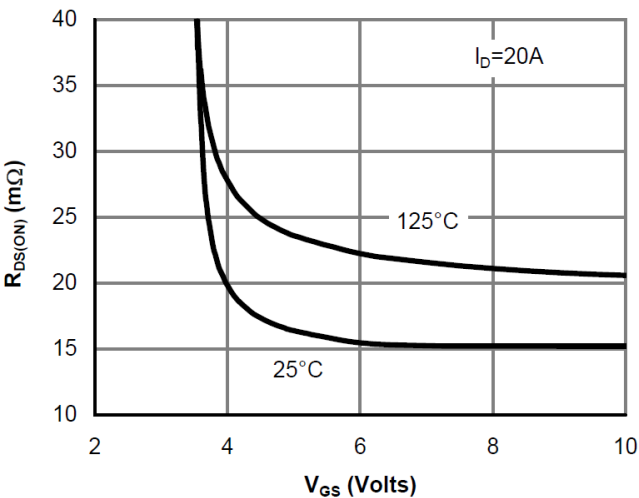


Figure 5: On-Resistance vs. Gate-Source Voltage

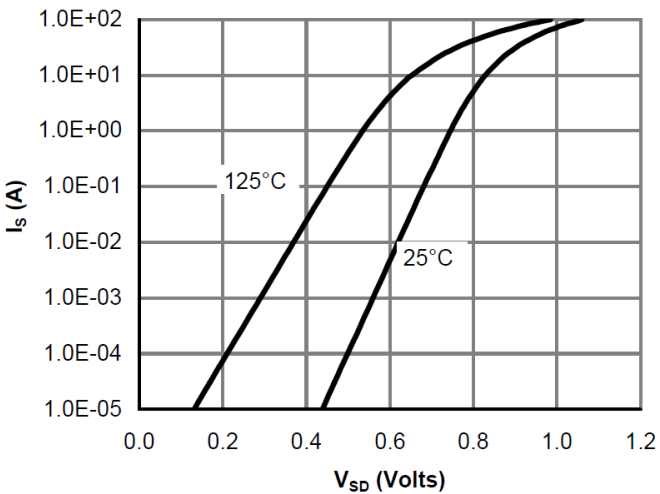


Figure 6: Body-Diode Characteristics

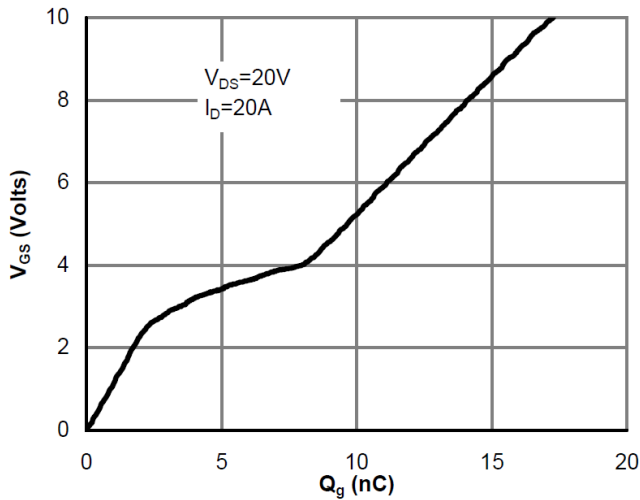


Figure 7: Gate-Charge Characteristics

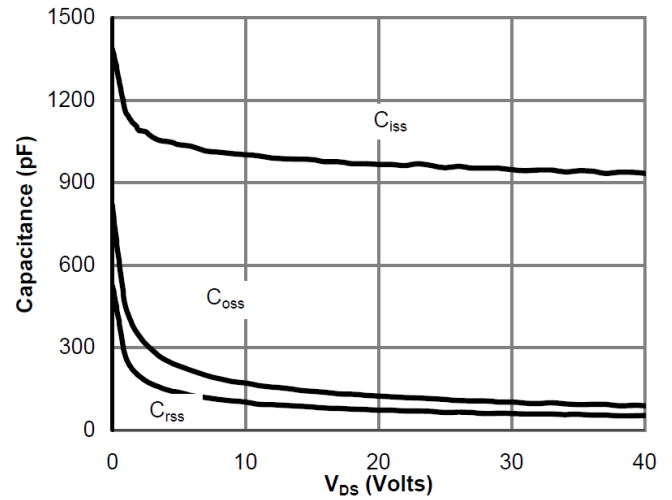


Figure 8: Capacitance Characteristics

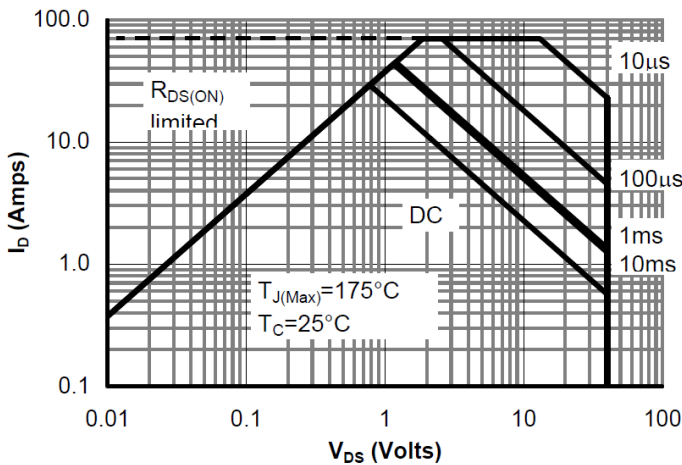


Figure 9: Maximum Forward Biased Safe Operating Area

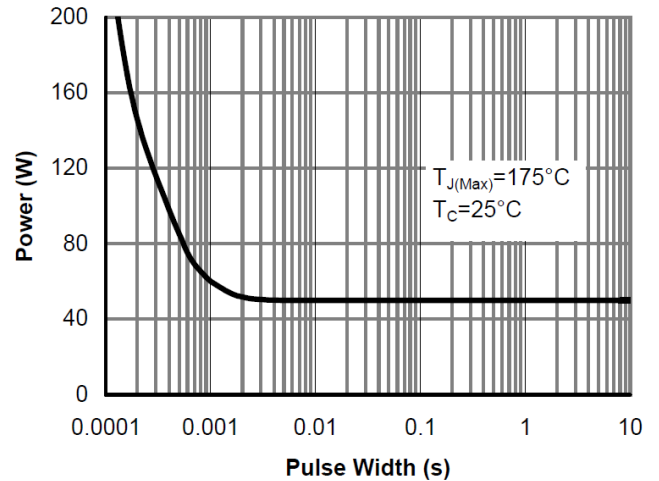


Figure 10: Single Pulse Power Rating Junction-to-Case

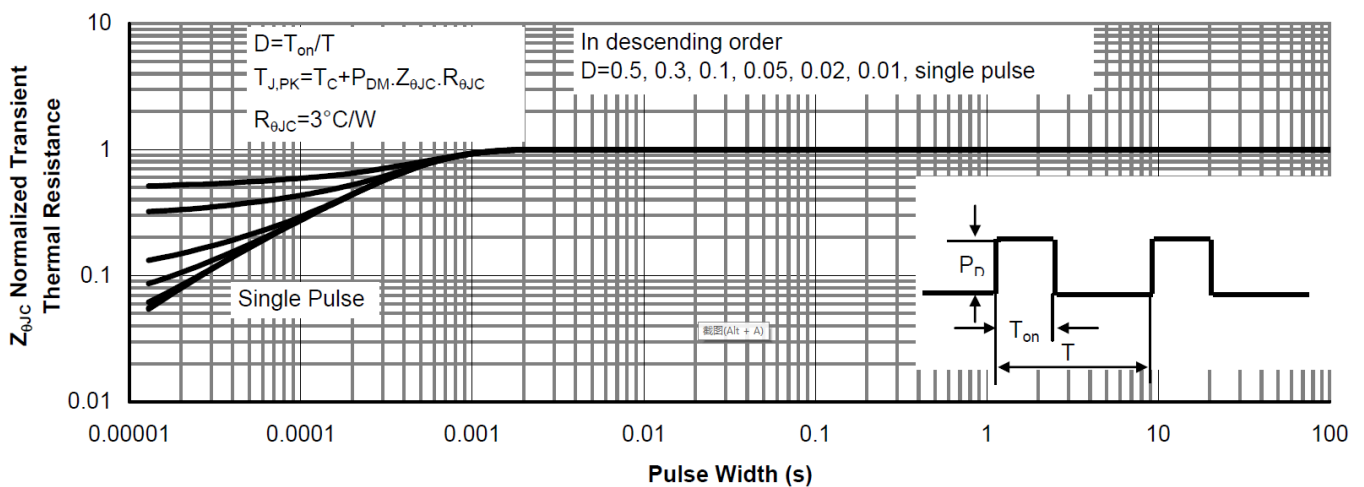
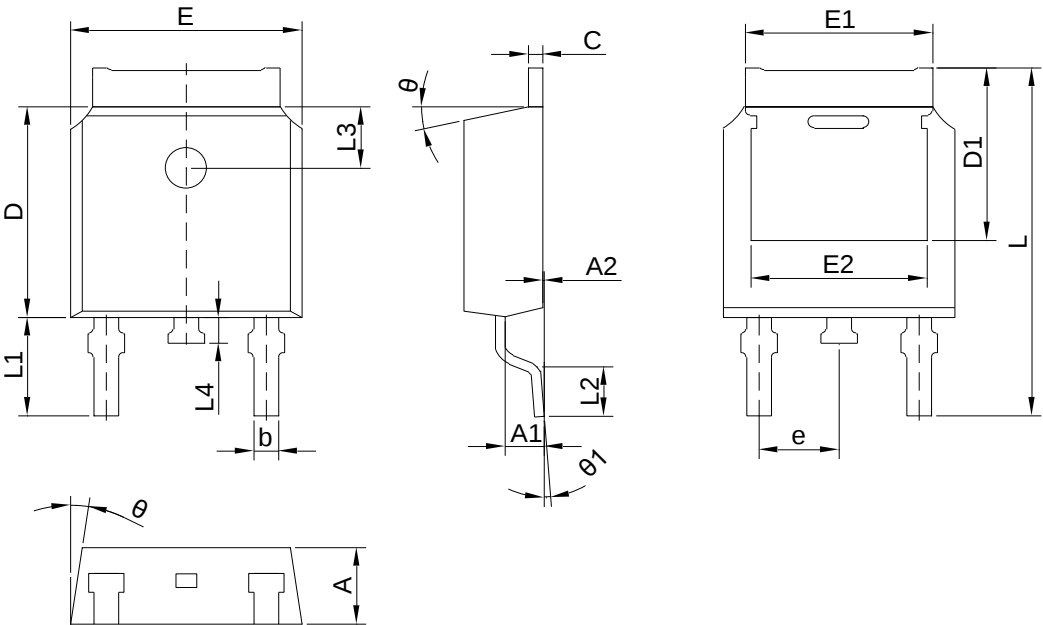


Figure 11: Normalized Maximum Transient Thermal Impedance

8. Dimension (TO-252)



COMMON DIMENSIONS CUNITS MEASURE=MILLIMETER							
SYMBOL	MIN	TYP	MAX	SYMBOL	MIN	TYP	MAX
A	2.10	2.30	2.50	E2	4.63	4.83	5.03
A1	0.97	1.07	1.17	L	9.90	10.10	10.30
A2	0.00	-	0.12	L1	2.74	2.94	3.14
b	0.66	0.76	0.86	L2	1.40	1.50	1.70
C	0.45	0.51	0.60	L3	1.65	1.80	1.95
D	5.90	6.10	6.30	L4	0.60	0.80	1.00
D1	5.10	5.30	5.45	e	2.286 BSC		
E	6.40	6.60	6.80	θ	5°	7°	10°
E1	5.10	5.33	5.45	θ1	0°	-	3°

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