

SuperESD - TPD2EUSB30DRTR-ES

1. Description

The TPD2EUSB30DRTR-ES is an ultra-low capacitance TVS (Transient Voltage Suppressor) array designed to protect high speed data interfaces. It has been specifically designed to protect sensitive electronic components which are connected to data and transmission lines from over-stress caused by Electrostatic Discharge.

2. Features

- IEC 61000-4-2 Level 4 ESD Protection
 - $\pm 15\text{kV}$ Contact Discharge
 - $\pm 15\text{kV}$ Air Discharge
- 30W Peak pulse Power (8/20us)
- Low clamping voltage
- Working voltage: 5V
- Low leakage current
- RoHS compliant
- Protecting two unidirectional lines
- Junction Capacitance: 0.5pF

3. Applications

- USB 2.0 and USB 3.0
- HDMI 1.3 and HDMI 1.4
- SATA and eSATA
- IEEE 1394
- PCI Express
- Notebooks

4. Ordering Information

Part Number	Package	Marking	Material	Packing	Quantity per reel	Flammability Rating	Reel Size
TPD2EUSB30DRTR-ES	SOT-723	M5	Halogen free	Tape & Reel	8,000 PCS	UL 94V-0	7 inches

Table-1 Ordering information

5. Pin Configuration and Functions

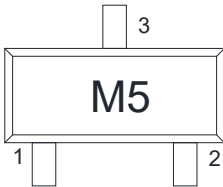
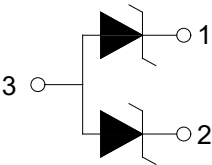
Pin	Name	Description	Outline	Circuit Diagram
1	IO	Connect to IO		
2	IO	Connect to IO		
3	GND	Connect to GND		

Table-2 Pin configuration

6. Specification

6.1. Absolute Maximum rating

Over operating free-air temperature range (unless otherwise noted)

Parameters	Symbol	Min.	Max.	Unit
Peak pulse power (tp=8/20us)@25°C	P _{pk}	-	30	W
Peak pulse current (tp=8/20us)@25°C	I _{PP}		3	A
ESD (IEC61000-4-2 air discharge) @25°C	V _{ESD}	-	±15	kV
ESD (IEC61000-4-2 contact discharge) @25°C	V _{ESD}	-	±15	kV
Junction temperature	T _J	-	150	°C
Operating temperature	T _{OP}	-40	125	°C
Storage temperature	T _{STG}	-55	150	°C
Lead temperature	T _L	-	260	°C

Table-3 Absolute Maximum rating

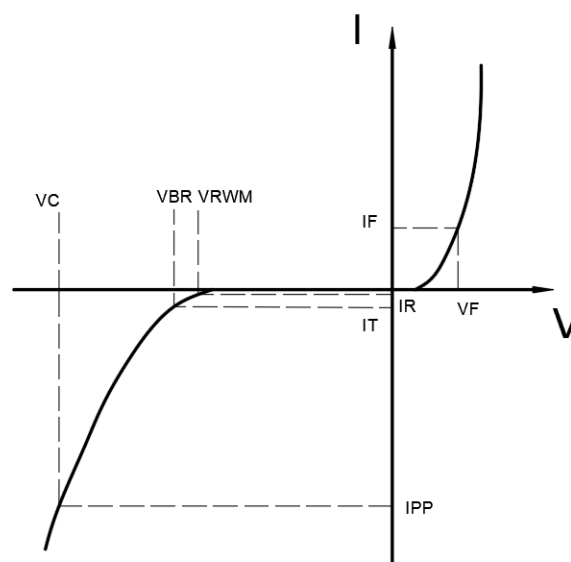
6.2. Electrical Characteristics

At TA = 25°C unless otherwise noted

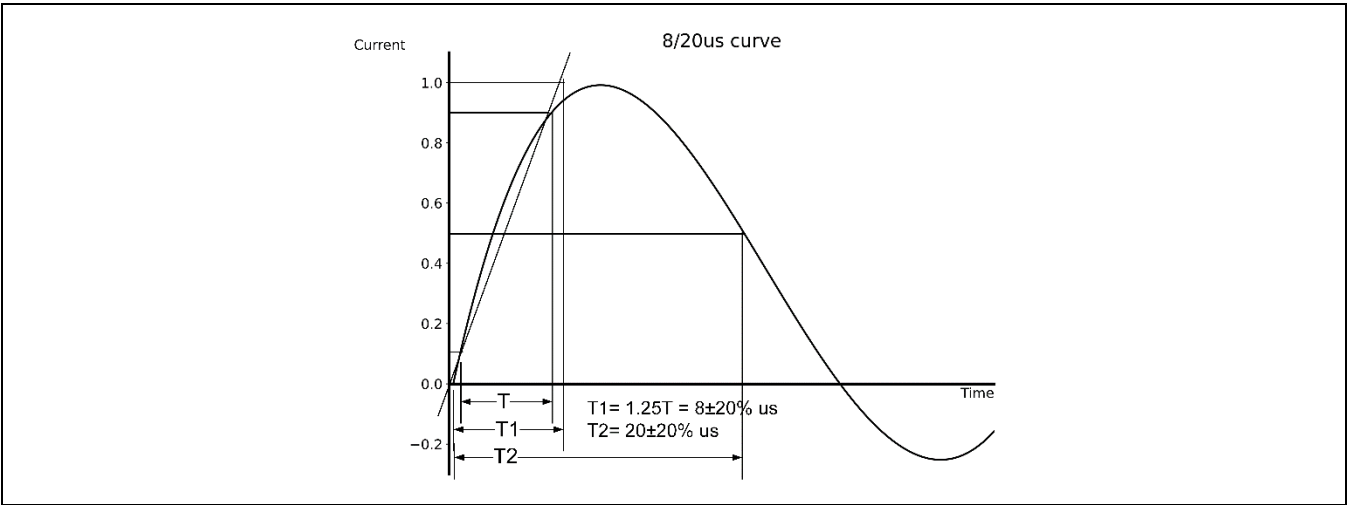
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Reverse Stand-off Voltage	V_{RWM}				5.0	V
Reverse Breakdown Voltage	V_{BR}	$I_T=1mA$	6.0			V
Reverse Leakage Current	I_R	$V_{RWM}=5V$			1.0	μA
Clamping Voltage	V_C	$I_{PP}=1A$; $t_p=8/20\mu s$		8.5	10.0	V
Clamping Voltage	V_C	$I_{PP}=3A$; $t_p=8/20\mu s$		10.0	12.0	V
Junction Capacitance	C_J	IO-GND $V_R=0V$; $f=1MHz$		0.5	0.8	pF
		IO-IO $V_R=0V$; $f=1MHz$		0.25	0.4	pF

Table-4 Electrical Characteristics

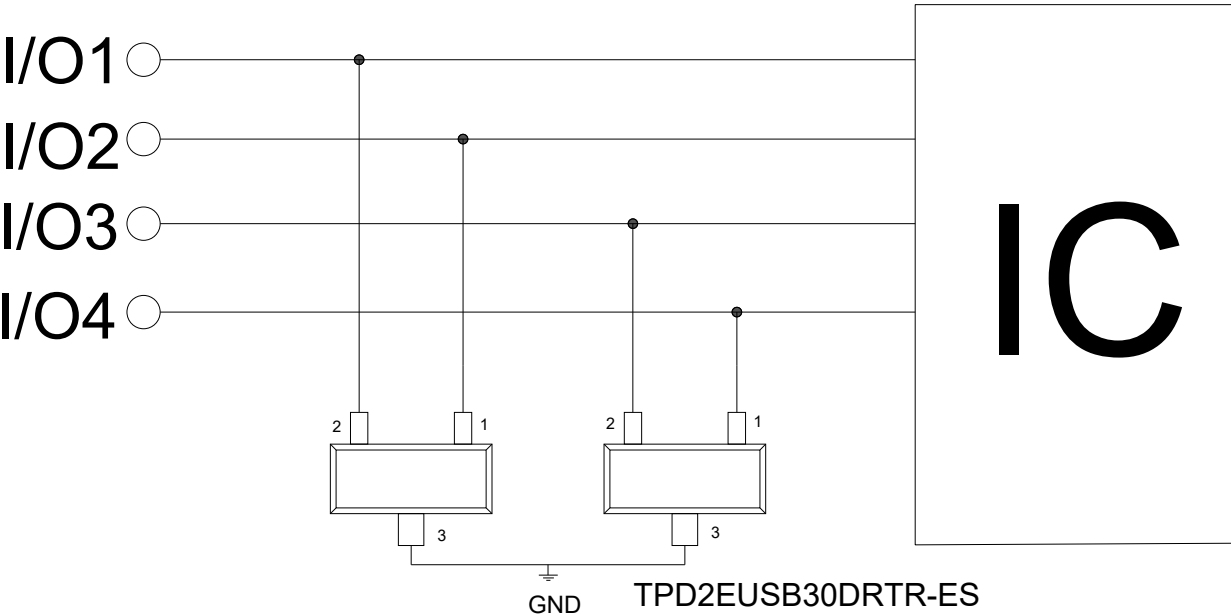
Symbol	Parameters
V_{RWM}	Peak Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
I_F	Forward Current
V_F	Forward Voltage @ I_F



7. Typical Characteristic



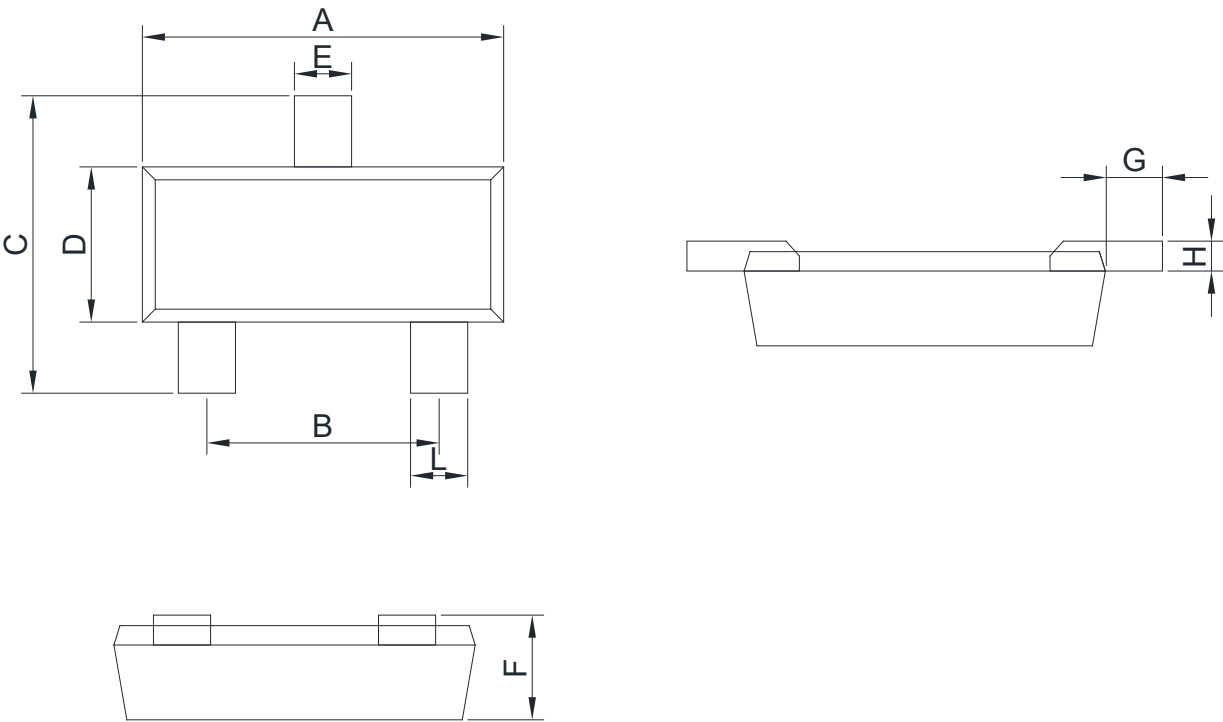
8. Typical Application



Typical Interface Application Protection

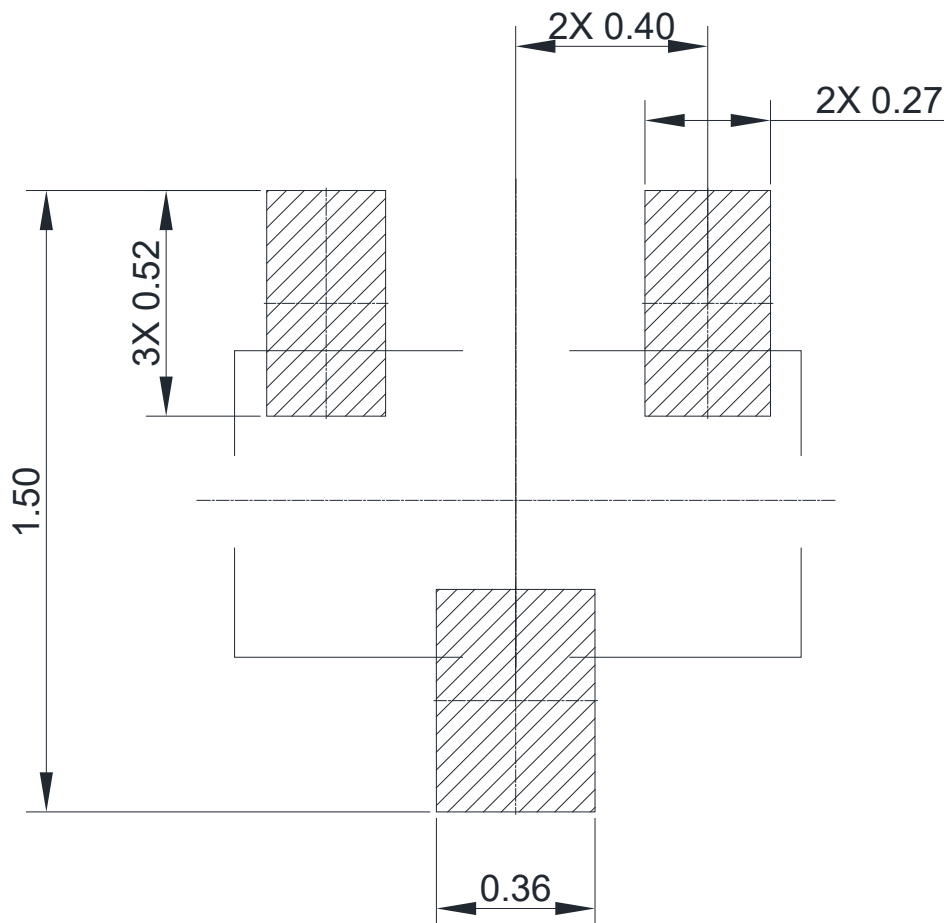
9. Dimension (SOT-723)

POD(Z)



Dimensions in Millimeters					
Symbol	Min.	Max.	Symbol	Min.	Max.
A	1.10	1.30	F	0.40	0.50
B	0.80TYP		G	0.10	0.30
C	1.10	1.30	H	0.06	0.16
D	0.70	0.90	L	0.15	0.25
E	0.15	0.35			

10. Recommended Soldering Footprint



DIMENSIONS: MILLIMETERS

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