<u>ElecSuper</u>

SuperESD - TPESD0502B

1. Description

The TPESD0502B is an ultra-low capacitance TVS (Transient Voltage Suppressor) array designed to protect high speed data interfaces. It has been specifically designed to protect sensitive electronic components which are connected to data and transmission lines from over-stress caused by Electrostatic Discharge.

2. Features

- IEC 61000-4-2 Level 4 ESD Protection
 - ±15kV Contact Discharge
 - ±15kV Air Discharge
- 72W Peak pulse Power (8/20us)
- Low clamping voltage

- Working voltage: 5V
- Low leakage current
- RoHS compliant
- Protecting two unidirectional lines

3. Applications

- USB 2.0 and USB 3.0
- HDMI 1.3 and HDMI 1.4
- SATA and eSATA

- IEEE 1394
- PCI Express
- Notebooks

4. Ordering Information

Part Number	Package I	Marking	Material	Packing	Quantity	Flammability	Reel
Part Number					per reel	Rating	Size
TPESD0502B	SOT-523	P5	Halogen free	Tape & Reel	3,000 PCS	UL 94V-0	7 inches

Table-1 Ordering information



5. Pin Configuration and Functions

Pin	Name	Description	Outline	Circuit Diagram
1	Ю	Connect to IO	<u> </u>	1
2	Ю	Connect to IO	P5	3 •
3	GND	Connect to GND	1 2	• 2

Table-2 Pin configuration

6. Specification

6.1. Absolute Maximum rating

Over operating free-air temperature range (unless otherwise noted)

Parameters	Symbol	Min.	Max.	Unit
Peak pulse power (tp=8/20us)@25°C	P_{pk}	-	72	W
Peak pulse current (tp=8/20us)@25°C	I _{PP}		4	А
ESD (IEC61000-4-2 air discharge) @25°C	V _{ESD}	-	±15	kV
ESD (IEC61000-4-2 contact discharge) @25°C	V _{ESD}	-	±15	kV
Junction temperature	TJ	-	150	°C
Operating temperature	T _{OP}	-40	125	°C
Storage temperature	T _{STG}	-55	150	°C
Lead temperature	TL	-	260	°C

Table-3 Absolute Maximum rating



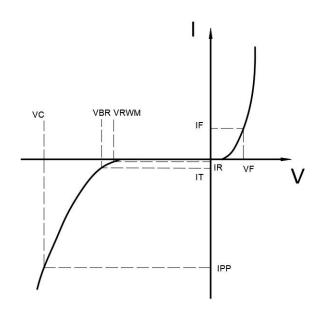
6.2. Electrical Characteristics

At TA = 25°C unless otherwise noted

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Reverse Stand-off Voltage	V_{RWM}				5.0	V
Reverse Breakdown Voltage	V_{BR}	IT=1mA	6.0		9.0	V
Reverse Leakage Current	I _R	VRWM=5V			1.0	uA
Clamping Voltage	Vc	IPP=1A; tp=8/20us		10.0	12.0	V
Clamping Voltage	Vc	IPP=4A; tp=8/20us		15.0	18.0	V
Junction Capacitance	Сл	VR=0V; f=1MHz		0.6	1.0	pF

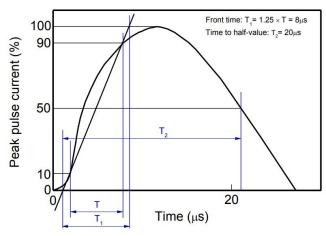
Table-4 Electrical Characteristics

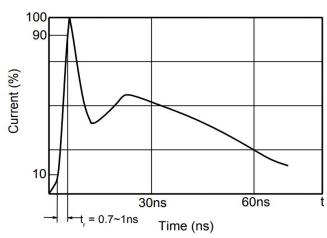
Symbol	Parameters
V _{RWM}	Peak Reverse Working Voltage
I _R	Reverse Leakage Current @ V _{RWM}
V_{BR}	Breakdown Voltage @ I⊤
I _T	Test Current
I _{PP}	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @ IPP
I _F	Forward Current
V _F	Forward Voltage @ I _F



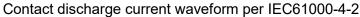


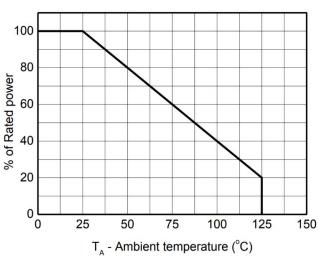
7. Typical Characteristic

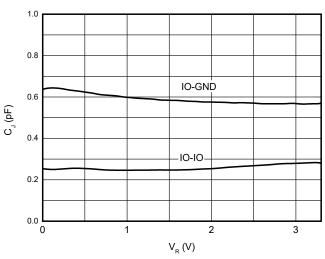




8/20µs waveform per IEC61000-4-5

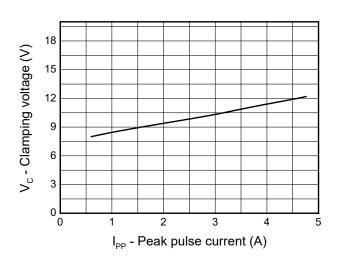






Power derating vs. Ambient temperature

Capacitance vs. Reverse voltage

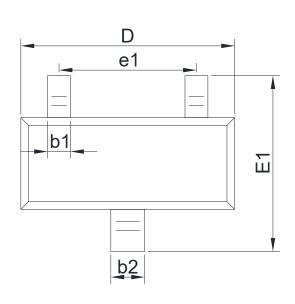


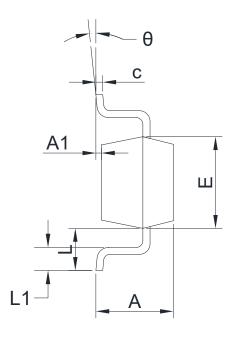


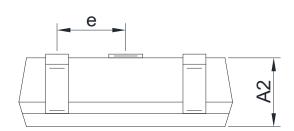
Clamping voltage vs. Peak pulse current

8. Dimension (SOT-523)

POD(Z)



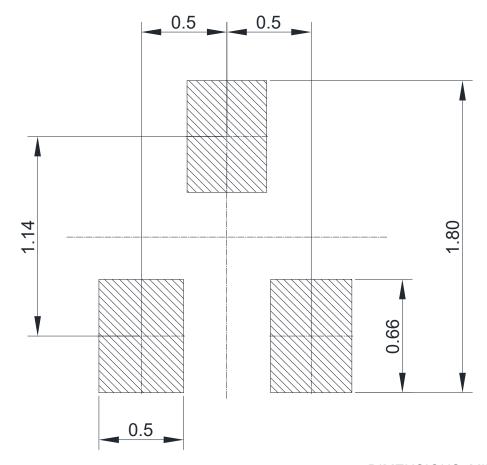




REF	Millimeters		REF	Millimeters		
	Min	Max	KEF	Min	Max	
Α	0.70	0.90	E1	1.45	1.75	
A1	0.00	0.10	E	0.70	0.90	
A2	0.70	0.80	е	0.50TYP		
b1	0.15	0.25	e1	0.90	1.10	
b2	0.25	0.35	L	0.40REF		
С	0.10	0.20	L1	0.25	0.46	
D	1.50	1.70	θ	0°	8°	



9. Recommended Soldering Footprint



DIMENSIONS: MILLIMETERS

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