TPD4E001DRLR-ES

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SuperESD – TPD4E001DRLR-ES

1.Description

The TPD4E001DRLR-ES is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability. Low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium.

2.Features

- IEC 61000-4-2 Level 4 ESD Protection
 - ±12kV Contact Discharge
 - ±17kV Air Discharge
- IEC 61000-4-4 EFT Protection
 - 40A (5/50ns)
- IEC 61000-4-5 Surge
 - 4.0A (8/20us)

- RoHS compliance
- Protecting four I/O line
- Ultra-low Capacitance:0.6pF (Typical)
- Low clamping voltage
- Low leakage current
- Solid-state silicon technology

3.Applications

- Cellular Handsets & Accessories
- Cordless Phones
- Personal Digital Assistants (PDA's)
- Portable Instrumentation
- Notebooks & Handhelds
- Digital Cameras

4. Ordering Information

Part Number	Package	Marking	Material	Packing	Quantity	Flammability	Reel
				Facking	per reel	Rating	Size
TPD4E001DRLR-ES	SOT 562	.E54	Halogen	Tape &	3.000	UL 94V-0	7
	301-303		free	Reel	PCS	UL 94V-0	inches

Table-1Ordering information



Pin	Name	Description	Outline	Circuit Diagram				
1	IO	Connect to IO						
2	GND	Connect to GND		5				
3	IO	Connect to IO						
4	IO	Connect to IO	E54					
5	VCC	Connect to Vcc						
6	IO	Connect to IO						
Table-2 Pin configuration								

5.Pin Configuration and Functions

6.Specification

6.1. Absolute Maximum rating

Over operating free-air temperature range (unless otherwise noted)

Parameters	Symbol	Min.	Max.	Unit
Peak pulse power (tp=8/20us)@25°C	P _{pk}	-	60	W
Peak pulse current (tp=8/20us)@25°C	I _{PP}		4.0	А
ESD (IEC61000-4-2 air discharge) @25°C	V_{ESD}	-	±17	kV
ESD (IEC61000-4-2 contact discharge) @25°C	V_{ESD}	-	±12	kV
Junction temperature	TJ	-	150	°C
Operating temperature	T _{OP}	-40	125	°C
Storage temperature	T _{STG}	-55	150	°C
Lead temperature	T_L	-	260	°C

Table-3 Absolute Maximum rating



6.2. Electrical Characteristics

At TA = 25° C unless other	erwise noted
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Parameters	Symbol	conditions	Min.	Тур.	Max.	Unit
Reverse stand-off voltage	V _{RWM}				5.0	V
Reverse Breakdown Voltage	V _{BR}	I⊤= 1mA	6.0			V
Reverse Leakage Current	I _R	V _{RWM} =5V			1.0	uA
Peak Pulse Current	IPP	TP=8/20us@25°C		4.0		А
Clamping Voltage	V _{CL}	I _{PP} =1.0A; TP=8/20us		8.5	10.0	V
Clamping Voltage	V _{CL}	I _{PP} =4.0A; TP=8/20us		12.0	15.0	V
	CJ	I/O pins to ground; VR=0V; f = 1MHz		0.6	0.8	
Junction capacitance		Between I/O pins; VR=0V; f = 1MHz		0.3	0.4	pF

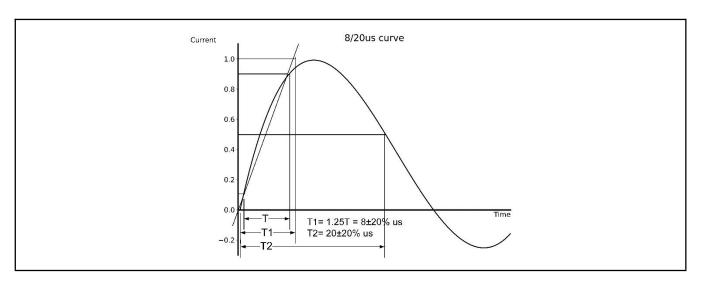
Table-4 Electrical Characteristics



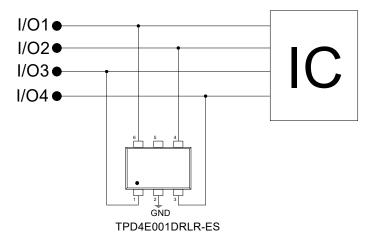
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7. Typical Characteristic



8. Typical Application

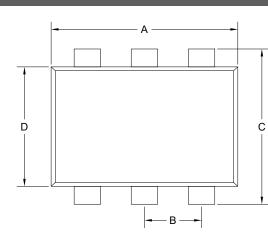


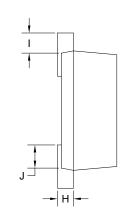
Typical Interface Application

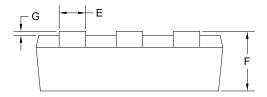
9.Dimensions (SOT-563)



TPD4E001DRLR-ES







Units in millimeters

symbol	А	В	С	D	E	F	G	Н	I	J
Min.	1.50	0.50	1.55	1.10	0.15	0.50	0.00	0.10	0.10	0.15
Max.	1.70	0.60	1.70	1.25	0.30	0.60	0.05	0.18	0.30	0.20

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