

SuperMOS – SOT-723 60V BV_{DSS} 1.9Ω $R_{DS(on)}$ N-channel MOSFET

1. Description

The 2N7002M3T5G-ES is N-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. Device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product 2N7002M3T5G-ES is Pb-free.

2. Features

- 60V, $R_{DS(on)}=1.9\Omega$ (Typ), $V_{GS}=10V$
 $R_{DS(on)}=2.4\Omega$ (Typ), $V_{GS}=4.5V$
- Use trench MOSFET technology
- High density cell design for low $R_{DS(on)}$
- Material: Halogen free
- Reliable and rugged
- Avalanche Rated
- Low leakage current

3. Applications

- PWM applications
- Load switch
- Power management in portable/desktop PCs
- DC/DC conversion

4. Ordering Information

Part Number	Package	Marking	Material	Packing	Quantity per reel	Flammability Rating	Reel Size
2N7002M3T5G-ES	SOT-723	72KR	Halogen free	Tape & Reel	8,000 PCS	UL 94V-0	7 inches

Table-1 Ordering information

5. Pin Configuration and Functions

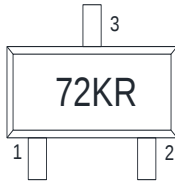
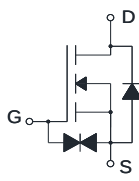
Pin	Function	Outline	Circuit Diagram
1	Gate		
2	Source		
3	Drain		

Table-2 Pin configuration

6. Specification

Absolute Maximum Rating & Thermal Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		BV_{DSS}	60	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_A=25^{\circ}\text{C}$	I_D	0.18	A
	$T_A=75^{\circ}\text{C}$		0.14	
Maximum Power Dissipation		P_D	150	mW
Pulsed Drain Current ^a		I_{DM}	0.72	A
Operating Junction Temperature		T_J	150	$^{\circ}\text{C}$
Lead Temperature		T_L	260	$^{\circ}\text{C}$
Storage Temperature Range		T_{stg}	-55 to 150	$^{\circ}\text{C}$

Thermal resistance ratings

Single Operation			
Parameter	Symbol	Typical	Unit
Junction-to-Ambient Thermal Resistance	$R_{\theta JA}$	833	$^{\circ}\text{C/W}$

Note:

a: Repetitive rating, pulse width limited by junction temperature, $t_p=10\mu\text{s}$, Duty Cycle=1%

Electrical Characteristics

At TA = 25°C unless otherwise specified

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =10mA	60			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V, T _J =25°C			1.0	uA
		V _{DS} =48V, V _{GS} =0V, T _J =125°C			100	
Gate-to-source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±10	uA
Forward Trans conductance	g _{fs}	V _{DS} =10V, I _D =0.1A		0.24		S
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250uA	0.8	1.5	2.5	V
Drain-to-source On-resistance	R _{DS(on)}	V _{GS} =10V, I _D =0.3A		1.9	3	Ω
		V _{GS} =4.5V, I _D =0.2A		2.4	4	
CHARGES, CAPACITANCES AND GATE RESISTANCE						
Input Capacitance	C _{ISS}	V _{GS} =0V, f=1MHz, V _{DS} =10V		30.5	45	pF
Output Capacitance	C _{OSS}			5.5	10	
Reverse Transfer Capacitance	C _{RSS}			4.1	8	
Total Gate Charge	Q _{G(TOT)}	V _{GS} =10V, V _{DS} =30V, I _D =0.2A		1.12	2	nC
Gate-to-Source Charge	Q _{GS}			0.1	0.2	
Gate-to-Drain Charge	Q _{GD}			0.23	0.5	
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{d(ON)}	V _{GS} =10V, V _{DS} =30V, I _D =0.2A, R _G =6Ω		3	6	ns
Rise Time	t _r			5	10	
Turn-Off Delay Time	t _{d(OFF)}			14	27	
Fall Time	t _f			9	17	
BODY DIODE CHARACTERISTICS						
Forward Voltage	V _{SD}	V _{GS} =0V, I _S =1A			1.5	V

7. Typical Characteristic

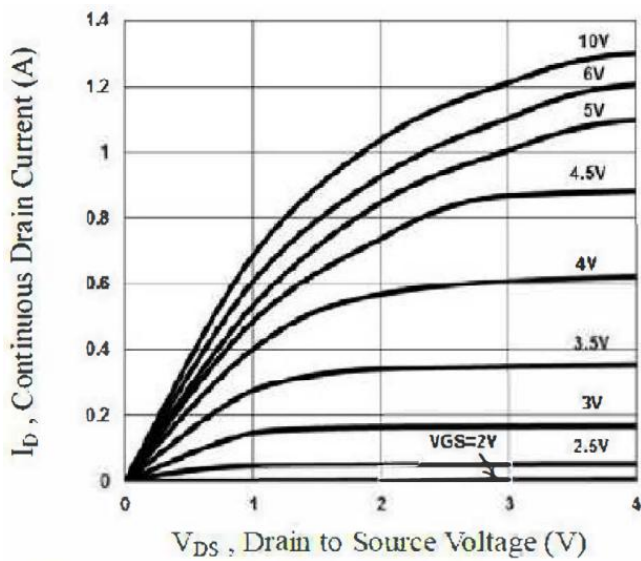


Fig.1 Output Characteristics

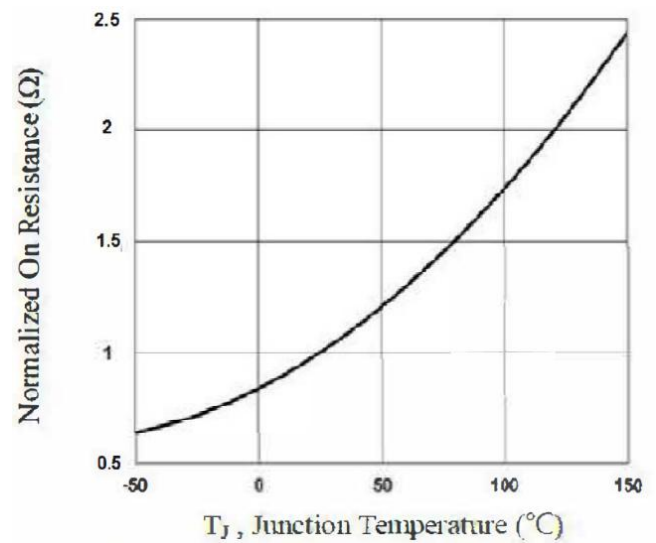


Fig.2 Normalized $R_{DS(on)}$ vs. T_J

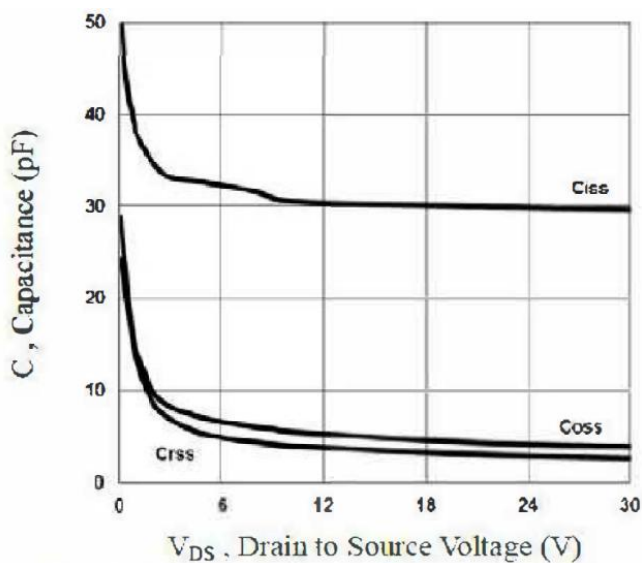
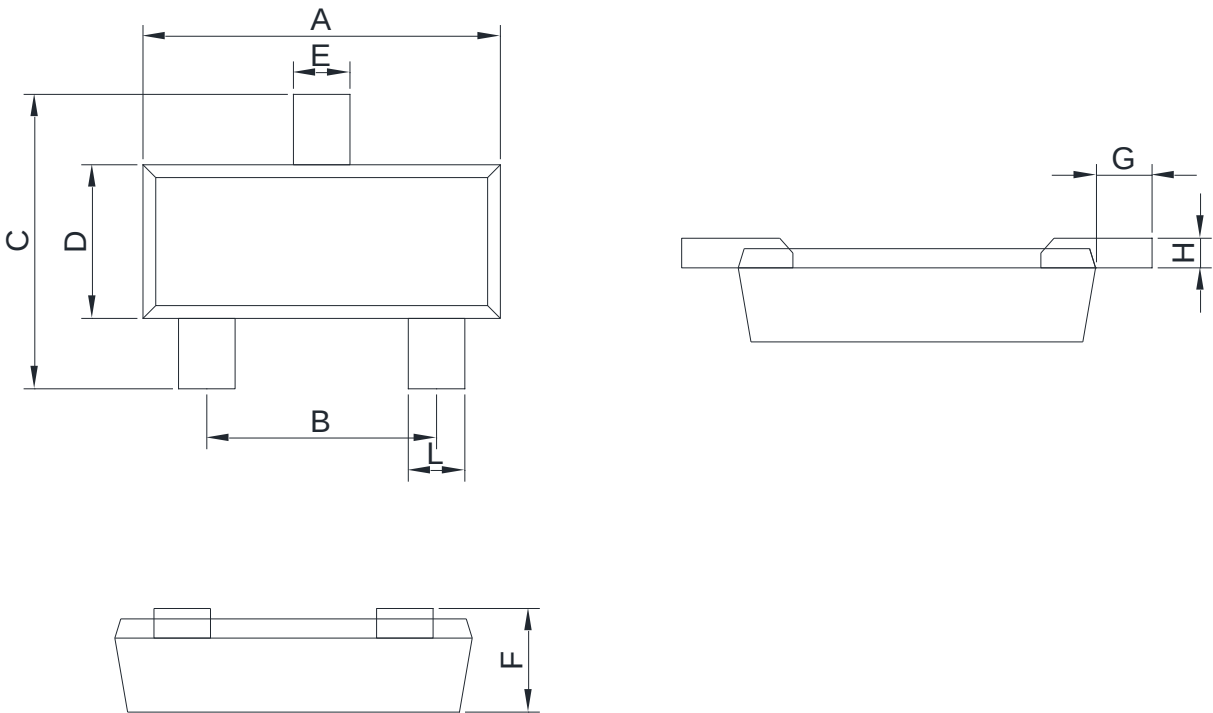


Fig.3 Capacitance Characteristics

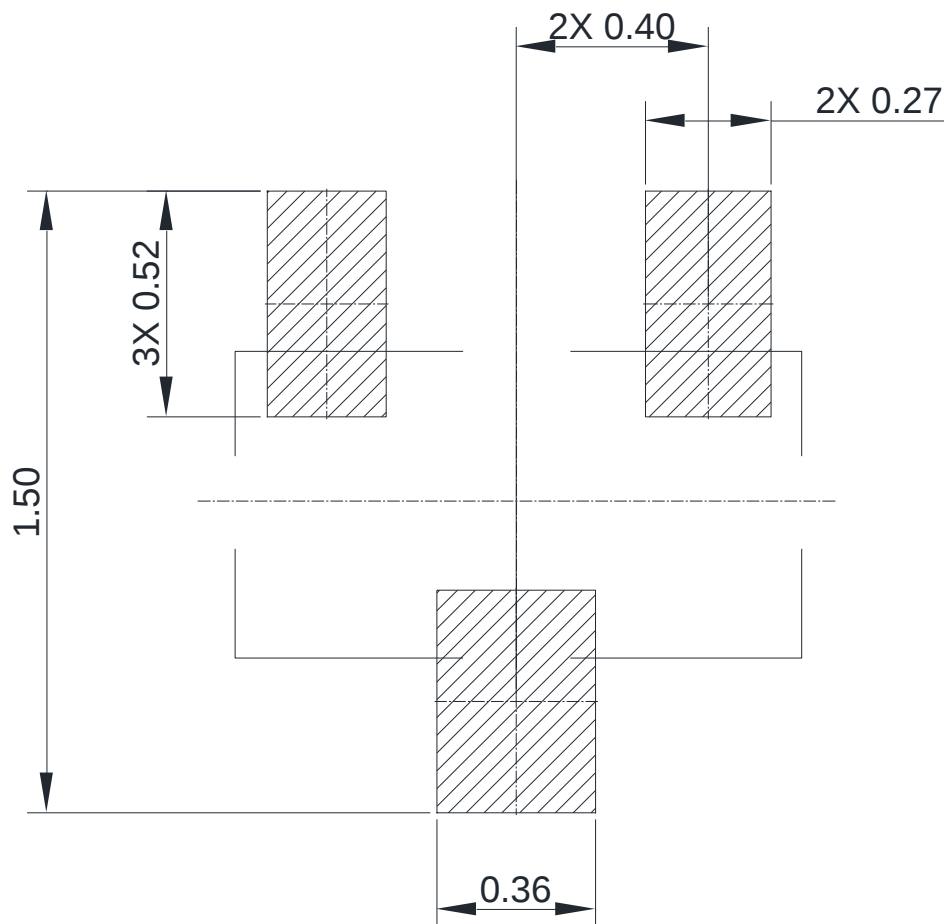
8. Dimension (SOT-723)

POD(Z)



Dimensions in Millimeters					
Symbol	Min.	Max.	Symbol	Min.	Max.
A	1.10	1.30	F	0.40	0.50
B	0.80TYP		G	0.10	0.30
C	1.10	1.30	H	0.06	0.16
D	0.70	0.90	L	0.15	0.25
E	0.15	0.35			

9. Recommended Soldering Footprint



DIMENSIONS: MILLIMETERS

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