

SuperMOS –SOT-723 60V BV_{DSS} , $1.8\Omega R_{DS(ON)}$, N-channel MOSFET

1. Description

The 2SK3541T2L-ES is N-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. Device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product 2SK3541T2L-ES is Pb-free.

2. Features

- 60V, $R_{DS(ON)}=1.8\Omega(\text{Typ.}) @V_{GS}=10V$
 $R_{DS(ON)}=2.0\Omega(\text{Typ.}) @V_{GS}=4.5V$
- High density cell design for low $R_{DS(on)}$
- ESD Protection - HBM Class: 2
- Material: Halogen free
- Reliable and rugged
- Avalanche Rated
- Low leakage current

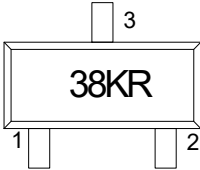
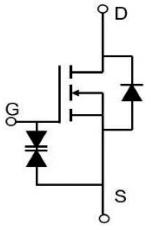
3. Applications

- PWM applications
- Load switch
- Power management in portable/desktop PCs
- DC/DC conversion

4. Ordering Information

Part Number	Package	Marking	Material	Packing	Quantity per Reel	Flammability Rating	Reel Sizes
2SK3541T2L-ES	SOT-723	38KR	Halogen free	Tape & Reel	8,000 PCS	UL 94V-0	7 inches

5. Pin Configuration and Functions

Pin	Function	Outline	Circuit Diagram
1	Gate		
2	Source		
3	Drain		

6. Specification

Absolute Maximum Rating & Thermal Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		BV_{DSS}	60	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	$T_A=25^{\circ}\text{C}$	I_D	0.3	A
	$T_A=100^{\circ}\text{C}$		0.2	
Maximum Power Dissipation		P_D	0.35	W
Pulsed Drain Current		I_{DM}	1.2	A
Operating Junction Temperature		T_J	150	°C
Lead Temperature		T_L	260	°C
Storage Temperature Range		T_{stg}	-55 to 150	°C

Thermal resistance ratings

Single Operation				
Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance ($t \leq 10\text{s}$)	$R_{\theta JA}$		357	°C/W

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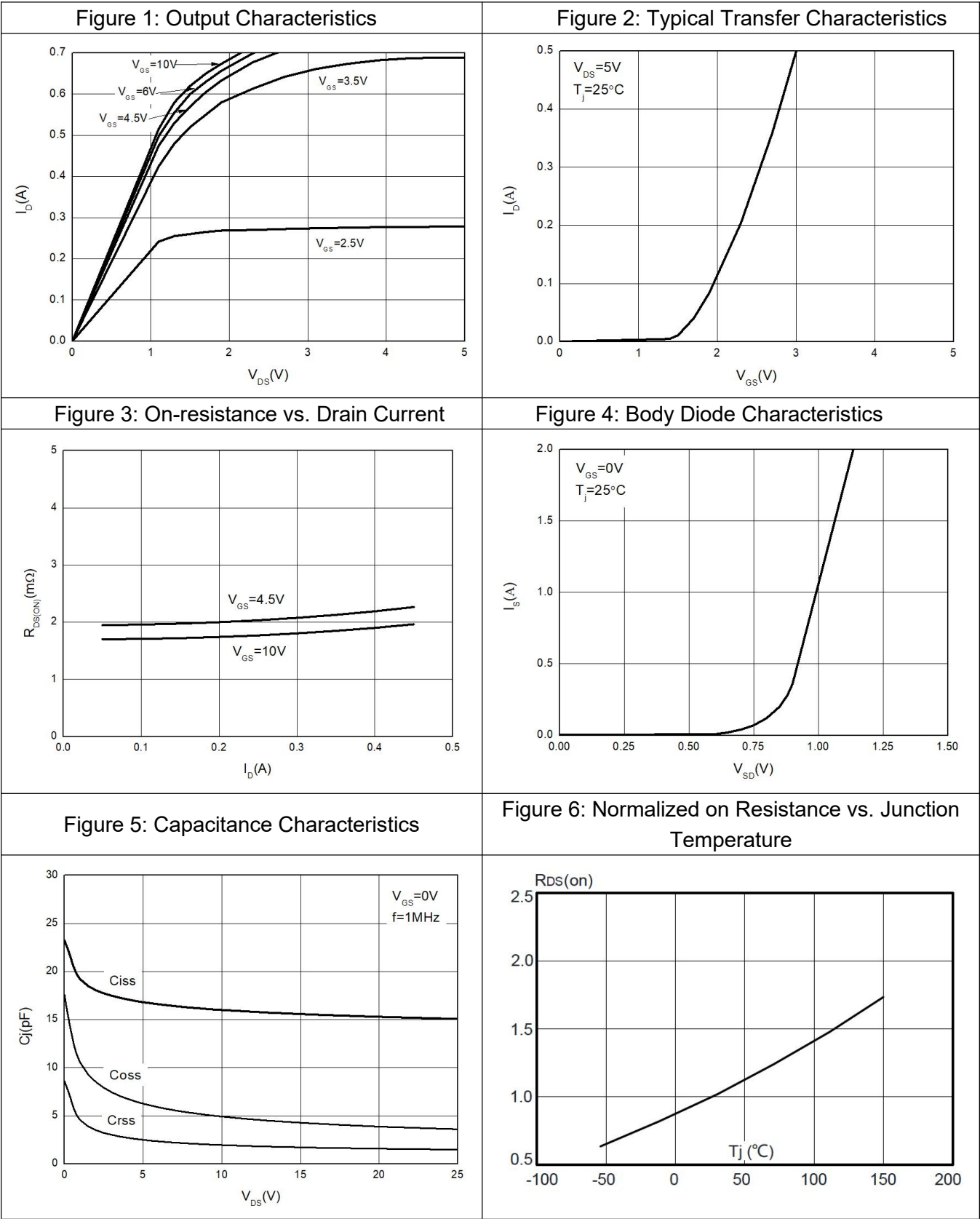
Rev-1.4

Electrical Characteristics

At TA = 25°C unless otherwise specified

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	60			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			1	uA
Gate-to-source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±10	uA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250uA	0.7	1.1	1.5	V
Drain-to-source On-resistance	R _{DS(on)}	V _{GS} =10V, I _D =0.3A		1.8	2.2	Ω
		V _{GS} =4.5V, I _D =0.2A		2.0	3.0	
CHARGES, CAPACITANCES AND GATE RESISTANCE						
Input Capacitance	C _{ISS}	V _{GS} =0V, f=1MHz, V _{DS} =25V		15		pF
Output Capacitance	C _{OSS}			3.6		
Reverse Transfer Capacitance	C _{RSS}			1.5		
Total Gate Charge	Q _{G(TOT)}	V _{GS} =4.5V, V _{DS} =10V, I _D =0.3A		1.6		nC
Gate-to-Source Charge	Q _{GS}			0.2		
Gate-to-Drain Charge	Q _{GD}			0.5		
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{d(ON)}	V _{GS} =10V, V _{DS} =10V, I _D =0.2A, R _G =10Ω		2		ns
Rise Time	t _r			14		
Turn-Off Delay Time	t _{d(OFF)}			6		
Fall Time	t _f			19		
BODY DIODE CHARACTERISTICS						
Forward Voltage	V _{SD}	V _{GS} =0V, I _S =0.3A			1.5	V

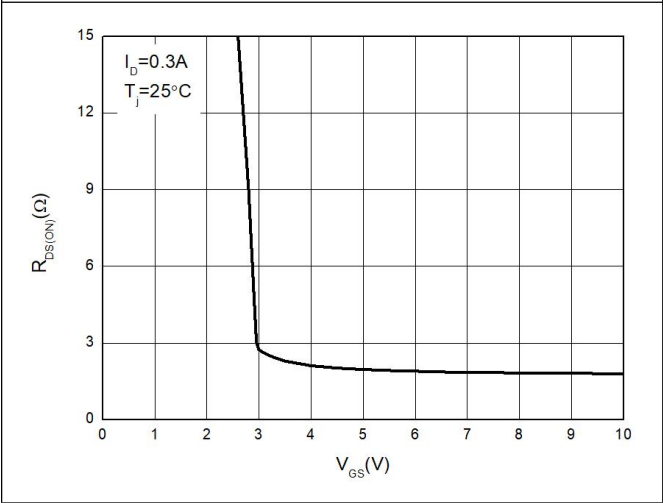
7. Typical Characteristics



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Figure7: on-Resistance vs. Gate to Source

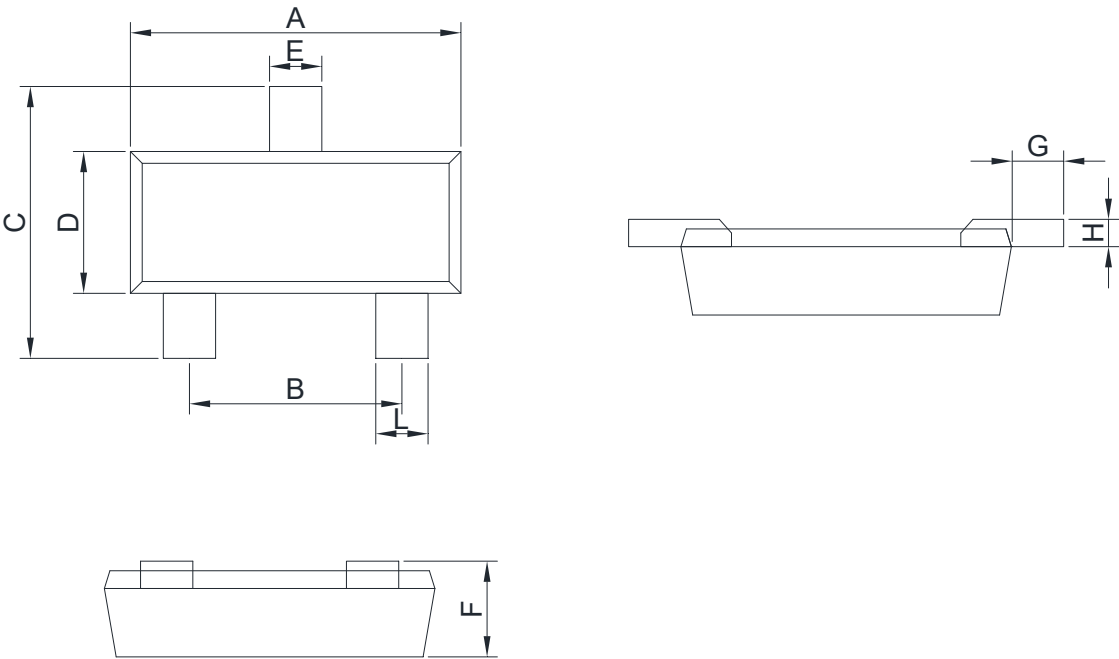


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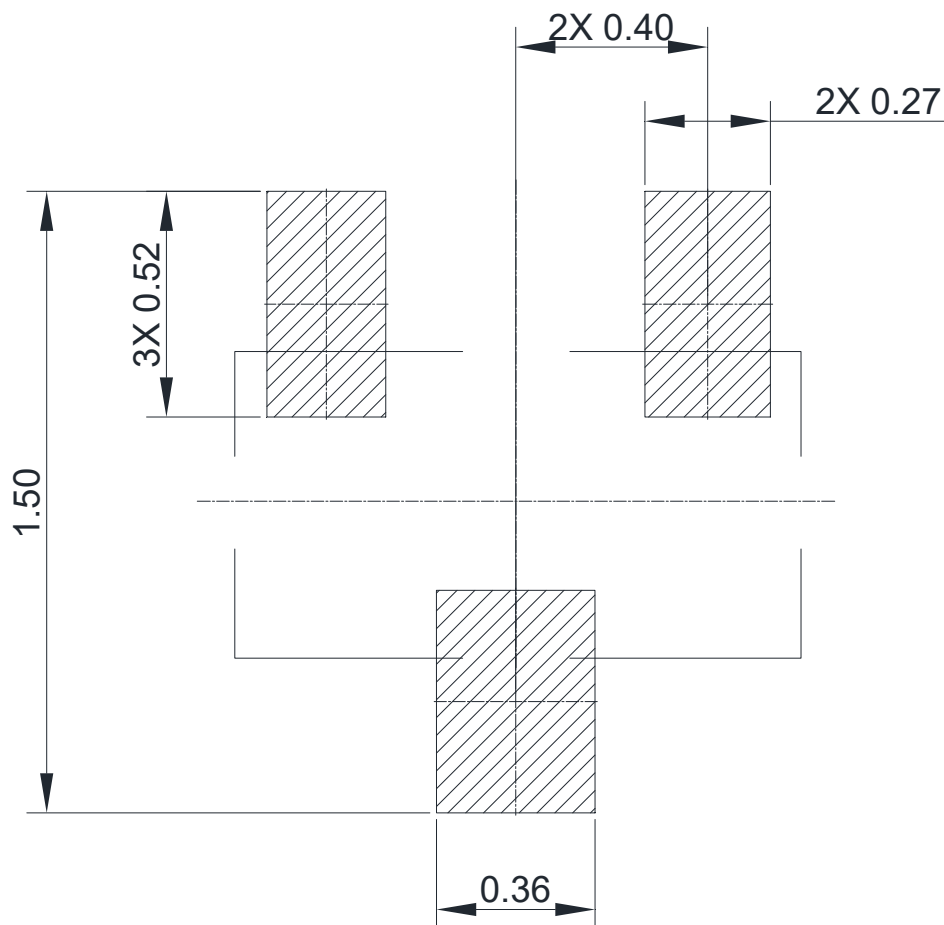
8. Dimension (SOT-723)

POD(Z)



Dimensions in Millimeters					
Symbol	Min.	Max.	Symbol	Min.	Max.
A	1.10	1.30	F	0.40	0.50
B	0.80TYP		G	0.10	0.30
C	1.10	1.30	H	0.06	0.16
D	0.70	0.90	L	0.15	0.25
E	0.15	0.35			

9. Recommended Soldering Footprint



DIMENSIONS: MILLIMETERS

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