

■ Features

- Short propagation delays for TTL and IPM applications
- Very high common mode transient immunity 15 kV/ μ s at VCM = 1500V
- High CTR: >25% at 25°C
- Guaranteed specifications for common IPM applications
- Guaranteed AC and DC performance over temperature: 0°C to 70°C
- Open collector output

■ Description

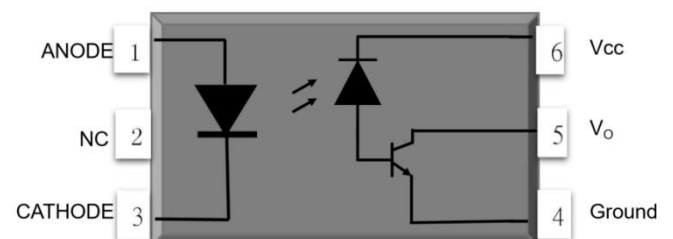
The MP454 CTR, propagation delays, and CMR are specified both for TTL load and drive conditions and for IPM (Intelligent Power Module) load and drive conditions. Specifications and typical performance plots for both TTL and IPM conditions are provided for ease of application.

This diode-transistor optocoupler uses an insulating layer between the light emitting diode and an integrated photo detector to provide electrical insulation between input and output. Separate connections for the photodiode bias and output transistor collector increase the speed up to a hundred times over that of a conventional phototransistor coupler by reducing the base-collector capacitance.

■ Applications

- Inverter circuits and intelligent power module (IPM)
- Interfacing – Shorter propagation delays and guaranteed specifications
- High-speed logic ground isolation -TTL/TTL,TTL/LTTL,TTL/CMOS,TTL/LSTTL
- Line receivers-High common mode transient immunity
- Replace pulse transformers-Save board space and weight
- Analog signal ground isolation -Integrated photo detector provides improved linearity over phototransistors

■ Schematic



ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT	Note
INPUT				
Forward Current	IF	25	mA	
Peak Input Current	IF	50	mA	1
Peak Transient Input Current	IF(trans)	1	A	2
Operating Frequency	f	10	kHz	
Reverse Voltage	VR	5	V	
Input Power Dissipation	PI	45	mW	
OUTPUT				
Supply Voltage	VCC	30	V	
Output Voltage	VO	20	V	
Peak Output Current	IO	16	mA	
Output Power Dissipation	PO	100	mW	
COMMON				
Isolation Voltage	Viso	5000	Vrms	3
Operating Temperature	Topr	-40~110	°C	
Storage Temperature	Tstg	-55~125	°C	
Soldering Temperature	Tsol	260	°C	4

Note 1. 50% duty cycle, 1ms pulse width

Note 2. ≤1ms P.W, 300pps

Note 3. AC For 1 Minute, R.H. = 40 ~ 60%

Note 4. For 10 seconds

TRUTH TABLE

LED	VO
On	Low
Off	High

RECOMMENDED OPERATION CONDITIONS

PARAMETER	SYMBOL	MIN	MAX	UNIT
Operating Temperature	TA	-55	100	°C
Supply Voltage	VCC	0	30	V
Input Current (ON)	IF(ON)	7	16	mA
Input Voltage (OFF)	VF(OFF)	-3.6	0.8	V

ELECTRICAL OPTICAL CHARACTERISTICS (VCC=30V, VEE=GND, TA=25°C unless specified otherwise)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
INPUT CHARACTERISTICS							
Forward Voltage	VF	-	1.3	1.7	V	IF=16mA	
Reverse Current	IR	-	-	10	μA	VR=5V	
Input Capacitance	Cin	-	60	-	pF	VF=0, f=1MHz	
OUTPUT CHARACTERISTICS							
High Level Supply Current	ICCH	-	0.01	1	μA	IF=0mA,VCC=15V,VO=Open	
Low Level Supply Current	ICCL	-	110	200		IF=16mA,VCC=15V,VO=Open	
TRANSFER CHARACTERISTICS							
Current Transfer Ratio	CTR	25	36	60	%	IF=16mA,VCC=5V,VO=0.4V	
		26	39	65		IF=16mA,VCC=5V,VO=0.5V	
Low Level Output Voltage	VOL	-	0.18	0.4	V	IF=16mA, VCC=4.5V,IO= 2.4mA	
		-	0.2	0.4		IF=16mA, VCC=4.5V,IO= 3.0mA	
High Level Output Current	IOH	-	0.003	0.5	μA	IF=0mA,VO= VCC=5.5V	
		-	0.01	1		IF=0mA,VO= VCC=15V	
Input Threshold Current	ITH	-	1.8	4.0	mA	VCC=15V,IO=0mA,VO>5V	
Isolation Resistance	Riso	10^9	10^12	-	Ω	DC500V, 40 ~ 60% R.H.	



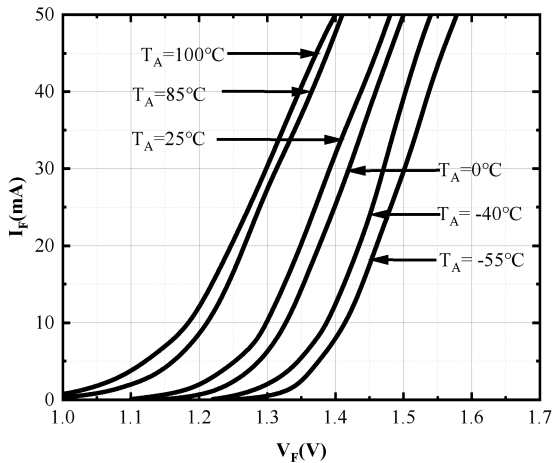
MP454 Series

LSOP6, IPM Interface

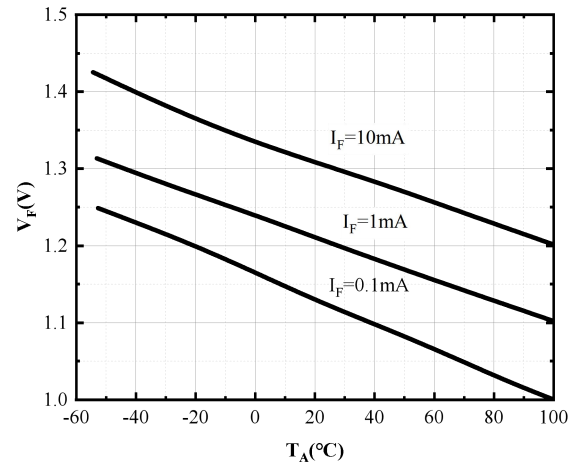
ELECTRICAL OPTICAL CHARACTERISTICS (VCC=30V, VEE=GND, TA=25°C unless specified otherwise)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
SWITCHING CHARACTERISTICS							
Propagation Delay Time to Output Low Level	TPHL	-	0.24	0.7	μ s	IF=12mA, VCC=15V, CL=100pF, RL=20K Ω , f=10kHz, Duty=50%	
Propagation Delay Time to Output High Level	TPLH	-	0.6	1.1			
Pulse Width Distortion	PDD	-	0.3	0.9			
Common Mode Transient Immunity at Logic High	CMH	15	30	-	kV/ μ s	IF=0mA, VCC=15V, RL=20K Ω CL=100pF, VCM= 1.5KVp-p	
Common Mode Transient Immunity at Logic Low	CML	15	30	-	kV/ μ s	IF=16mA, VCC=30V, RL=20K Ω CL=100pF, VCM= 1.5KVp-p	

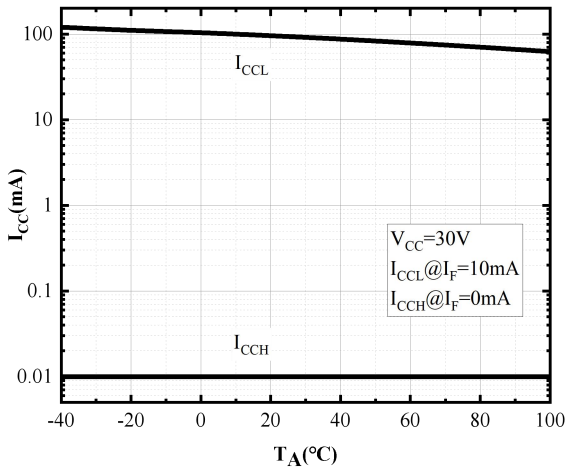
CHARACTERISTIC CURVES



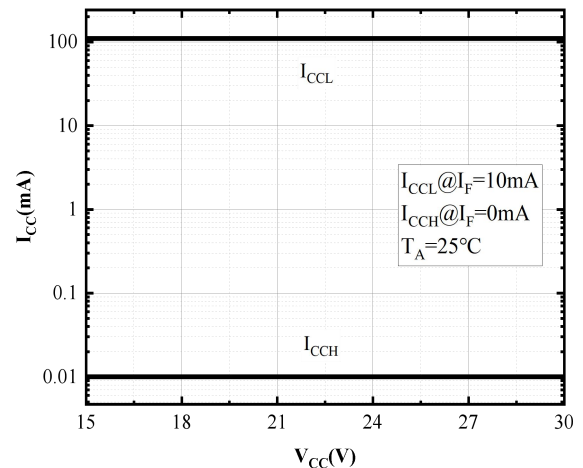
**Fig.1 Forward Current
vs. Forward Voltage**



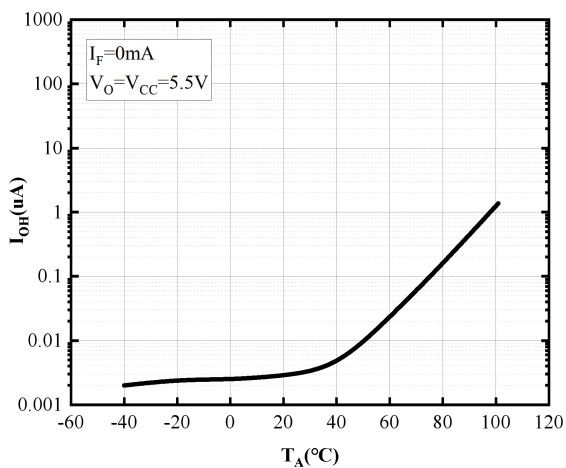
**Fig.2 Forward Voltage
vs. Ambient Temperature**



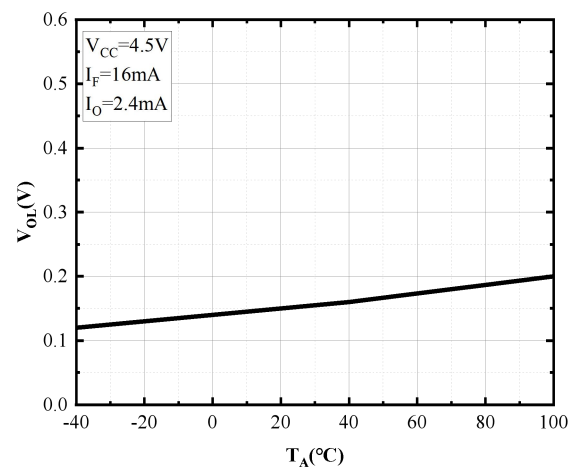
**Fig.3 Supply Current
vs. Ambient Temperature**



**Fig.4 Supply Current
vs. Supply Voltage**

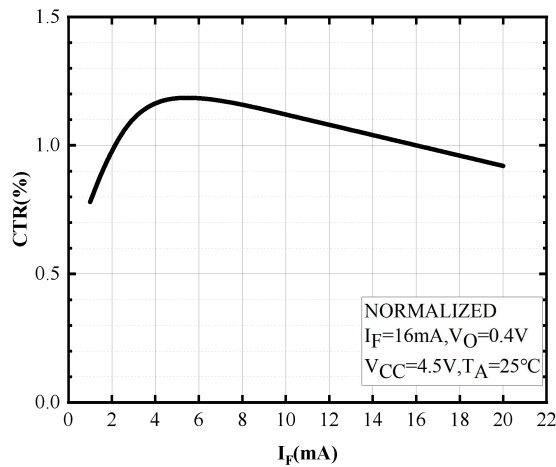


**Fig.5 High Level Output Current
vs. Ambient Temperature**

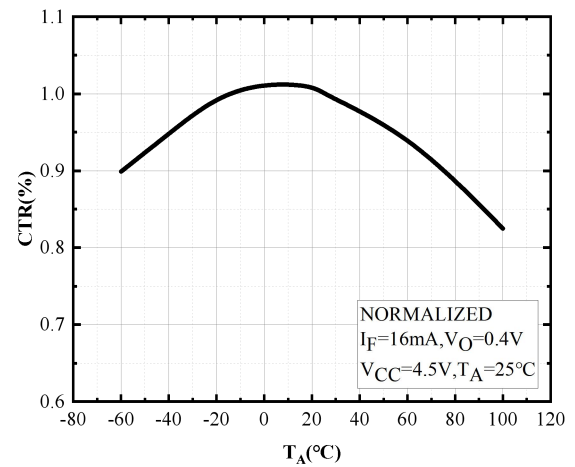


**Fig.6 Low Level Output Voltage
vs. Ambient Temperature**

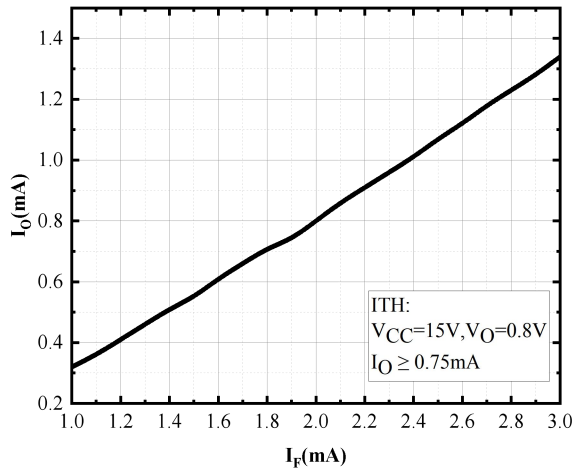
CHARACTERISTIC CURVES



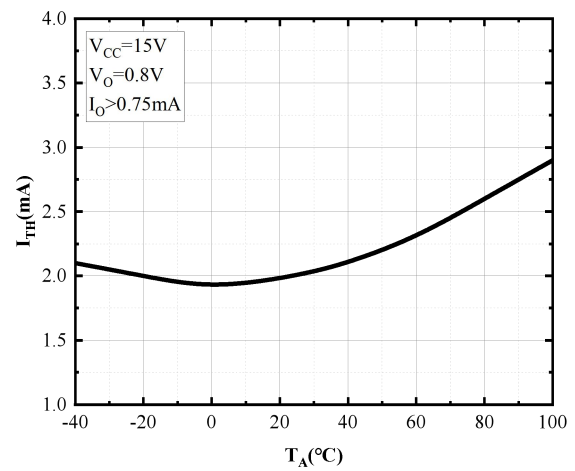
**Fig.7 Current Transfer Ratio
vs. Input Current**



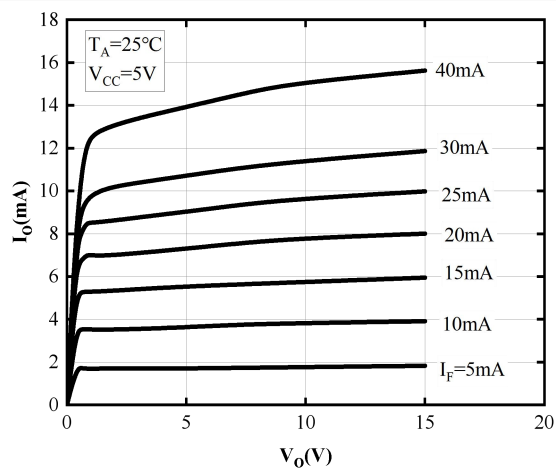
**Fig.8 Current Transfer Ratio
vs. Ambient Temperature**



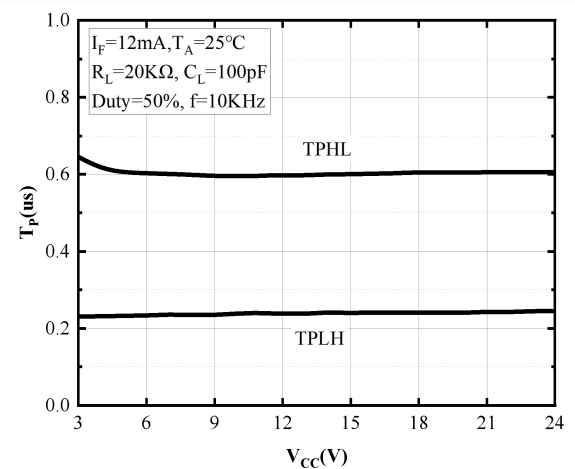
**Fig.9 Output Current
vs. Input Current**



**Fig.10 Input Threshold Current
vs. Ambient Temperature**

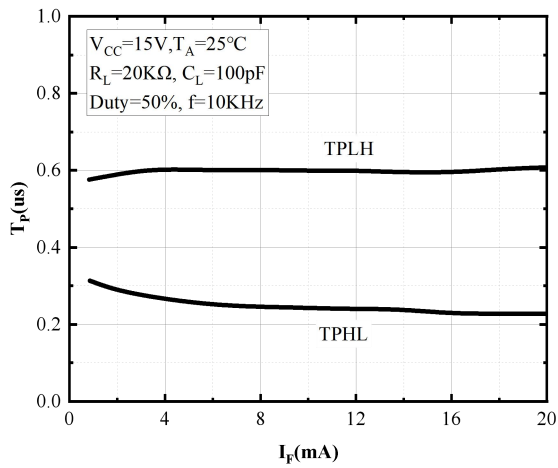


**Fig.11 Output Current
vs. Output Voltage**

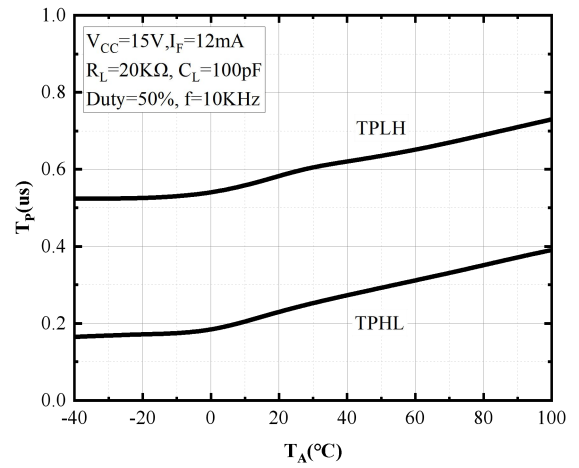


**Fig.12 Propagation Delay
vs. Supply Voltage**

CHARACTERISTIC CURVES



**Fig.13 Propagation Delay
vs. Forward Current**



**Fig.14 Propagation Delay
vs. Ambient Temperature**

TEST CIRCUITS

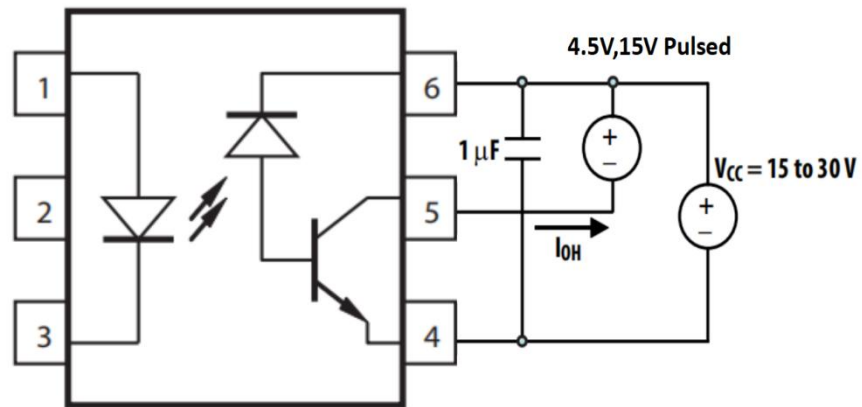


Fig.15 Test Circuits for IOH

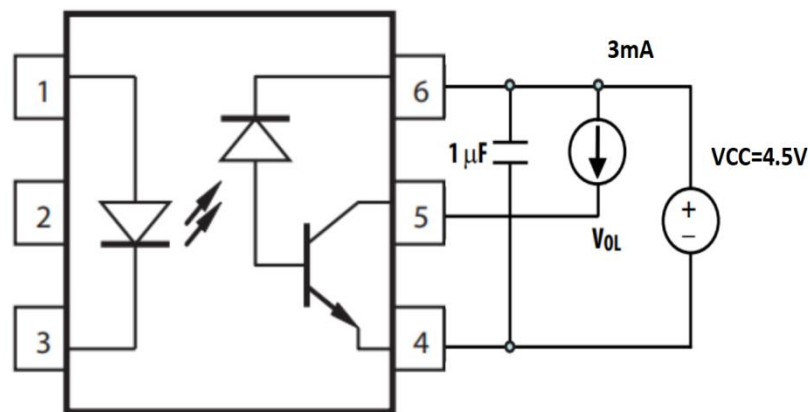


Fig.16 Test Circuits for VOL

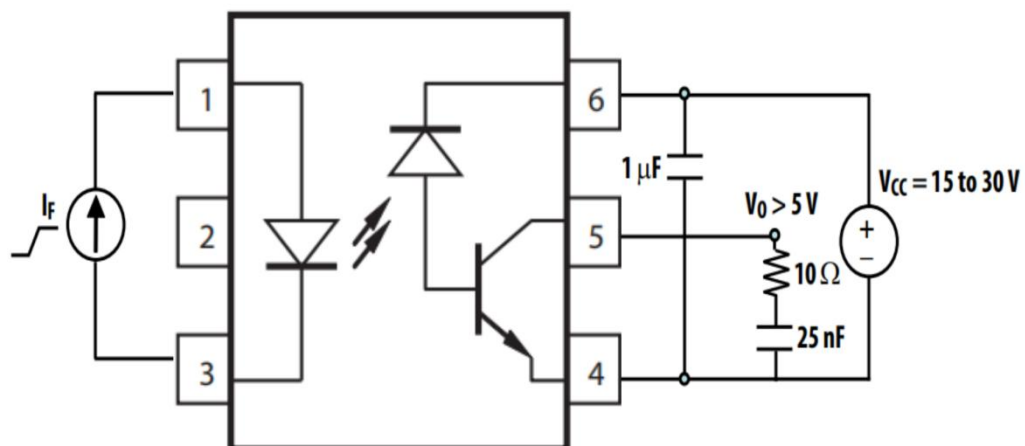


Fig.17 Test Circuits for ITH

TEST CIRCUITS

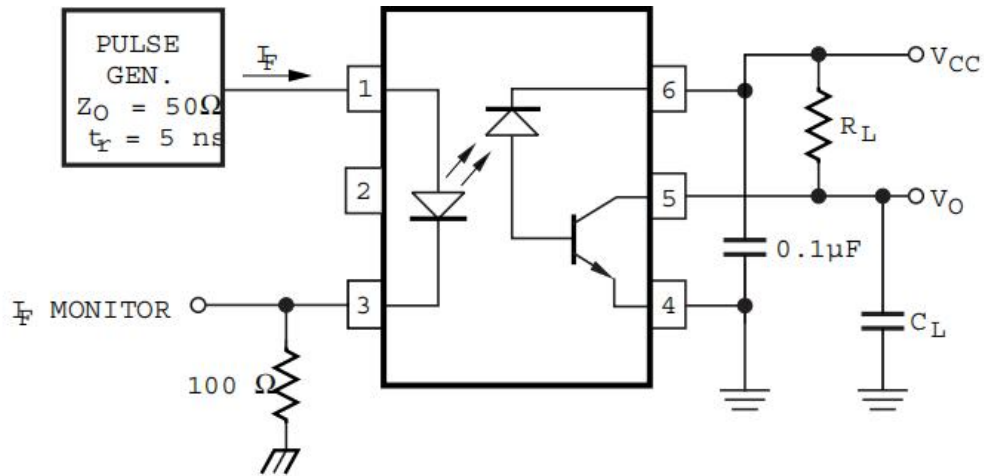


Fig.18 Test Circuits for TPHL, TPLH, tr, tf

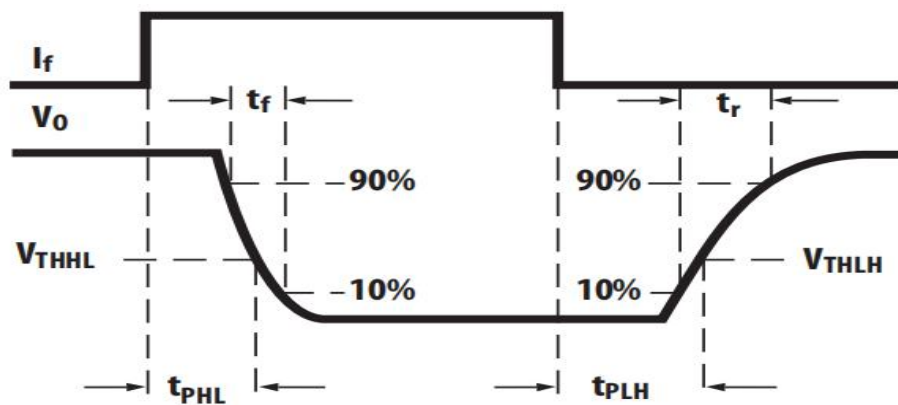


Fig.19 Wave forms of TPHL, TPLH, tr, tf

TEST CIRCUITS

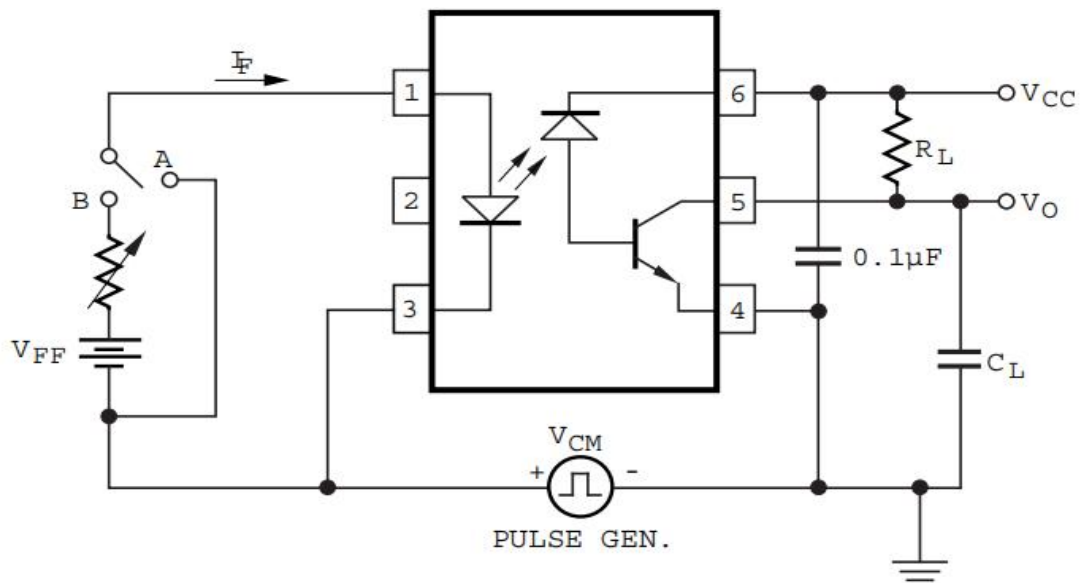


Fig.20 Test Circuits for Common Mode Transient Immunity

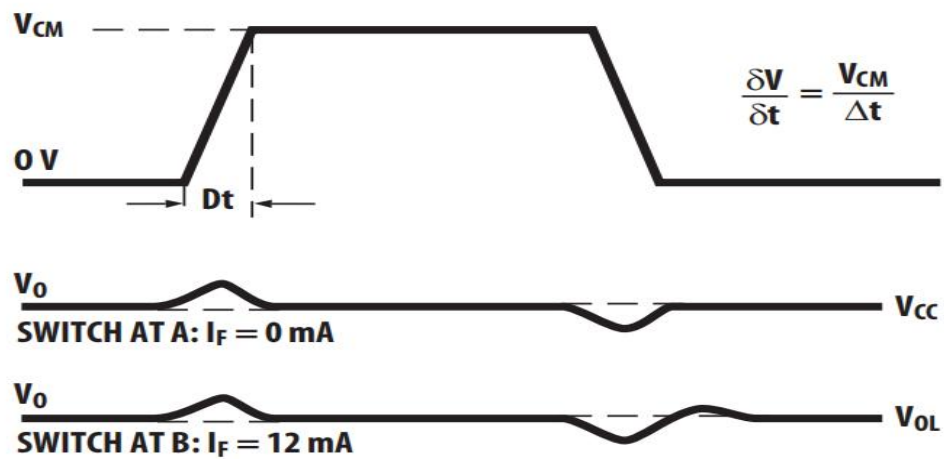
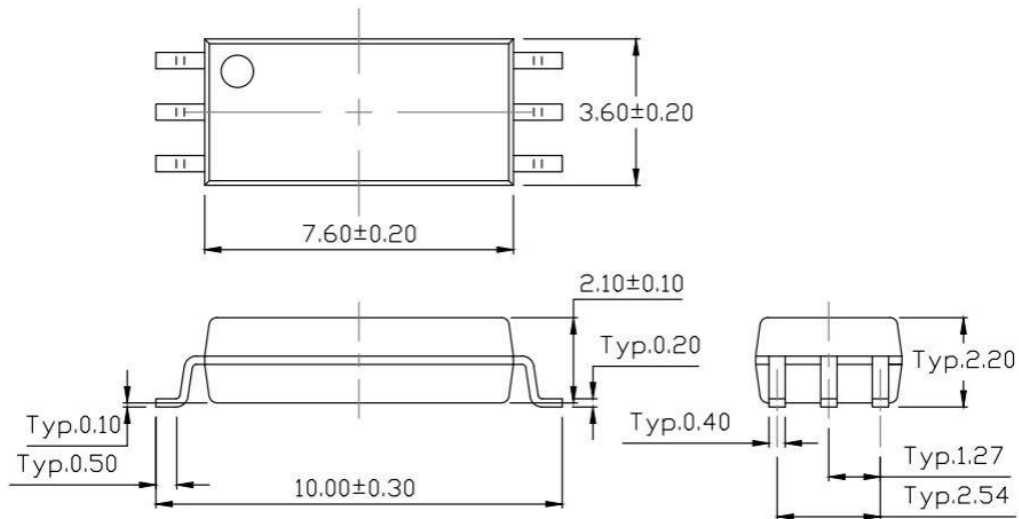


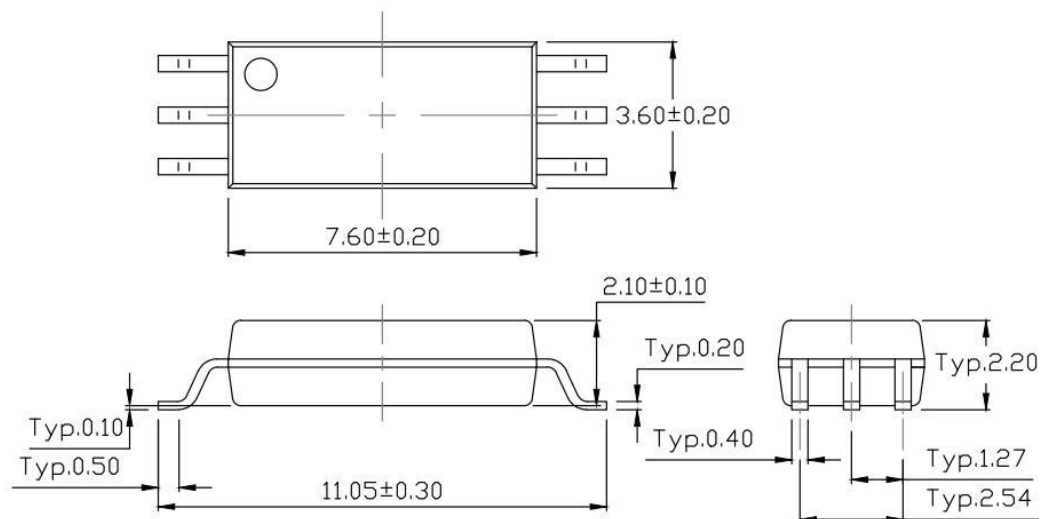
Fig.21 Waveforms of Common Mode Transient Immunity

PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated)

Standard P Type

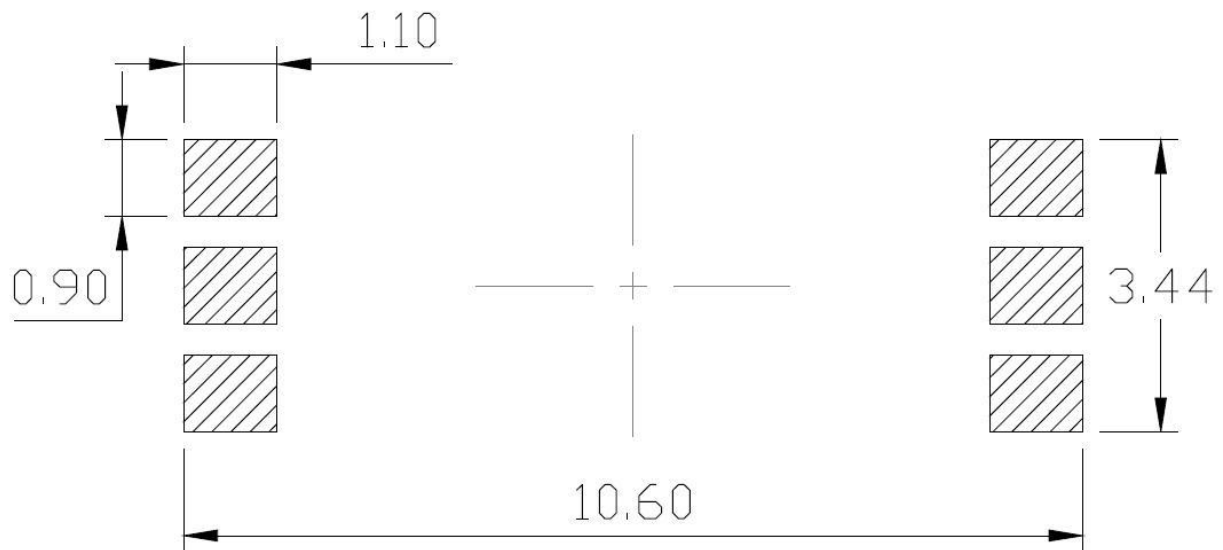


Standard W Type

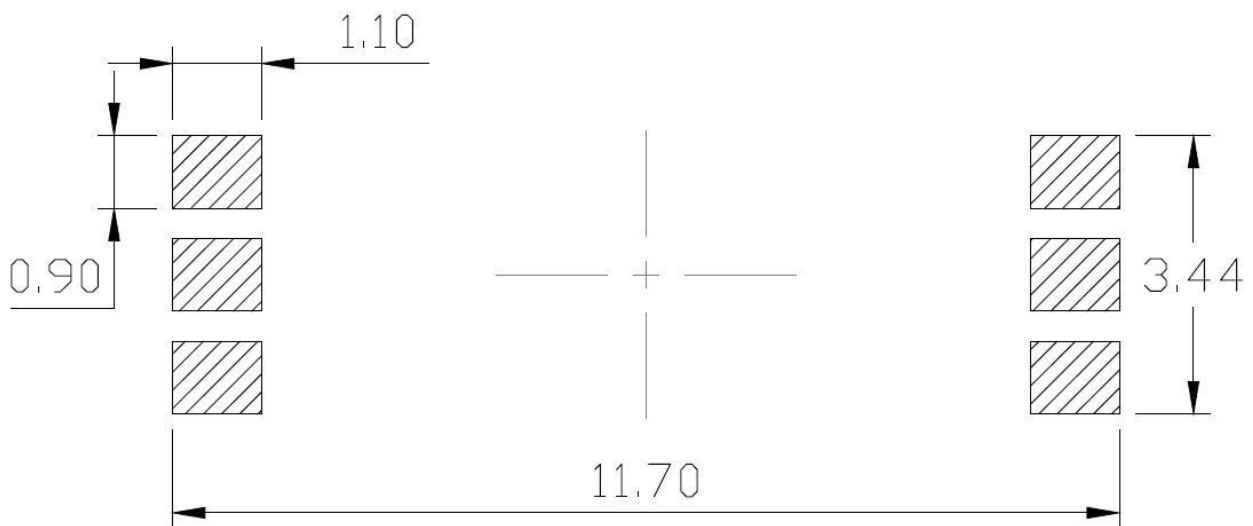


Recommended Solder Mask (Dimensions in mm unless otherwise stated)

Standard P Type

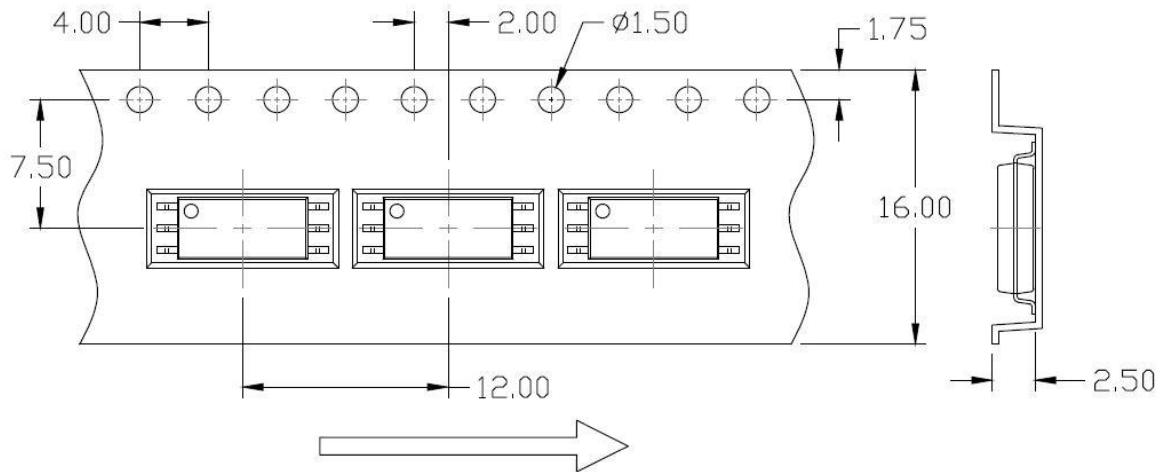


Standard W Type

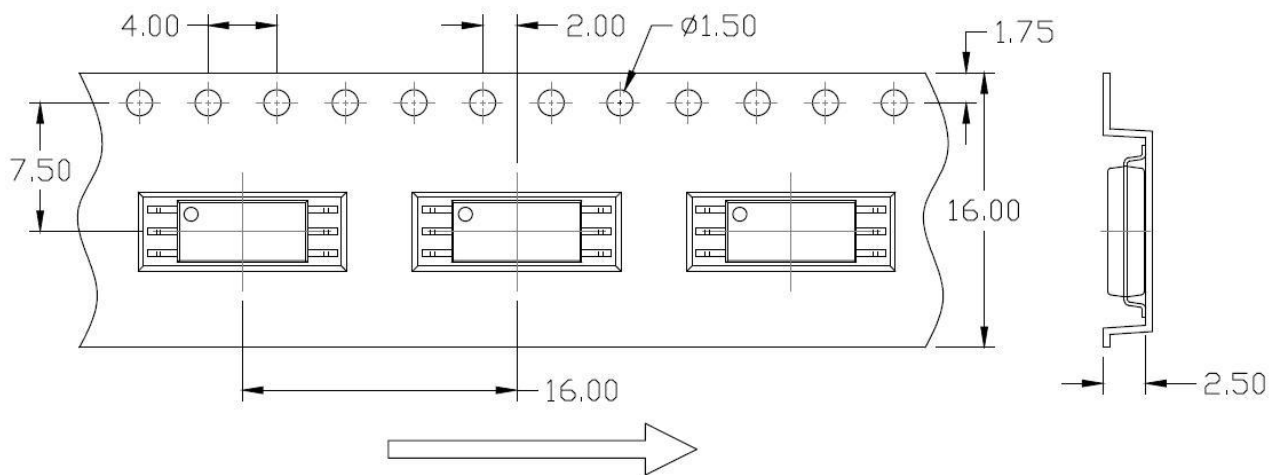


CARRIER TAPE SPECIFICATIONS (Dimensions in mm unless otherwise stated)

Option P(T3)

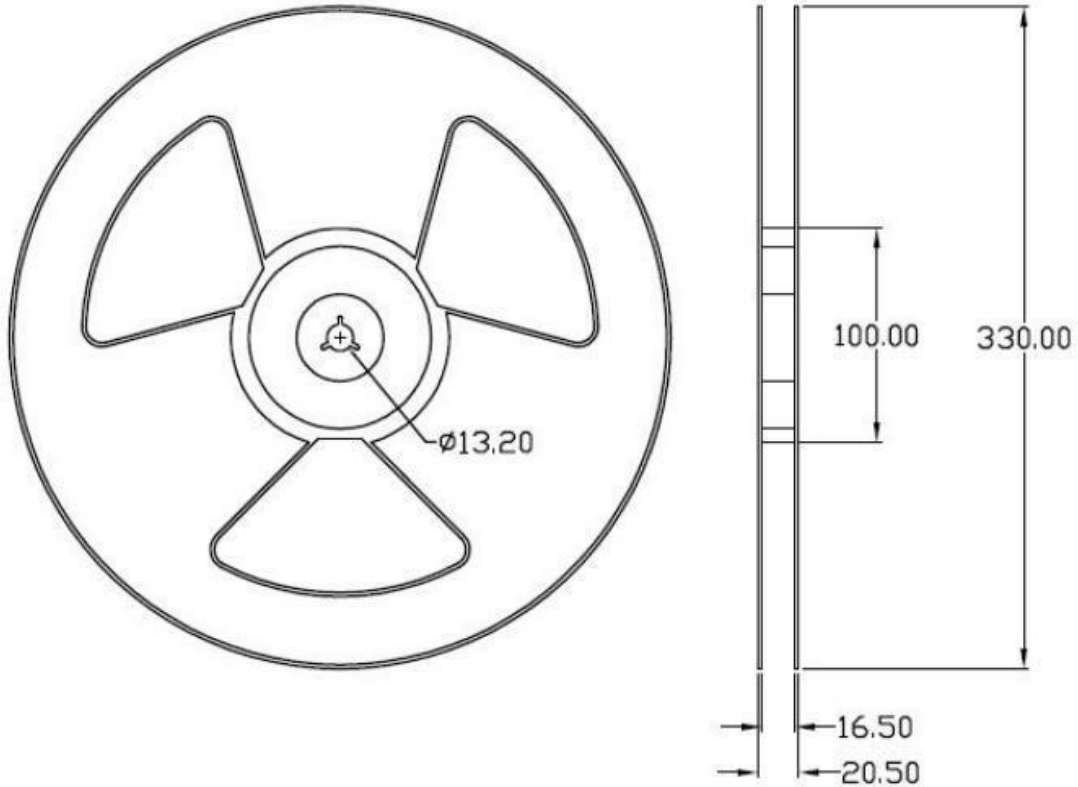


Option W (T3)



REEL SPECIFICATIONS (Dimensions in mm unless otherwise stated)

Option



ORDERING AND MARKING INFORMATION

MARKING INFORMATION



MP	: Company Abbr.
454	: Part Number
V	: VDE Option (V or None)
N	:Customer Specified Option(1~9 or None)
Y	: Fiscal Year
A	: Manufacturing Code
WW	: Work Week

ORDERING INFORMATION

MP454(Y)(Z)-GV(XN)

MP -Company Abbr.

454 -Part Number.

(Y) -Lead Form Option (P/W).

(Z) -Tape and Reel Option (T3).

G -Material Option (G: Green, None: Non-Green).

V -VDE Option (V or None).

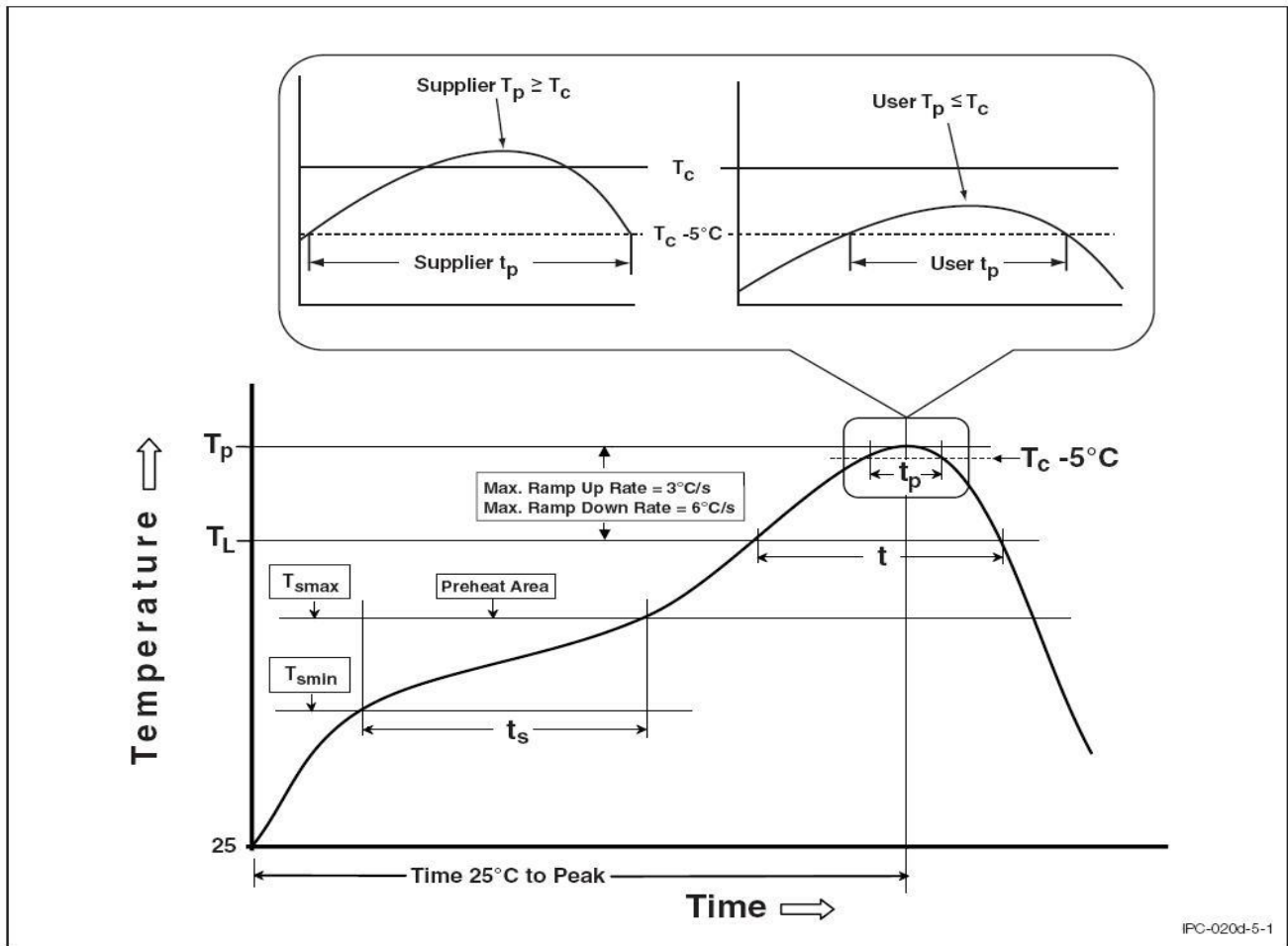
(XN)-Customer Specified Option (X: A~Z or None; N:1~9 or None)

PACKING QUANTITY

Option	Quantity	Quantity – Inner box	Quantity – Outer box
P(T3)	1500 Units/Reel	3 Reels/Inner box	5 Inner box/Outer box = 22.5k Units
W(T3)	1500 Units/Reel	3 Reels/Inner box	5 Inner box/Outer box = 22.5k Units

REFLOW INFORMATION

REFLOW PROFILE



Profile Feature	Sn-Pb Assembly Profile	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	100	150°C
Temperature Max. (Tsmax)	150	200°C
Time (ts) from (Tsmin to Tsmax)	60-120 seconds	60-120 seconds
Ramp-up Rate (tL to tP)	3°C/second max.	3°C/second max.
Liquidous Temperature (TL)	183°C	217°C
Time (tL) Maintained Above (TL)	60 – 150 seconds	60 – 150 seconds
Peak Body Package Temperature	235°C +0°C / -5°C	260°C +0°C / -5°C
Time (tP) within 5°C of 260°C	20 seconds	30 seconds
Ramp-down Rate (TP to TL)	6°C/second max	6°C/second max
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

DISCLAIMER

- Our company is continually improving the quality, reliability, function and design. Our company reserves the right to make changes without further notices.
- The characteristic curves shown in this datasheet are representing typical performance which are not guaranteed.
- This product is not intended to be used for military, aircraft, automotive, medical, life sustaining or lifesaving applications or any other application which can result in human injury or death.
- Immerge unit's body in solder paste is not recommended.
- Discoloration might be occurred on the package surface after soldering, reflow or long-time use. It neither impacts the performance nor reliability.

■ Revision History

Version	Date	Subjects (major changes since last revision)
0.1	2024-05-09	Datasheet Complete