

Features ➤ Split Gate Trench MOSFET technology ➤ Excellent package for heat dissipation ➤ High density cell design for low $R_{DS(ON)}$	<i>Bvdss</i>	<i>Rdson</i>	<i>ID</i>
	100V	4.1mΩ	120A
	Application ➤ DC-DC Converters ➤ Power management function ➤ Synchronous-rectification applications		



Package			
1. Marking and pin assignment	2. TO-220 top view	3. Schematic diagram	

Package Marking and Ordering Information

Device Marking	Device	Device Package	Quantity
120N10T	S120N10T	TO220	50

Absolute Maximum Ratings (T_C=25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V _{DS}	100	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Current (1)	T _C = 25°C	I _D	120	A
	T _C = 100°C	I _D	81	A
Pulsed Drain Current (2)		I _{DM}	512	A
Single Pulsed Avalanche Energy (3)		E _{AS}	486	mJ
Power Dissipation(4)	T _C = 25°C	P _d	178	W
	T _C = 100°C		71	W
Junction Temperature		T _J	-55~+150	°C
Storage Temperature		T _{STG}	-55~+150	°C

Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-ambient	R _{θJA}	46	°C/W
Thermal Resistance Junction-Case	R _{θJC}	0.7	°C/W



Ordering Information

Ordering Number	Package	Pin Assignment			Packing
Halogen Free		G	D	S	
HLS120N10T	TO220	1	2	3	Tube

Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Drain to Source Breakdown Voltage	$V_{(br)dss}$	$I_d = 250\mu\text{A}$, $V_{gs} = 0\text{V}$	100	-	-	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS} = 80\text{V}$, $V_{GS} = 0\text{V}$	-	-	1.0	μA
		$V_{DS} = 80\text{V}$, $V_{GS} = 0\text{V}$ $T_J = 55^{\circ}\text{C}$	-	-	5.0	μA
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}$, $V_{DS} = 0\text{V}$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS} = V_{DS}$, $I_D = 250\mu\text{A}$	2	3	4	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{V}$, $I_D = 20\text{A}$	-	4.1	5	m Ω
Forward Transconductance	g_{fs}	$V_{DS} = 5\text{V}$, $I_D = 20\text{A}$	-	35	-	S
Diode Forward Voltage	V_{SD}	$I_S = 1\text{A}$, $V_{GS} = 0\text{V}$	-	0.66	1.0	V
Diode Continuous Current	I_S	$T_C = 25^{\circ}\text{C}$	-	--	120	A
Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}$, $V_{DS} = 50\text{V}$, $f = 1\text{MHz}$	-	4102	-	pF
Output Capacitance	C_{oss}		-	592	-	pF
Reverse Transfer Capacitance	C_{rss}		-	19.8	-	pF
Gate Resistance	R_g	$V_{GS} = 0\text{V}$, $V_{DS} = 0\text{V}$, $f = 1\text{MHz}$	-	1.6	-	Ω
Turn-ON Delay Time	$t_{d(on)}$	$V_{GS} = 10\text{V}$, $V_{DS} = 50\text{V}$ $R_L = 2.5$, $R_{GEN} = 3\Omega$	-	69	-	ns
Rise Time	t_r		-	44	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	24	-	ns
Fall Time	t_f		-	18.5	-	ns
Total Gate Charge (@ $V_{GS} = 10\text{V}$)	Q_g	$V_{GS} = 0 \text{ to } 10\text{V}$ $V_{DS} = 50\text{V}$, $I_D = 20\text{A}$	-	69	-	nC
Total Gate Charge (@ $V_{GS} = 6.0\text{V}$)	Q_g		-	44	-	nC
Gate Source Charge	Q_{gs}		-	24	-	nC
Gate Drain Charge	Q_{gd}		-	18.5	-	nC
Body Diode Reverse Recovery Time	T_{rr}	$I_F = 20\text{A}$, $dI_F/dt = 100\text{A/s}$	-	64	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	126	-	nC

Notes:

1. Computed continuous current assumes the condition of T_{J_Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under $T_{J_Max} = 150^{\circ}C$.
3. EAS of 486 mJ is based on starting $T_J = 25^{\circ}C$, $L = 3.0mH$, $I_{AS} = 18A$, $V_{GS} = 10V$, $V_{DD} = 50V$; 100% test at $L = 0.1mH$, $I_{AS} = 67A$.
4. The power dissipation P_D is based on $T_{J_Max} = 150^{\circ}C$.

Typical Characteristics

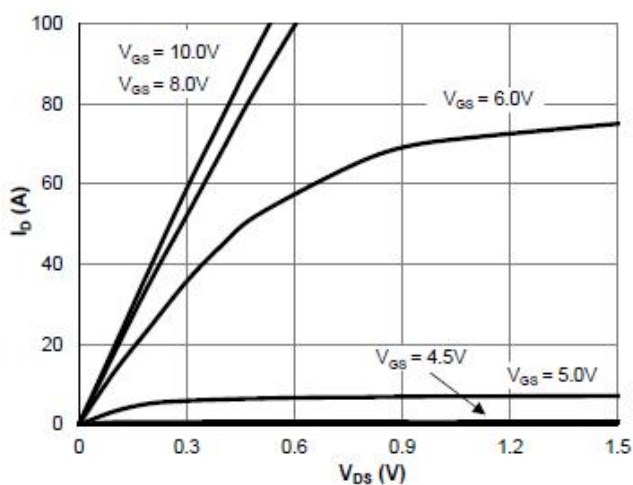


Figure 1: Saturation Characteristics

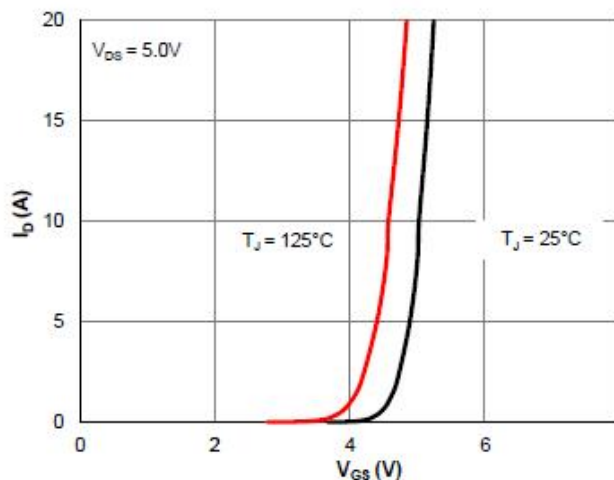


Figure 2: Transfer Characteristics

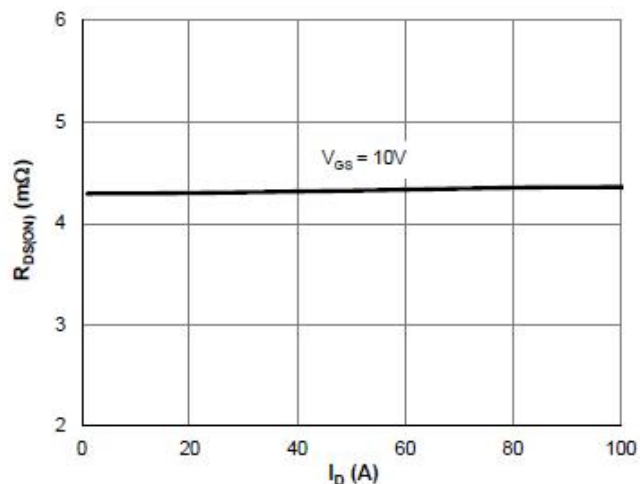


Figure 3: $R_{DS(on)}$ vs. Drain Current

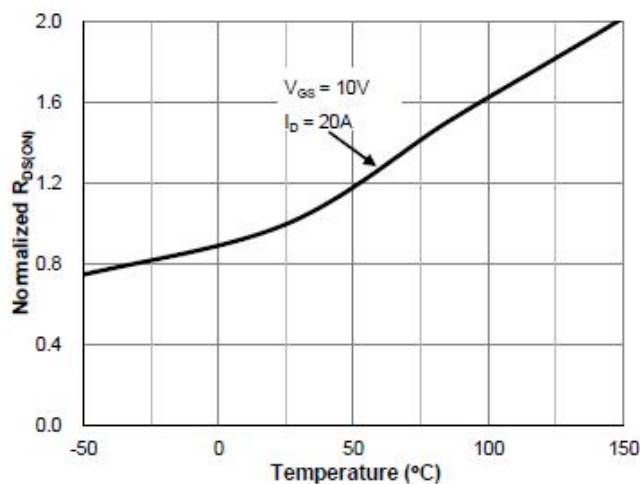


Figure 4: $R_{DS(on)}$ vs. Junction Temperature

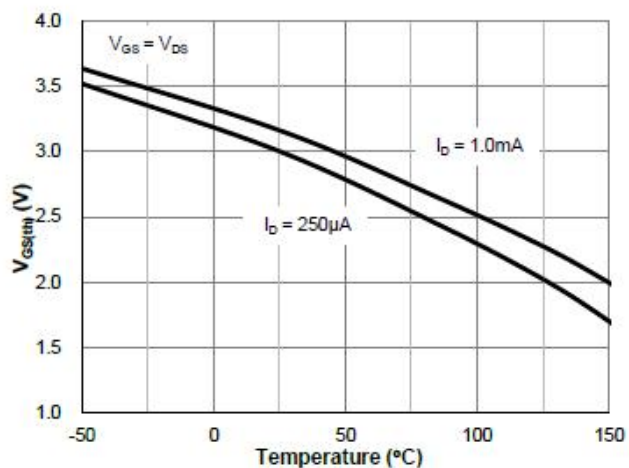


Figure 5: $V_{GS(th)}$ vs. Junction Temperature

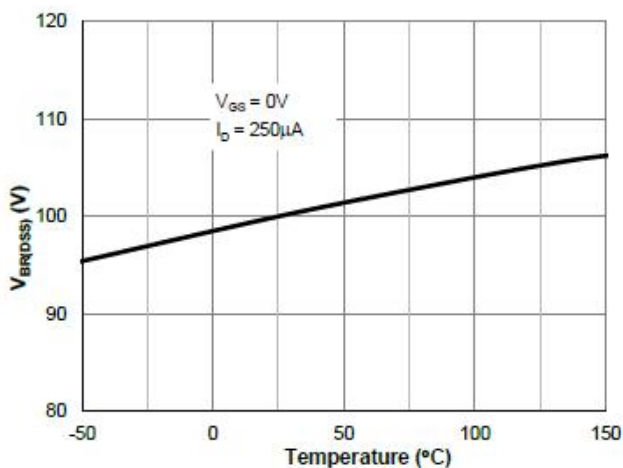


Figure 6: $V_{BR(DSS)}$ vs. Junction Temperature

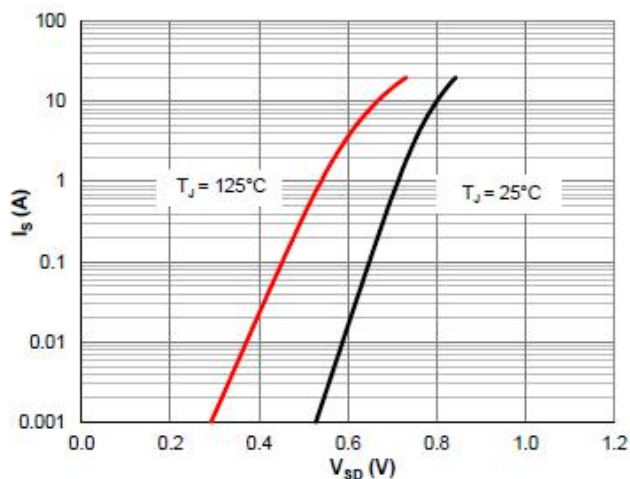


Figure 7: Body-Diode Characteristics

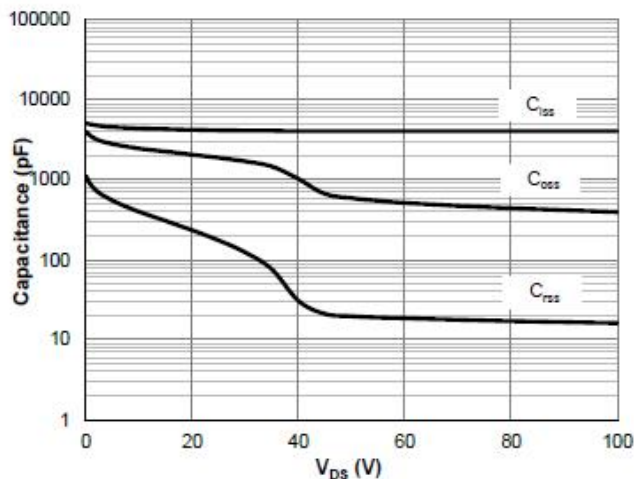


Figure 8: Capacitance Characteristics

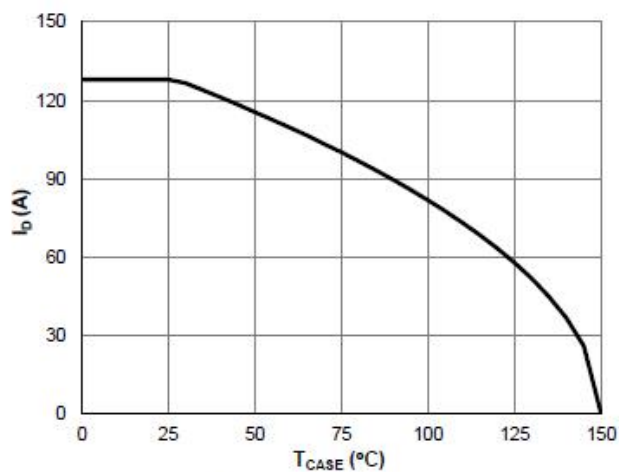


Figure 9: Current De-rating

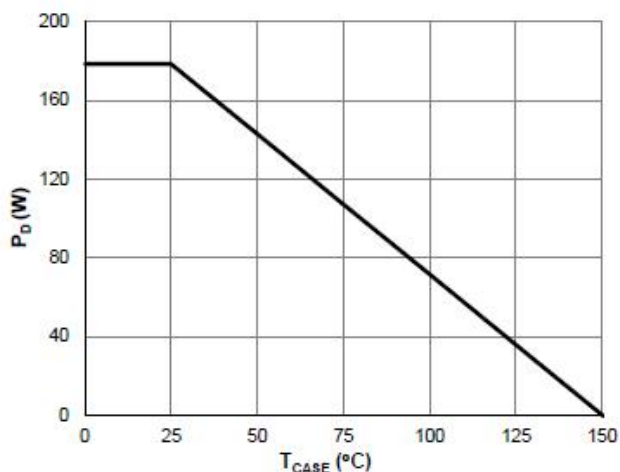
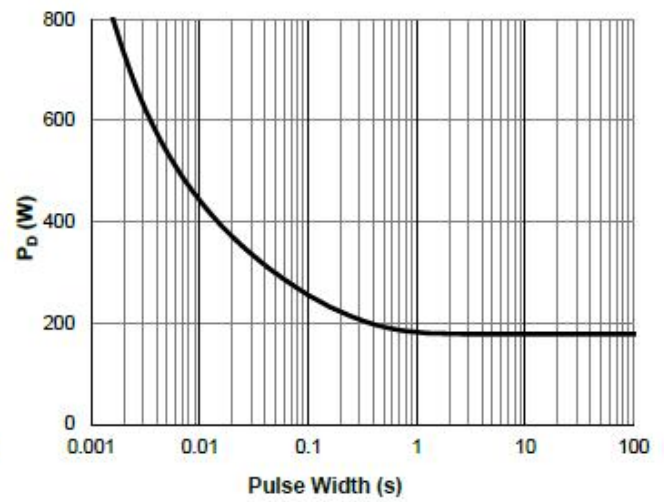
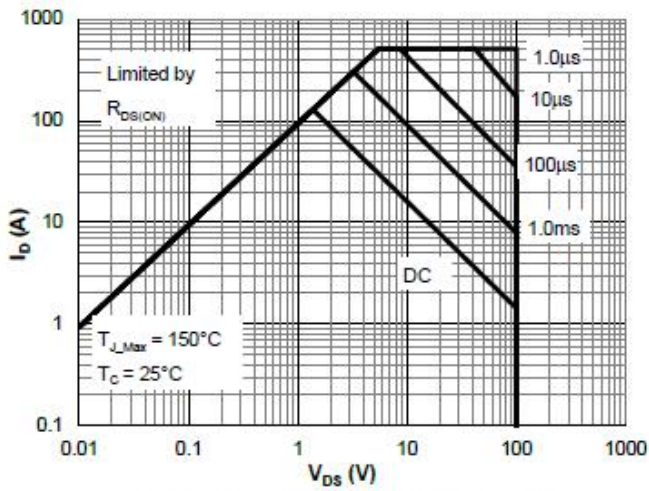
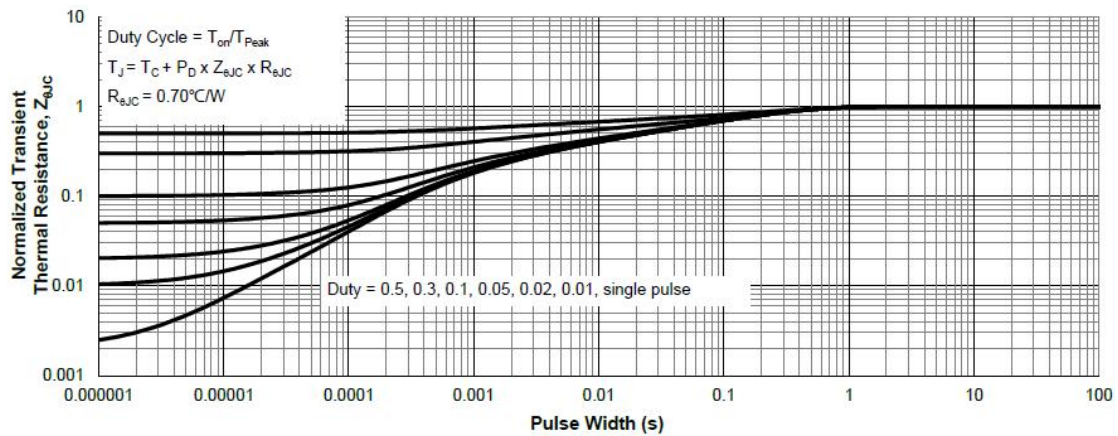


Figure 10: Power De-rating



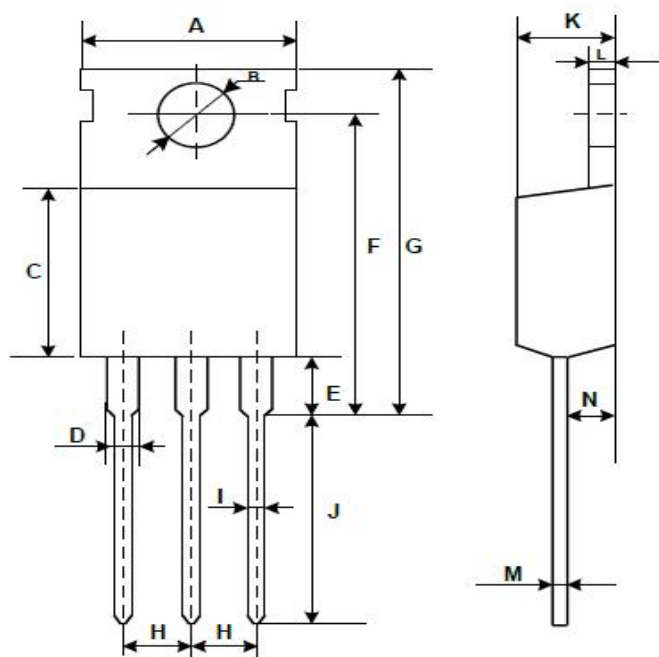
Typical Electrical & Thermal Characteristics





Package Dimensions TO-220

COMMON DIMENSIONS



SYMBOL	MM	
	MIN	MAX
A	9.70	10.30
B	3.40	3.80
C	8.80	9.40
D	1.17	1.47
E	2.60	3.50
F	15.10	16.70
G	19.55MAX	
H	2.54REF	
I	0.70	0.95
J	9.35	11.00
K	4.30	4.77
L	1.20	1.45
M	0.40	0.65
N	2.20	2.60



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