

FB31N20D-VB Datasheet

N-Channel 200 V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)
200	0.058 at $V_{GS} = 10$ V	35

FEATURES

- Trench Power MOSFETS
- 175 °C Junction Temperature
- New Low Thermal Resistance Package
- Compliant to RoHS Directive 2002/95/EC

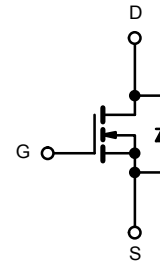


RoHS
COMPLIANT

APPLICATIONS

- Industrial

TO-220AB



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	200	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 175$ °C)	I_D	35	A
		23	
Pulsed Drain Current	I_{DM}	70	
Avalanche Current	I_{AR}	35	mJ
Repetitive Avalanche Energy ^a	E_{AR}	61	
Maximum Power Dissipation ^a	P_D	300 ^b	W
		3.75	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) ^c	R_{thJA}	40	°C/W
Junction-to-Case (Drain)	R_{thJC}	0.5	

Notes:

- Duty cycle ≤ 1 %.
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR-4 material).

SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min .	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{DS} = 0 V, I _D = 250 μA	200			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2		4	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 30 V			± 250	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 200 V, V _{GS} = 0 V			1	μA
		V _{DS} = 200 V, V _{GS} = 0 V, T _J = 125 °C			50	
		V _{DS} = 200 V, V _{GS} = 0 V, T _J = 175 °C			250	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	70			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 20 A		0.058		Ω
		V _{GS} = 10 V, I _D = 20 A, T _J = 125 °C		0.130		
		V _{GS} = 10 V, I _D = 20 A, T _J = 175 °C		0.170		
		V _{GS} = 6 V, I _D = 15 A		0.070		
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 20 A		70		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		2690		pF
Output Capacitance	C _{oss}			200		
Reverse Transfer Capacitance	C _{rss}			110		
Total Gate Charge ^c	Q _g	V _{DS} = 100 V, V _{GS} = 10 V, I _D = 45 A		95	140	nC
Gate-Source Charge ^c	Q _{gs}			28		
Gate-Drain Charge ^c	Q _{gd}			34		
Gate Resistance	R _g	f = 1 MHz		1.6		Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 100 V, R _L = 2.78 Ω I _D ≅ 45 A, V _{GEN} = 10 V, R _g = 2.5 Ω		22	35	ns
Rise Time ^c	t _r			220	330	
Turn-Off Delay Time ^c	t _{d(off)}			40	60	
Fall Time ^c	t _f			145	220	
Source-Drain Diode Ratings and Characteristics (T _C = 25 °C) ^b						
Continuous Current	I _S				45	A
Pulsed Current	I _{SM}				70	
Forward Voltage ^a	V _{SD}	I _F = 45 A, V _{GS} = 0 V		1	1.5	V
Reverse Recovery Time	t _{rr}	I _F = 45 A, di/dt = 100 A/μs		150	225	ns
Peak Reverse Recovery Current	I _{RM(REC)}			12	18	A
Reverse Recovery Charge	Q _{rr}			0.9	2	μC

Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

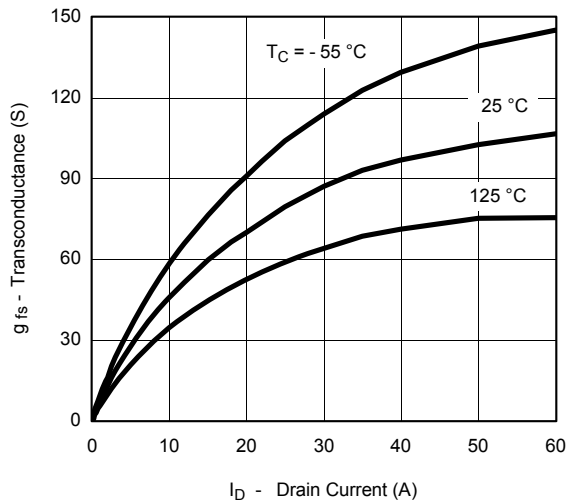
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Output Characteristics



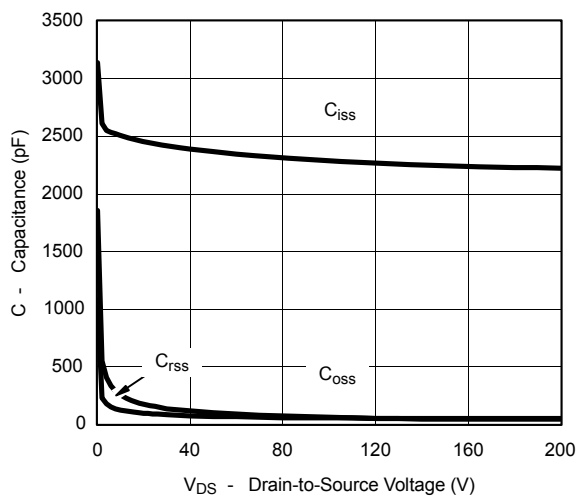
Transfer Characteristics



Transconductance



On-Resistance vs. Drain Current

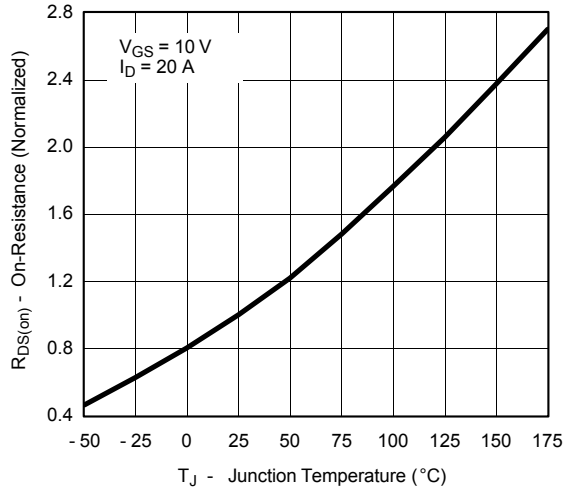


Capacitance

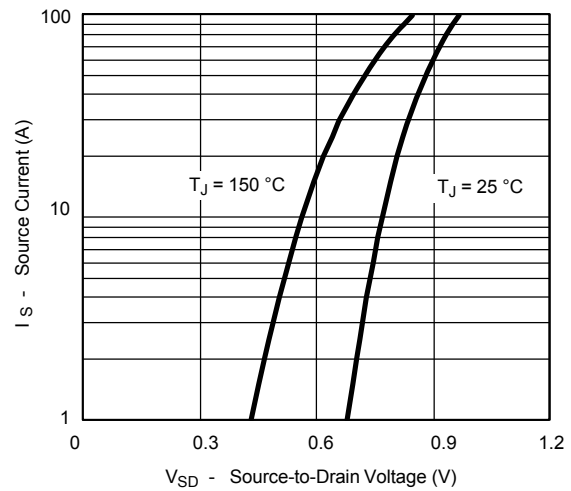


Gate Charge

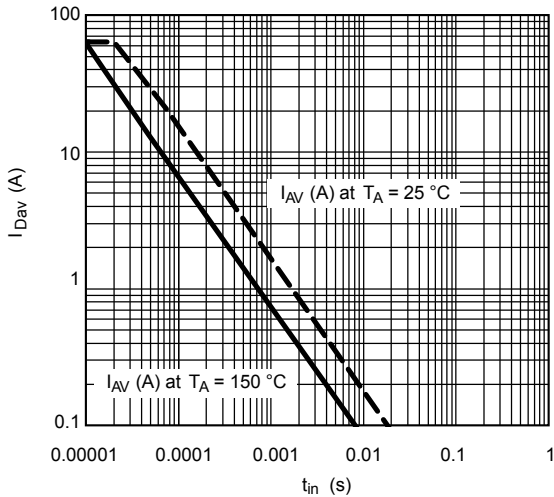
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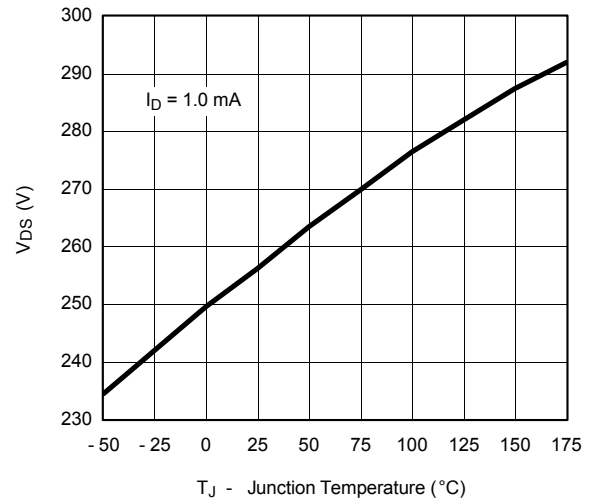
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

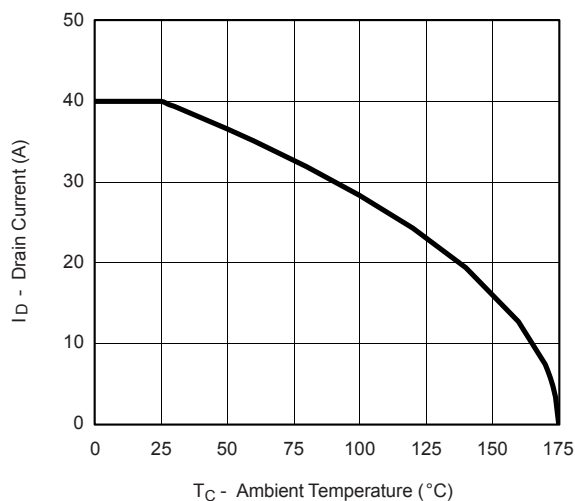


Avalanche Current vs. Time

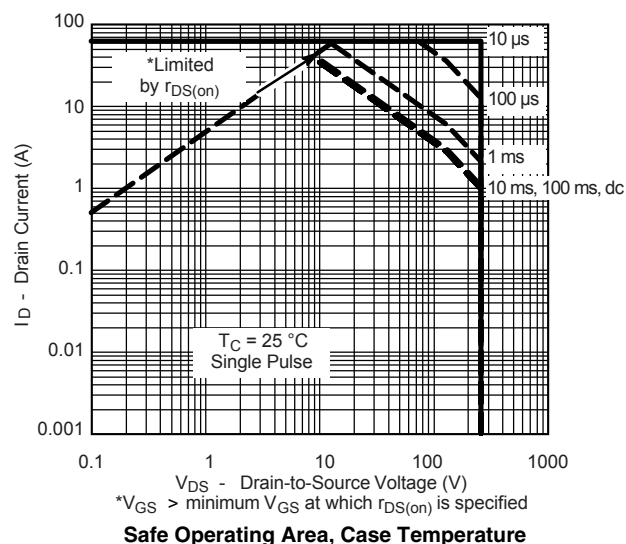


Drain Source Breakdown vs. Junction Temperature

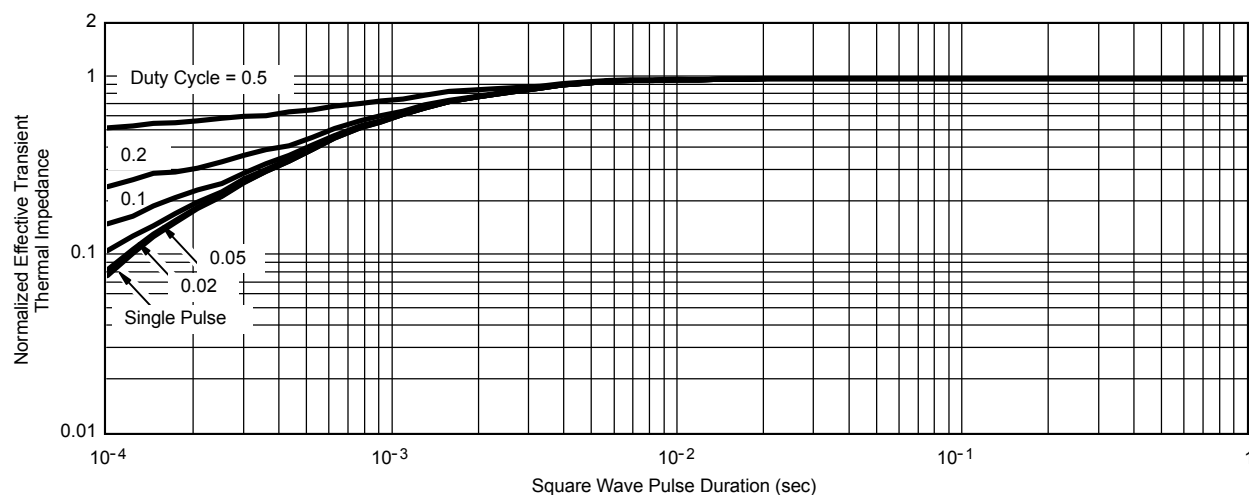
THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature



Safe Operating Area, Case Temperature



Normalized Thermal Transient Impedance, Junction-to-Case

Technical drawing of a connector with three pins, showing front, side, and top views with dimensions.

Front View (Top):

- Overall width: E
- Overall height: D
- Pin height: $H(1)$
- Pin width: Q
- Pin diameter: $\varnothing P$
- Pin positions: 1, 2, 3
- Pin length: $L(1)$
- Pin diameter: M^*
- Pin width: $b(1)$
- Pin diameter: b
- Pin width: e
- Pin length: $e(1)$

Side View (Right):

- Overall width: A
- Pin width: F
- Pin diameter: C
- Pin length: $J(1)$

Top View (Bottom):

- Overall width: $D2$

ECN: T14-0413-Rev. P, 16-Jun-14
DWG: 5471

* M = 1.32 mm to 1.62 mm (dimension including protrusion)
Heatsink hole for HVM

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