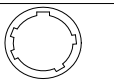
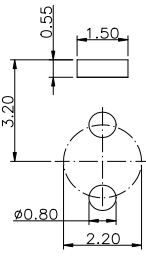
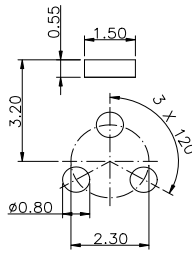
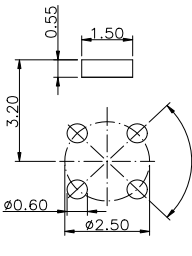
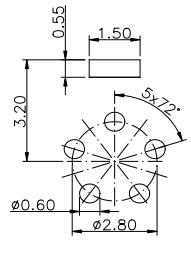
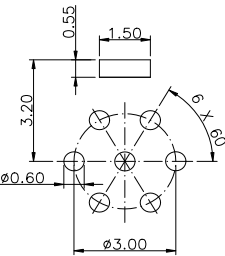
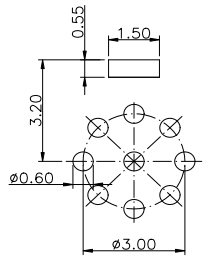
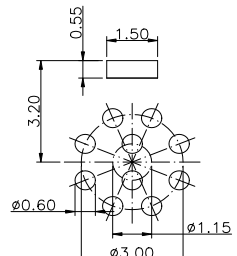


	1	2	3	4	5	6	7	8
F								
E								
D	FDYB-XXEFP-NDK-001 Code ① Housing Material & Surface Plating Code ② Key Point D AL ALLOY/BK Cr Graphic  Code ③ Pin Configuration (Solder Definition) Graphic  2 PIN  3 PIN  4 PIN  5 PIN  7 PIN  9 PIN  10 PIN Housing Material & Surface Plating ① KEY POINT ② PIN CONFIGURATION ③ SIZE(CONNECTOR)							
C	PCB Layout  2 PIN  3 PIN  4 PIN  5 PIN  7 PIN  9 PIN  10 PIN							
B								
A	UNIT:mm   TOLERANCE X ±0.25 .XX ±0.1 .XXX ±0.05 ANGLES ±1° TITLE FLOS, DY-SER, SZ 0, 09P, PANEL F S/NUT, F, PCB S, 5KEY GN SCALE 2:1 SIZE A4 SHEET 2 OF 2 REV. A WC-REV. .0 :Inspection item Amphenol LTW 安費諾亮泰企業股份有限公司 ITEM NO FDYB-XXEFP-NDK-001 DWG NO FDYB-XXEFP-NDK-001 CUSTOMER ALTW DRAWN BY Vincent Huang CHECKED BY Aric Ko APPROVED BY Max Yang DATE 2021/04/30 DATE 2021/04/30 DATE 2021/04/30							
	1	2	3	4	5	6	7	8

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. IT IS CONFIDENTIAL AND A PROPERTY OF Amphenol LTW. DISCLOSURE TO THIRD PARTY HAS TO BE AUTHORIZED BY Amphenol LTW.