

TPMAX40200ANS+T

Low Quiescent current Load Switch

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Features

- Wide Supply Voltage Range: 1.5V to 5.5V
- Quiescent current :0.4uA @ Typ
- Standby current : 80nA @ Typ
- Maximum Output current : 2A
- Enable Active High
- Typical Rdson 32mΩ @5.5V
- Controlled Rise Time: 570us at 3.3VIN
- Quick Output Discharge (QOD) : 85Ω (Typ.)
- Hot Plug-In Application(Soft start)
- Reverse Current Flow Blocking (no body diode)
- RoHS and Halogen free compliance
- ESD Protection: 2kV HBM, 200V MM
- Package WLCSP-4L

Applications

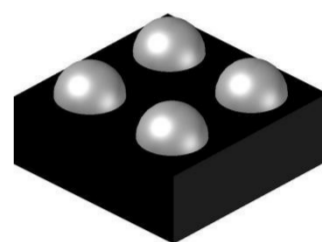
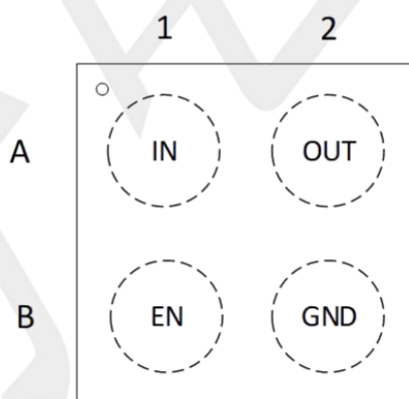
- Notebook and Desktop Computers
- High-Side Power Protection Switches
- Consumer Electronics
- Telecom Systems
- Wearables
- USB Device Power Switch
- Portable Speakers

General Description

The is a small, single channel load switch using P-Channel MOSFET for minimum power loss. Advanced gate control design supports operating voltages as low as 1.5 V with minimal increase in ON-Resistance and power loss. It is designed for load switching applications with ultra-low quiescent current (0.5uA) and ultra-low standby current (150nA).

The offers industry leading True Reverse Current Blocking performance. It minimizes reverse current flow in the event that the VOUT pin voltage exceeds the VIN voltage. The device is controlled by external logic pin, allowing optimization of battery life, and portable device autonomy.

Pin Information



Pin Information (Top-Through View)

WLCSP- 4L

Pin Configurations

Pin Number	Pin Name	Pin Function
A1	IN	Power Supply Input.
A2	OUT	Output Voltage Pin.
B1	EN	Enable (Active high)
B2	GND	Ground.

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Absolute Maximum Ratings

Parameter	Symbol	Condition	Min.	Max.	Unit
Input voltage	V_{IN}		-0.3	6	V
Output voltage	V_{OUT}		-0.3	6	V
Enable voltage	V_{EN}		-0.3	6	V
Maximum continuous switch current	I_{MAX}		--	2	A
Maximum junction temperature	$T_{J,MAX}$		--	125	°C
Lead Temperature	T_{LEAD}	Soldering, 10 sec.	--	300	°C
Storage Temperature Range	T_{STG}		-65	150	°C
Human Body Model, JESD22-A114	HBM		4000		V
Charged Device Model, JESD22-C101	CDM		2000		V
MSL			Level 1		

The absolute maximum ratings are stress ratings only. Stresses exceeding the range in Table might cause substantial damage to the device. Functional operation of the device under other conditions is not implied. Prolonged exposure to extreme conditions might affect device reliability.

Thermal Information

Parameter	Symbol	Value	Unit
Junction-to-Ambient thermal resistance	$R_{\theta JA}$	110	°C/W

Surface mounted on FR-4 Board using 2 oz, 4 layer board, PCB board size is 3*3 square inches

Recommended Operation Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit
Input Voltage	V_{IN}	1.5	--	5.5	V
Enable voltage	V_{EN}	0	--	5.5	V
Output voltage	V_{OUT}	0	--	5.5	V
Operating Junction Temperature	T_J	-40	--	125	°C
Operating Ambient Temperature	T_A	-40	--	85	°C

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Electrical Characteristics (Ta=25°C, VIN=3.3V, CIN=COUT=1μF, unless otherwise noted)

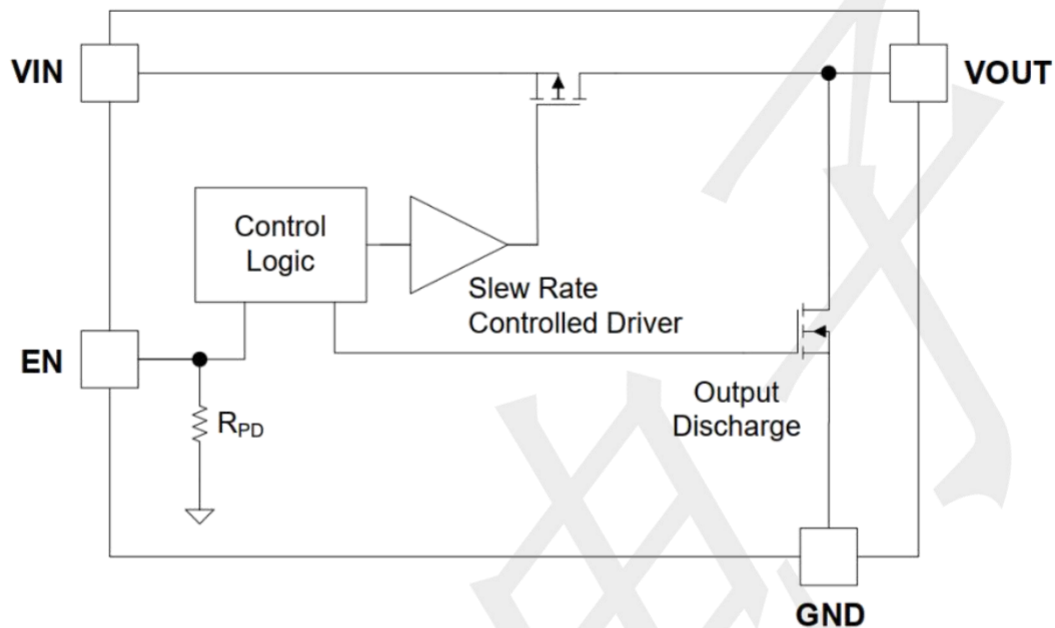
Symbol	Parameter	Conditions	Min	Typ	Max	Units
Basic Operation						
IQ	Quiescent Current	EN = Enable, IOUT=0 mA, VIN = VEN =5.5 V	--	0.4	--	uA
		EN=Enable, IOUT=0mA, VIN=VEN=5.5V, 85 °C	--	0.7	--	
ISD	Shut Down Current	EN = Disable, IOUT=0 mA, VIN=1.5 V	--	65	--	nA
		EN = Disable, IOUT=0 mA, VIN=3.3 V	--	70	--	
		EN = Disable, IOUT=0 mA, VIN=4.2 V	--	75	--	
		EN = Disable, IOUT=0 mA, VIN=5.5 V	--	80	--	
		EN= Disable, IOUT=0 mA, VIN=5.5 V, 55 °C	--	140	--	
RON	On-Resistance	VIN=5.5 V, IOUT= 500 mA	Ta=25 °C	32	--	mΩ
				36	--	
		VIN=3.3 V, IOUT= 500 mA	Ta=25 °C	42	--	
				48	--	
		VIN=1.8 V, IOUT= 300 mA	Ta=25 °C	72	--	
		VIN=1.5 V, IOUT= 100 mA	Ta=25 °C	100	--	
RDSC	Output Discharge Resistance	EN=Low , IFORCE= 10 mA	--	85	--	Ω
VIH	EN Input Logic	VIN=1.5-5.5 V	1.2	--	--	V
VIL	High Voltage	VIN=1.5-5.5 V	--	--	0.4	V
REN	EN pull down resistance	Internal Resistance	--	10	--	MΩ
IEN	EN Current	EN=VIN or GND	--	0.5	--	uA
VRCB_TH	RCB Protection Threshold Voltage	VOUT – VIN	--	25	--	mV
VRCB_RL	RCB Protection Release Voltage	VIN – VOUT	--	30	--	mV
Switching Characteristics						
tdON	Turn-On Delay	RL=150 Ω, COUT=0.1 μF	--	430	--	us
tR	VOUT Rise Time		--	570	--	
tdOFF	Turn-Off Delay	RL=150 Ω, COUT=0.1 μF	--	17	--	us
tF	VOUT Rise Time		--	15	--	

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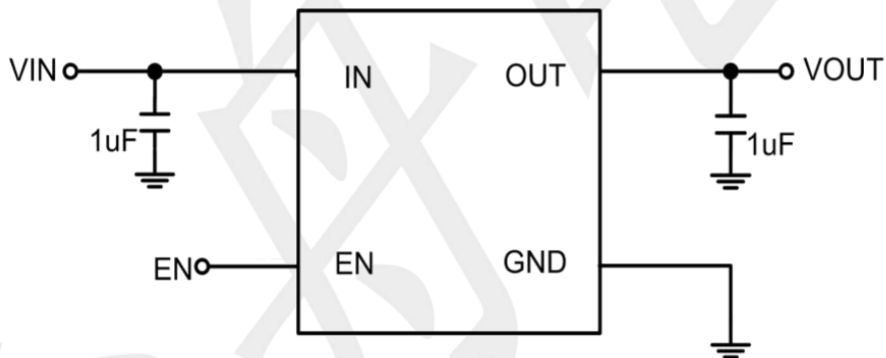
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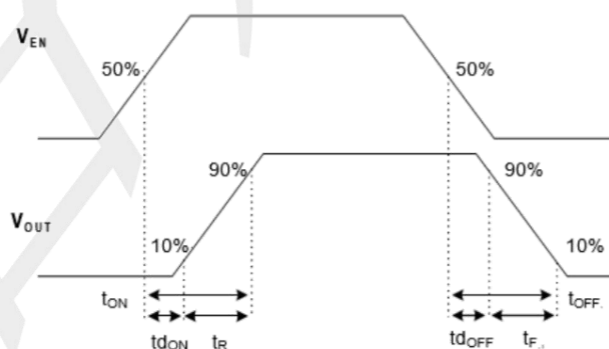
BLOCK DIAGRAM



Typical Application Circuit



Timing Diagram

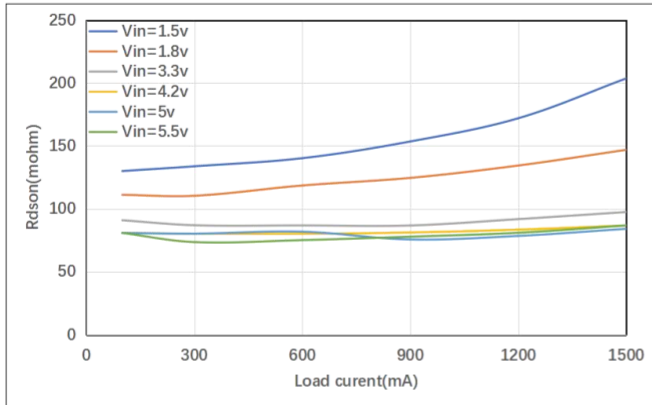


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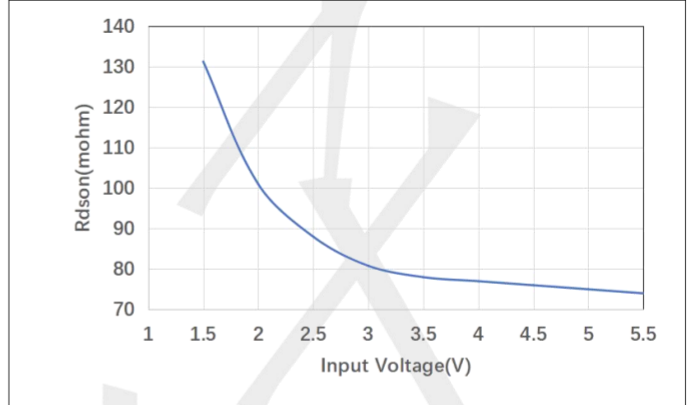
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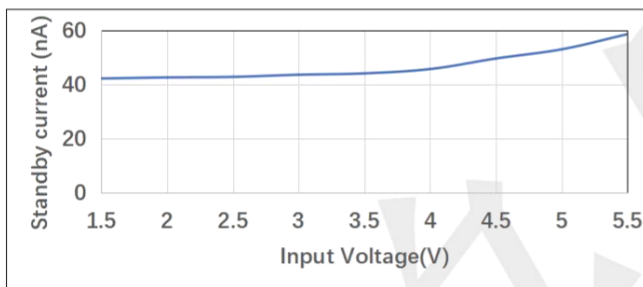
Typical characteristics



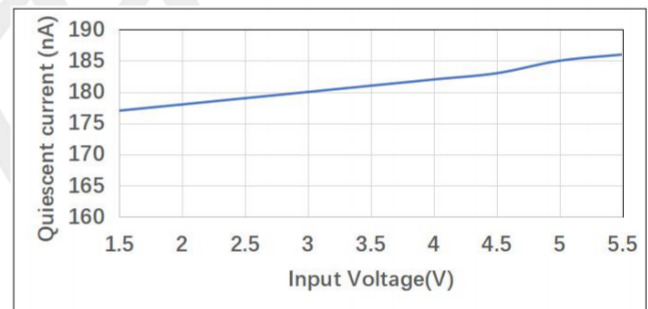
RON vs. Load current (VEN=H)



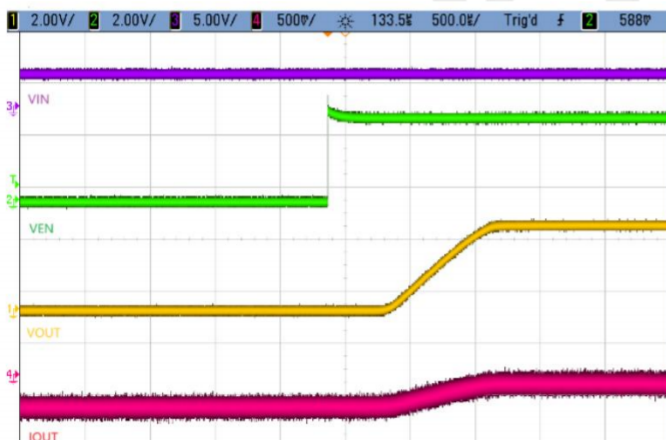
RON vs. VIN (VEN=VIN, ILOAD=200mA)



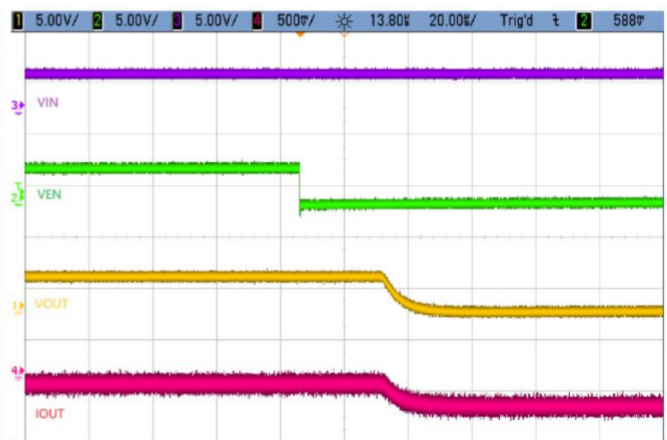
Standby current vs. VIN (VOUT=OPEN)



Quiescent current vs. VIN (VEN=H, NO LOAD)



Turn on transient (Cin=1uF, Cout=0.1uF, RLOAD=150Ω, VIN=3.3V)



Turn off transient (Cin=1uF, Cout=0.1uF, RLOAD=150Ω, VIN=3.3V)

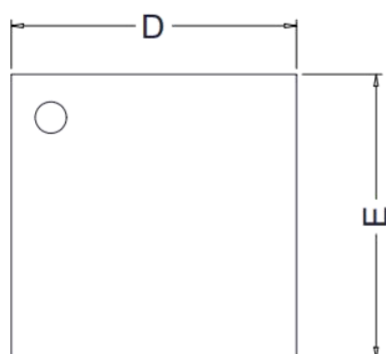
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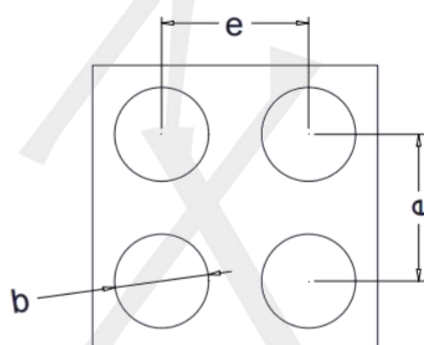
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Package information

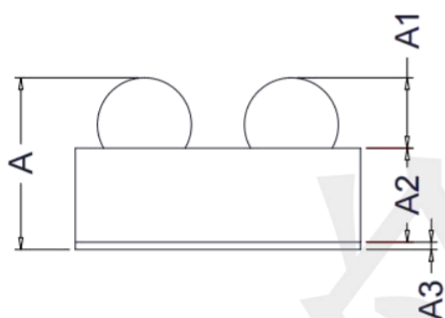
WLCSP-4L



TOP VIEW



BOTTOM VIEW



SIDE VIEW

Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.395	0.445	0.495
A1	0.144	0.160	0.176
A2	0.255	0.285	0.315
A3	0.020	0.025	0.030
D	0.755	0.770	0.785
E	0.755	0.770	0.785
e	0.350	0.400	0.450
b	0.190	0.210	0.230