

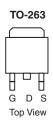
# IRF3710ZSTRPBF-VB Datasheet N-Channel 100-V (D-S) MOSFET

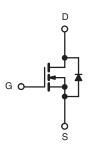
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)			
100	0.010 at V <sub>GS</sub> = 10 V	100			
100	0.023 at $V_{GS} = 4.5 \text{ V}$	85			

#### **FEATURES**

- Trench Power MOSFET
- 175 °C Maximum Junction Temperature
- · Compliant to RoHS Directive 2002/95/EC







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted					
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		$V_{DS}$	100	V	
Gate-Source Voltage		$V_{GS}$	± 20	V	
Continuous Drain Current (T <sub>.I</sub> = 150 °C)	T <sub>C</sub> = 25 °C	. I <sub>D</sub>	100	A	
Continuous Diam Guilent (1) = 130 C)	T <sub>C</sub> = 125 °C		75 <sup>a</sup>		
Pulsed Drain Current	$I_{DM}$	300	A		
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	75		
Single Pulse Avalanche Energy <sup>b</sup>	L = 0.1 IIII1	E <sub>AS</sub>	280	mJ	
Mariana Darray Dissipation	T <sub>C</sub> = 25 °C (TO-220AB and TO-263)	$P_{D}$	250 <sup>c</sup>	W	
Maximum Power Dissipation <sup>b</sup>	T <sub>A</sub> = 25 °C (TO-263) <sup>d</sup>	ט י	3.75	VV	
Operating Junction and Storage Temperate	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	PCB Mount (TO-263) <sup>d</sup>	- R <sub>thJA</sub>	40	°C/W	
Junction-to-Ambient	Free Air (TO-220AB)	' 'thJA	62.5		
Junction-to-Case		R <sub>thJC</sub>	0.6		

#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			•				
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	100				
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2		4	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	μΑ	
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α	
		$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$		0.010			
	<sub>B</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		0.023		Ω	
Drain-Source On-State Resistance <sup>a</sup>	H <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C		0.020			
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C		0.030			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 30 \text{ A}$	25			S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			6550		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		665			
Reverse Transfer Capacitance	C <sub>rss</sub>	]		265			
Total Gate Charge <sup>c</sup>	$Q_g$			105	160		
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 85 \text{ A}$		17		nC	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$	7		23			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			12	25		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, R_{L} = 0.6 \Omega$		90	135		
Turn-Off DelayTime <sup>c</sup>	t <sub>d(off)</sub>	$I_D\cong 85~A,~V_{GEN}=10~V,~R_g=2.5~\Omega$		55	85	ns -	
Fall Time <sup>c</sup>	t <sub>f</sub>	7		130	195		
Source-Drain Diode Ratings and Cha	acteristics T <sub>C</sub>	= 25 °C <sup>b</sup>					
Continuous Current	I <sub>S</sub>				85	Λ	
Pulsed Current	I <sub>SM</sub>				240	Α	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 85 A, V <sub>GS</sub> = 0 V		1.0	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			85	140	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 50 A, dI/dt = 100 A/μs		4.5	7	Α	
Reverse Recovery Charge	Q <sub>rr</sub>	1		0.17	0.35	μC	

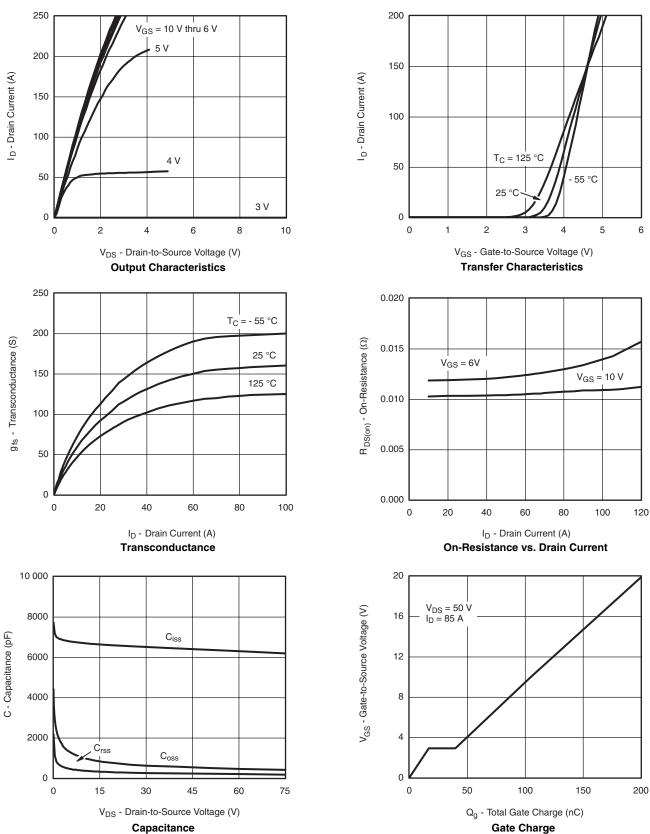
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

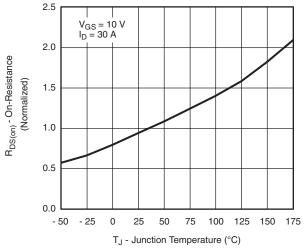


**TYPICAL CHARACTERISTICS**  $T_A = 25 \, ^{\circ}C$ , unless otherwise noted

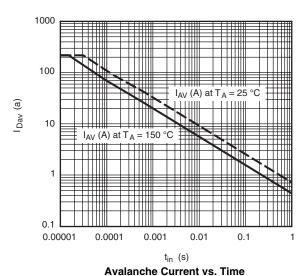




### **TYPICAL CHARACTERISTICS** $T_A = 25 \, ^{\circ}C$ , unless otherwise noted

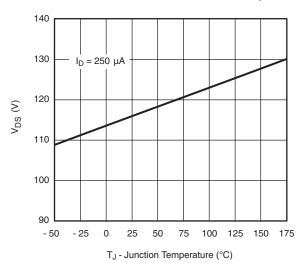


#### On-Resistance vs. Junction Temperature



T<sub>J</sub> = 150 °C T<sub>J</sub> = 25 °C T<sub>J</sub>

Source-Drain Diode Forward Voltage

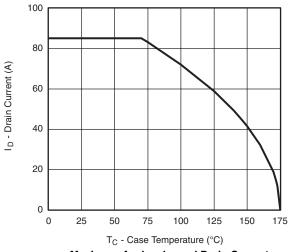


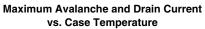
T<sub>J</sub> - Drain-Source Breakdown vs. Junction-Temperature

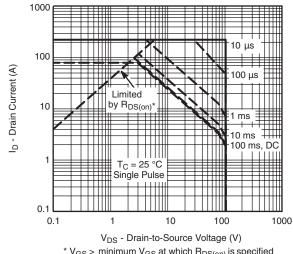


5

#### **THERMAL RATINGS**

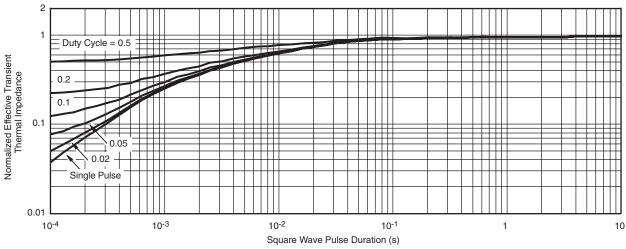






\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

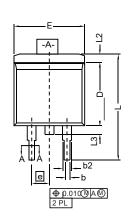
Safe Operating Area

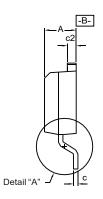


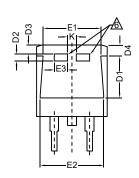
Normalized Thermal Transient Impedance, Junction-to-Case



## **TO-263 (D<sup>2</sup>PAK): 3-LEAD**

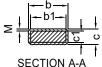








**DETAIL A (ROTATED 90°)** 



_	ļ	— b — b´	 	ļ	ţ
_	T			2	7
	SE	CTIC	NC	A-A	ı

- 1. Plane B includes maximum features of heat sink tab and plastic.
- 2. No more than 25 % of L1 can fall above seating plane by max.
- 3. Pin-to-pin coplanarity max. 4 mils.
- 4. \*: Thin lead is for SUB, SYB. Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

  This feature is for thick lead.

	INCHES MILLIMETER			METERS	
	DIM.	MIN.	MAX.	MIN.	MAX.
Α		0.160	0.190	4.064	4.826
	b	0.020	0.039	0.508	0.990
	b1	0.020	0.035	0.508	0.889
	b2	0.045	0.055	1.143	1.397
С*	Thin lead	0.013	0.018	0.330	0.457
C	Thick lead	0.023	0.028	0.584	0.711
c1	Thin lead	0.013	0.017	0.330	0.431
61	Thick lead	0.023	0.027	0.584	0.685
	c2	0.045	0.055	1.143	1.397
	D	0.340	0.380	8.636	9.652
	D1	0.220	0.240	5.588	6.096
	D2	0.038	0.042	0.965	1.067
	D3	0.045	0.055	1.143	1.397
D4		0.044	0.052	1.118	1.321
	E	0.380	0.410	9.652	10.414
	E1	0.245	-	6.223	-
	E2	0.355	0.375	9.017	9.525
	E3	0.072	0.078	1.829	1.981
	е	0.100 BSC		2.54 BSC	
K		0.045	0.055	1.143	1.397
L		0.575	0.625	14.605	15.875
L1		0.090	0.110	2.286	2.794
L2		0.040	0.055	1.016	1.397
L3		0.050	0.070	1.270	1.778
	L4 0.010 BSC 0.254 BS0		BSC		
	М	-	0.002	-	0.050
FCN T13-0707-Rev K 30-Sep-13					

ECN: T13-0707-Rev. K, 30-Sep-13

DWG: 5843



## **Disclaimer**

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

## **Material Category Policy**

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be RoHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.