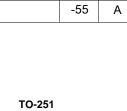


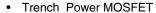
P-Channel 40 V (D-S) MOSFET

V _{DS}	-40	V	
RDS(on),typ	VGS=10V	10	mΩ
R _D S(on),typ	VGS=4.5V	14	mΩ
lD	-55	Α	



FEATURES

Halogen-free According to IEC 61249-2-21 Definition

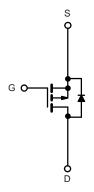


- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

- Power Switch
- DC/DC Converters



P-Channel MOSFET

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ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
Parameter	Symbol	Limit	Unit		
Gate-Source Voltage	V _{GS}	± 20	V		
Continuous Drain Current (T _J = 175 °C)	T _C = 25 °C	,	- 55 ^a		
	T _C = 125 °C	I _D	- 50	A	
Pulsed Drain Current	I _{DM}	- 220			
Avalanche Current	I _{AR}	- 60			
Repetitive Avalanche Energy ^b	L = 0.1 mH	E _{AR}	180	mJ	
Power Dissipation	T _C = 25 °C	В	45	W	
	T _A = 25 °C	P _D	3.75	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	PCB Mount (TO-263) ^c	В	40	°C/W	
	Free Air (TO-220AB)	R _{thJA}	62.5		
Junction-to-Case	·	R _{thJC}	0.8		

Notes:

- a. Package limited.
- b. Duty cycle ≤ 1 %.
- c. When mounted on 1" square PCB (FR-4 material).
- d. See SOA curve for voltage derating.

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage		$V_{GS} = 0 \text{ V, } I_D = -250 \mu\text{A}$ - 40					
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 1.0		- 2.5	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V _{DS} = - 40 V, V _{GS} = 0 V			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 40 V, V _{GS} = 0 V, T _J = 125 °C			- 50	μA	
		V _{DS} = - 40 V, V _{GS} = 0 V, T _J = 175 °C			- 250		
On-State Drain Current ^a	I _{D(on)}	V _{DS} = -5 V, V _{GS} = -10 V	- 120			Α	
		V _{GS} = - 10 V, I _D = - 30 A		10		mΩ	
Drain-Source On-State Resistance ^a	D	V _{GS} = - 10 V, I _D = - 30 A, T _J = 125 °C		16			
Diani-Source On-State Resistance	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 30 A, T _J = 175 °C		23			
		V _{GS} = - 4.5 V, I _D = - 20 A		14			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 75 A	20			S	
Dynamic ^b							
Input Capacitance	C _{iss}			3000		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = -25 \text{ V}, f = 1 \text{ MHz}$		620			
Reversen Transfer Capacitance	C _{rss}			315			
Total Gate Charge ^c	Q_g				90		
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -75 \text{ A}$			32	nC	
Gate-Drain Charge ^c	Q_{gd}				30		
Turn-On Delay Time ^c	t _{d(on)}			25	40		
Rise Time ^c	t _r	$V_{DD} = -15 \text{ V}, R_{L} = 0.2 \Omega$		225	360	20	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ - 75 A, V_{GEN} = - 10 V, R_g = 2.5 Ω		150	240	ns	
Fall Time ^c	t _f			210	340	1	
Source-Drain Diode Ratings and Cha	racteristics ^b	(T _C = 25 °C)					
Continuous Current	I _S			- 220		А	
Pulsed Current	I _{SM}				- 240	А	
Forward Voltage ^a	V_{SD}	I _F = -75 A, V _{GS} = 0 V		- 1.2	- 1.5	V	
Reverse Recovery Time	t _{rr}			55	100	ns	
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = - 75 A, dI/dt = 100 A/μs		2.5	5	Α	
Reverse Recovery Charge	Q _{rr}	1		0.07	0.25	иC	

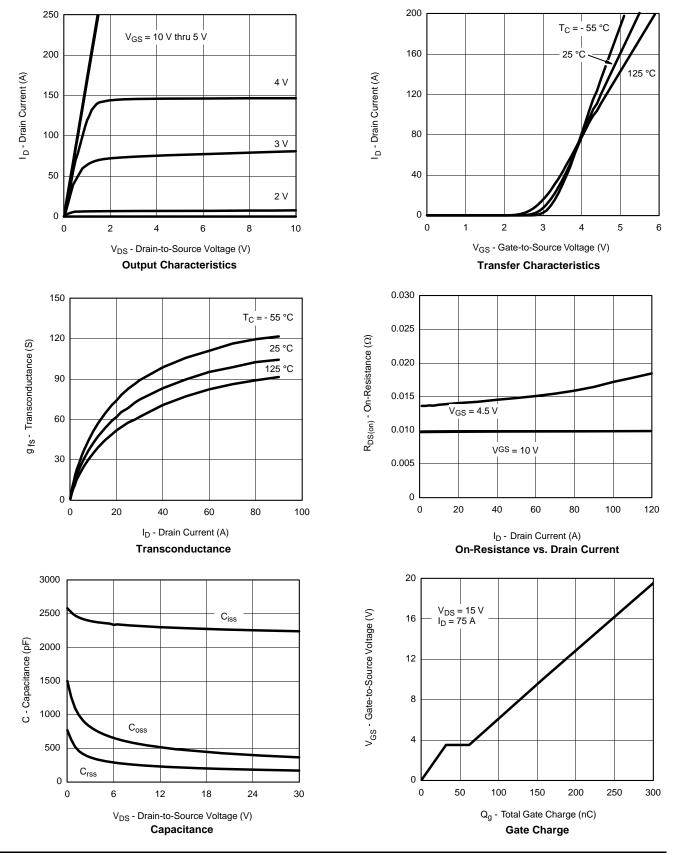
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

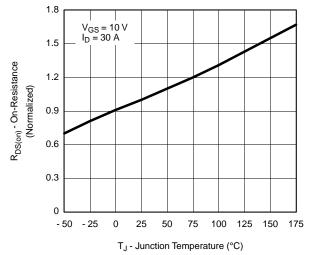


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

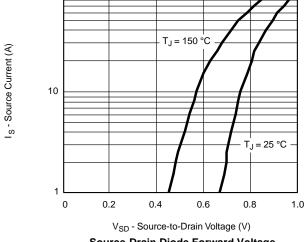




TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

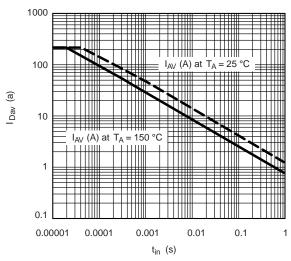


On-Resistance vs. Junction Temperature

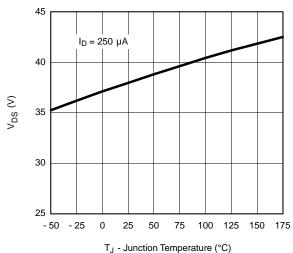


100

Source-Drain Diode Forward Voltage



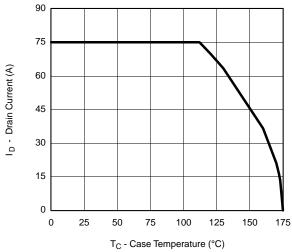
Avalanche Current vs. Time

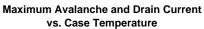


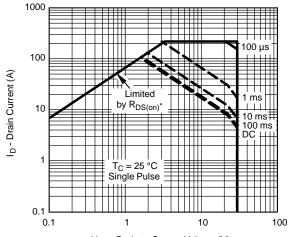
Drain Source Breakdown vs. Junction Temperature



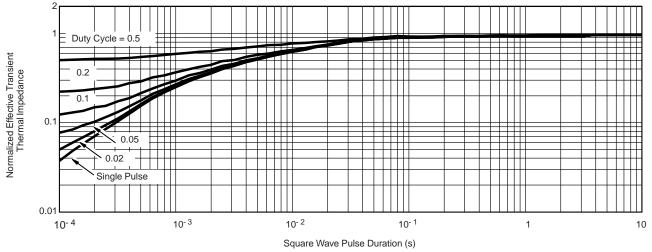
THERMAL RATINGS







$$\begin{split} &V_{DS}\text{ - Drain-to-Source Voltage (V)}\\ ^*V_{GS}>&\min\text{minimum }V_{GS}\text{ at which }R_{DS(on)}\text{ is specified}\\ &\textbf{Safe Operating Area} \end{split}$$



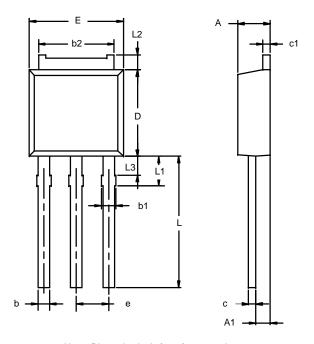
Normalized Thermal Transient Impedance, Junction-to-Case

服务热线:400-655-8788

5



TO-251AA (DPAK)



Note: Dimension L3 is for reference only.

	MILLIMETERS		INC	HES		
Dim	Min	Max	Min	Max		
Α	2.21	2.38	0.087	0.094		
A1	0.89	1.14	0.035	0.045		
b	0.71	0.89	0.028	0.035		
b1	0.76	1.14	0.030	0.045		
b2	5.23	5.43	0.206	0.214		
С	0.46	0.58	0.018	0.023		
с1	0.46	0.58	0.018	0.023		
D	5.97	6.22	0.235	0.245		
Е	6.48	6.73	0.255	0.265		
е	2.28 BSC		0.090 BSC			
L	8.89	9.53	0.350	0.375		
L1	1.91	2.28	0.075	0.090		
L2	0.89	1.27	0.035	0.050		
L3	1.15	1.52	0.045	0.060		
ECN: S-03946—Rev. E, 09-Jul-01 DWG: 5346						



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