

Dual P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY

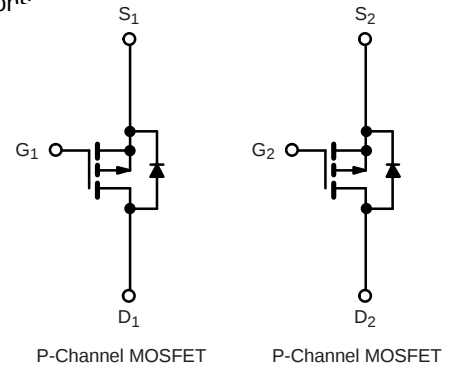
V_{DS} (V)	$R_{DS(on)}$ (Ω), Typ.	I_D (A) ^{d, e}	Q_g (Typ.)
- 30	0.013 at $V_{GS} = -10$ V	- 8	15 nC
	0.017 at $V_{GS} = -4.5$ V	- 6	

FEATURES

- Halogen-free
- Trench Power MOSFET
- 100 % UIS Tested

APPLICATIONS

- Load Switches
 - Notebook PCs
 - Desktop PCs
 - Game Station[®]


RoHS
 COMPLIANT


ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	- 30	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150^\circ\text{C}$)	$T_C = 25^\circ\text{C}$	-8 ^e	A
	$T_C = 70^\circ\text{C}$	-6 ^e	
	$T_A = 25^\circ\text{C}$	- 7 ^{a, b}	
	$T_A = 70^\circ\text{C}$	- 5 ^{a, b}	
Pulsed Drain Current	I_{DM}	- 50 ^e	A
Continuous Source-Drain Diode Current	$T_C = 25^\circ\text{C}$	- 4.1	
	$T_A = 25^\circ\text{C}$	- 2.0 ^{a, b}	
Avalanche Current	$L = 0.1$ mH	I_{AS}	- 20
Single-Pulse Avalanche Energy		E_{AS}	20
Maximum Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	5.0
	$T_C = 70^\circ\text{C}$		3.2
	$T_A = 25^\circ\text{C}$		2.5 ^{a, b}
	$T_A = 70^\circ\text{C}$		1.6 ^{a, b}
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{a, c}	$t \leq 10$ s	R_{thJA}	38	50	$^\circ\text{C/W}$
Maximum Junction-to-Foot	Steady State	R_{thJF}	20	25	

Notes:

- Surface mounted on 1" x 1" FR4 board.
- $t = 10$ s.
- Maximum under Steady State conditions is 85°C/W .
- Based on $T_C = 25^\circ\text{C}$.
- Limited by package.

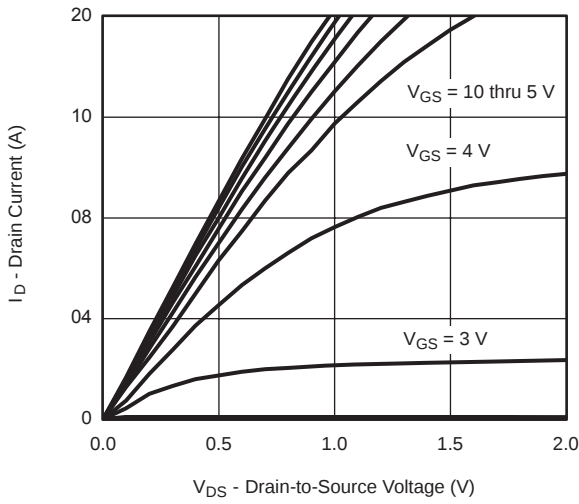
SPECIFICATIONS $T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	- 30			V	
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = -250\text{ }\mu\text{A}$		- 31		mV/ $^{\circ}\text{C}$	
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			4.5			
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	- 1.0		- 3.0	V	
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30\text{ V}, V_{GS} = 0\text{ V}$			- 1	μA	
		$V_{DS} = -30\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^{\circ}\text{C}$			- 5		
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq -10\text{ V}, V_{GS} = -10\text{ V}$	- 30			A	
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -7.3\text{ A}$		0.013		Ω	
		$V_{GS} = -4.5\text{ V}, I_D = -6.2\text{ A}$		0.017			
Forward Transconductance ^a	g_{fs}	$V_{DS} = -10\text{ V}, I_D = -9.1\text{ A}$		23		S	
Dynamic ^b							
Input Capacitance	C_{iss}	$V_{DS} = -15\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		3160		pF	
Output Capacitance	C_{oss}			215			
Reverse Transfer Capacitance	C_{rss}			185			
Total Gate Charge	Q_g	$V_{DS} = -15\text{ V}, V_{GS} = -10\text{ V}, I_D = -9.1\text{ A}$			15	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = -15\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -9.1\text{ A}$			13		
Gate-Drain Charge	Q_{gd}				4		
Gate Resistance	R_g				7.5		
Turn-On Delay Time	$t_{d(on)}$	$f = 1\text{ MHz}$		5.8		Ω	
Rise Time	t_r		$V_{DD} = -15\text{ V}, R_L = 15\text{ }\Omega$ $I_D \cong -1\text{ A}, V_{GEN} = -10\text{ V}, R_g = 1\text{ }\Omega$		10		15
Turn-Off DelayTime	$t_{d(off)}$				8		15
Fall Time	t_f				45		70
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -15\text{ V}, R_L = 15\text{ }\Omega$ $I_D \cong -1\text{ A}, V_{GEN} = -4.5\text{ V}, R_g = 1\text{ }\Omega$			12	25	
Rise Time	t_r			42	70		
Turn-Off DelayTime	$t_{d(off)}$			35	60		
Fall Time	t_f			40	70		
Drain-Source Body Diode Characteristics							
Continous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^{\circ}\text{C}$			- 4.1	A	
Pulse Diode Forward Current	I_{SM}				- 32		
Body Diode Voltage	V_{SD}	$I_S = -2\text{ A}, V_{GS} = 0\text{ V}$		- 0.75	- 1.2	V	
Body Diode Reverse Recovery Time	t_{rr}	$I_F = -2\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^{\circ}\text{C}$		34	60	ns	
Body Diode Reverse Recovery Charge	Q_{rr}			22	40	nC	
Reverse Recovery Fall Time	t_a			11		ns	
Reverse Recovery Rise Time	t_b			23			

Notes:

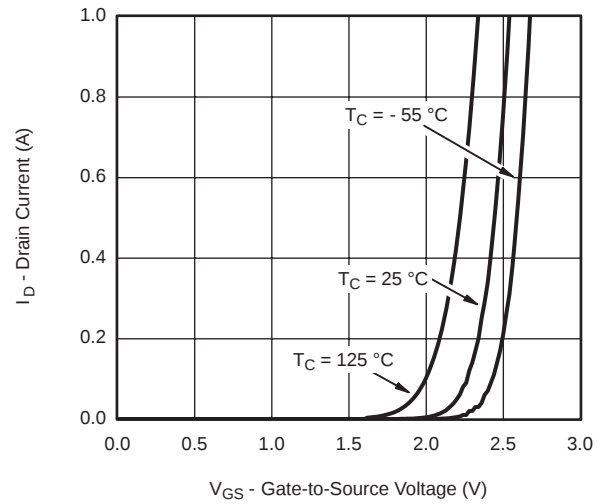
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

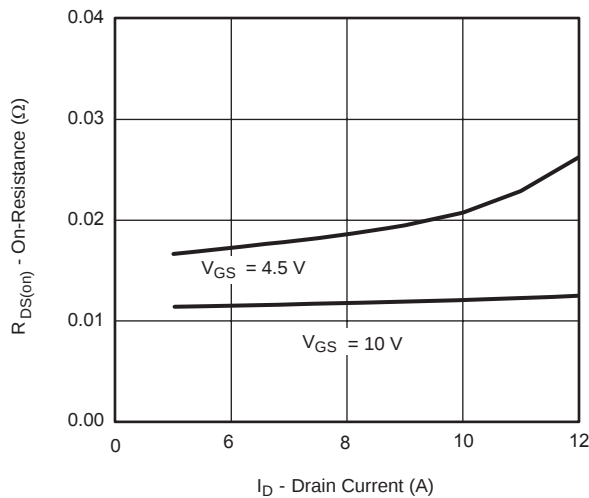
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



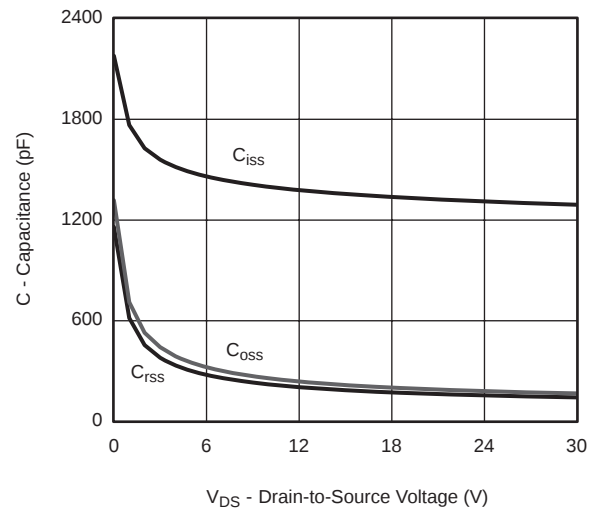
Output Characteristics



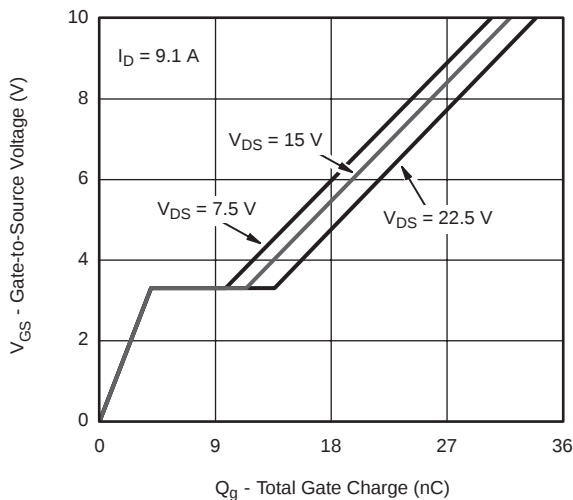
Transfer Characteristics



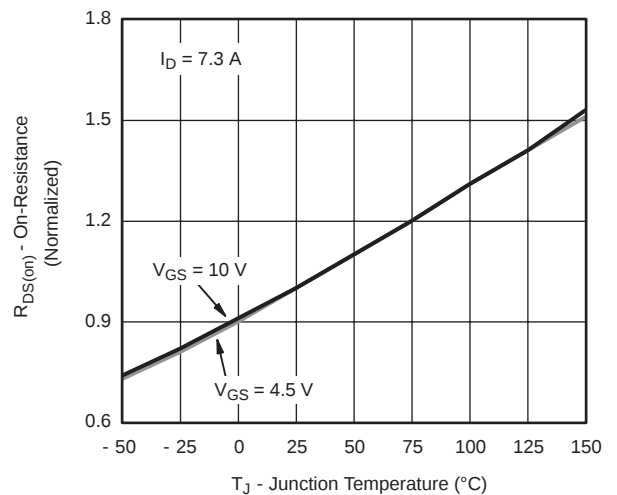
On-Resistance vs. Drain Current



Capacitance

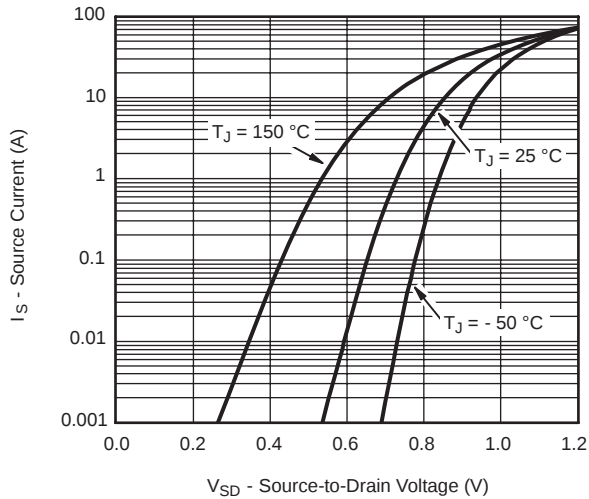


Gate Charge

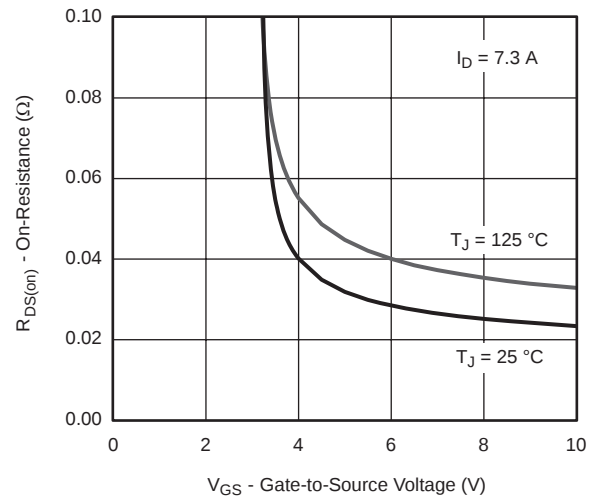


On-Resistance vs. Junction Temperature

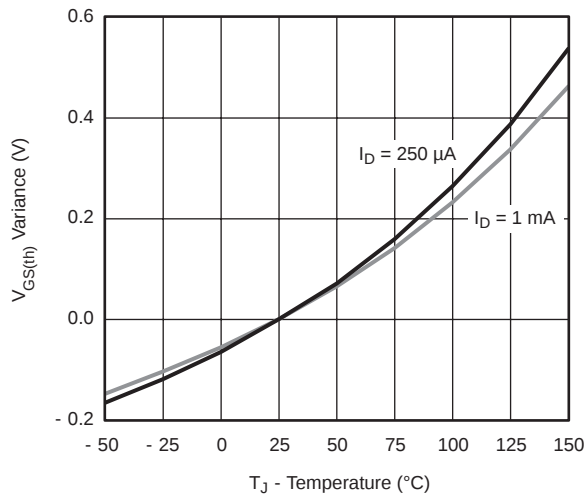
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



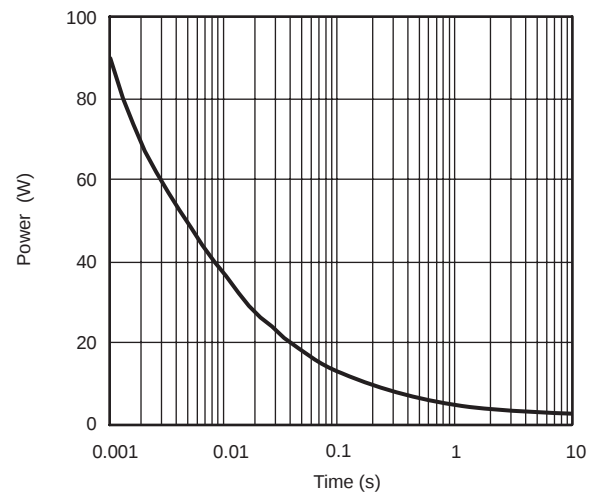
Source-Drain Diode Forward Voltage



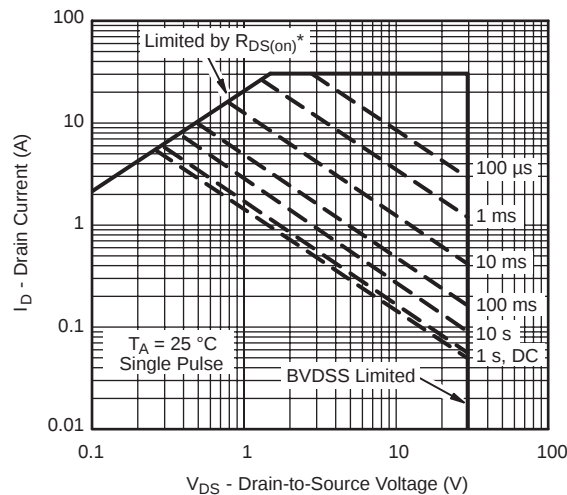
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

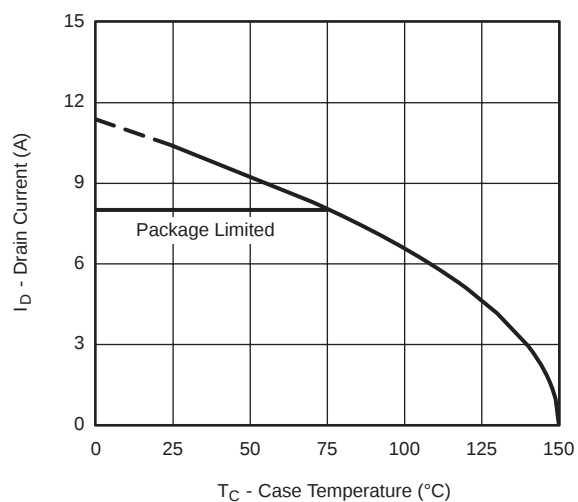
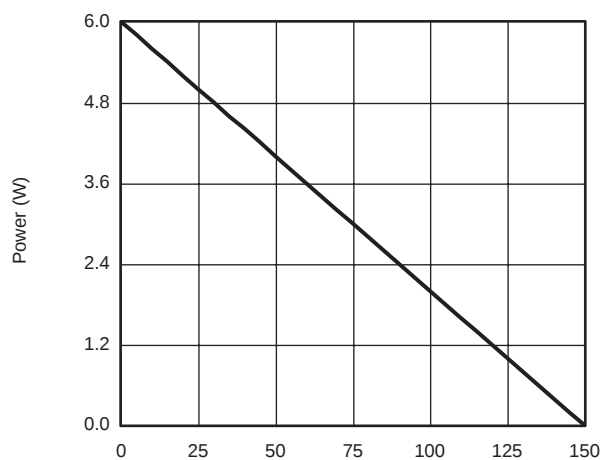
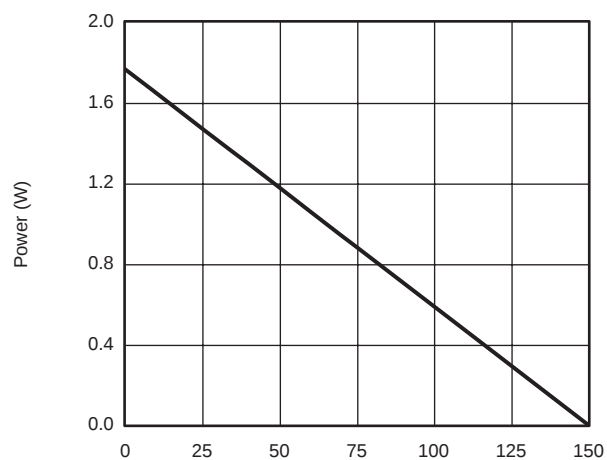


Single Pulse Power, Junction-to-Ambient



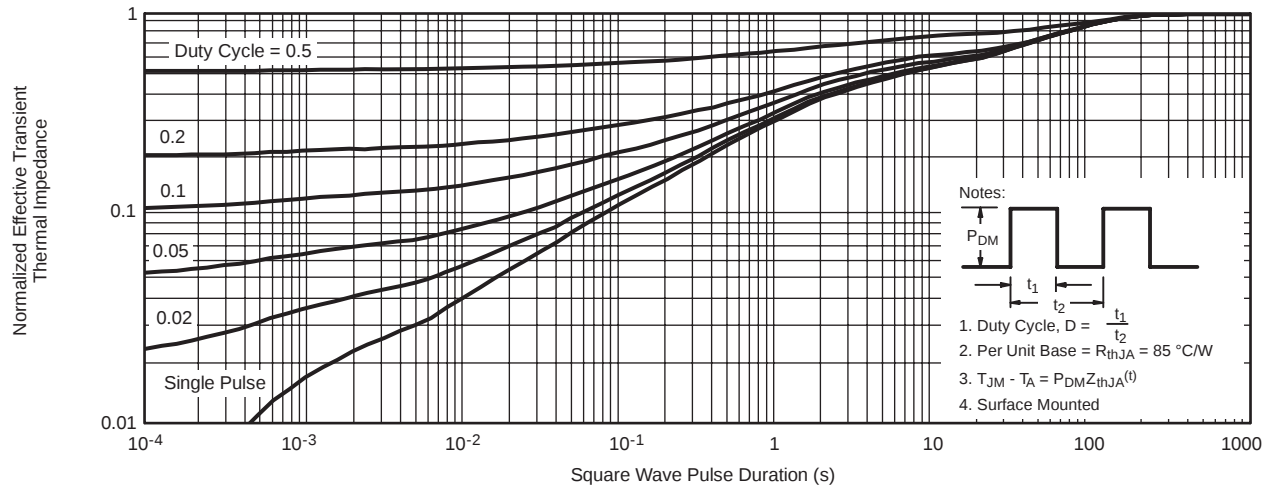
* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area

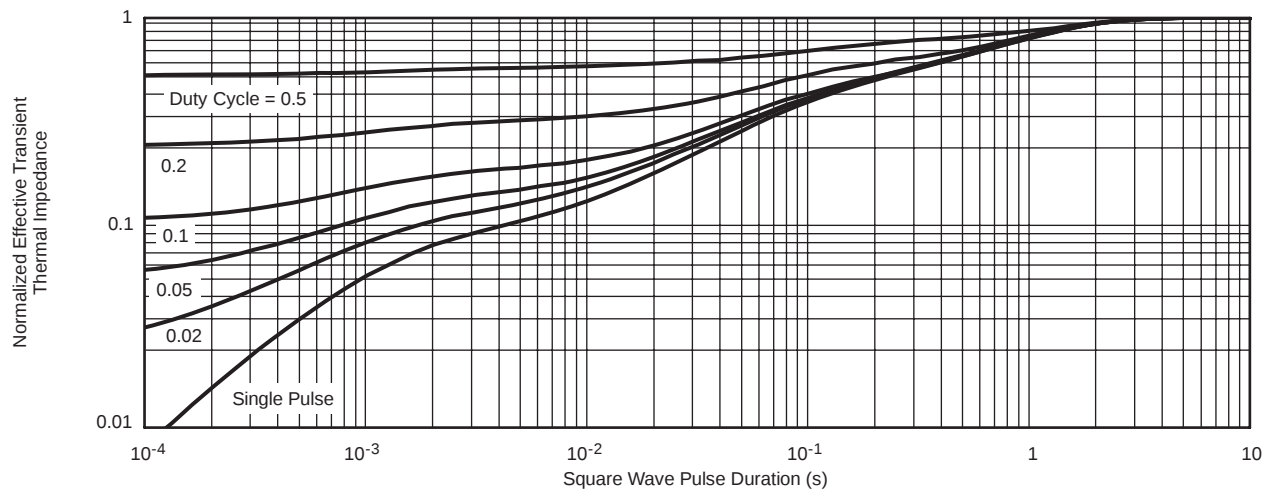
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Current Derating*

Power, Junction-to-Foot

Power Derating, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



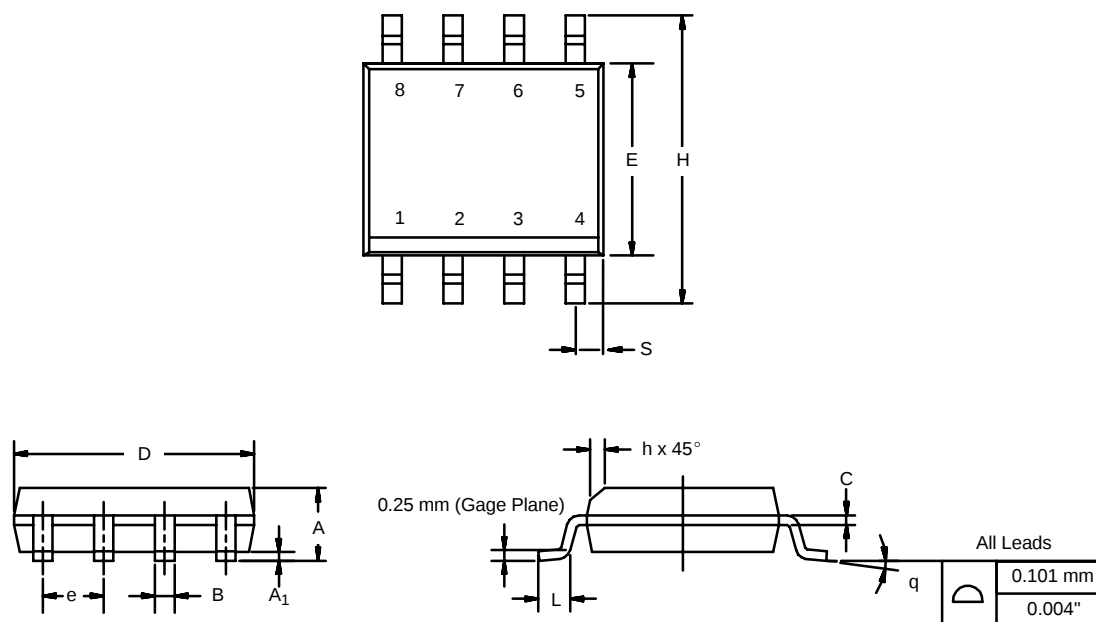
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

SOIC (NARROW): 8-LEAD

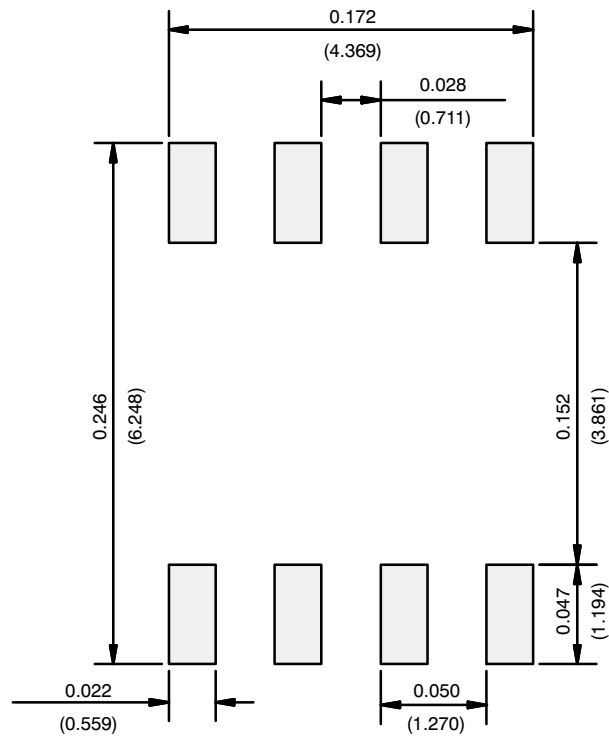
JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A ₁	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026

ECN: C-06527-Rev. I, 11-Sep-06
DWG: 5498

RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads
Dimensions in Inches/(mm)

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