

Dual N-Channel 60-V (D-S) MOSFET

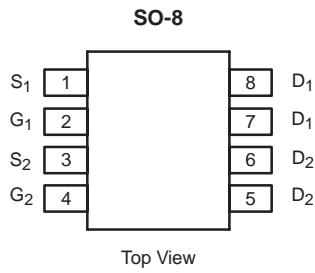
PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ.)
60	0.012 at $V_{GS} = 10$ V	10	15 nC
	0.015 at $V_{GS} = 4.5$ V	9.0	

FEATURES

- Halogen-free According to IEC 61249-2-21
- Definition
- Trench Power MOSFET
- 100 % UIS Tested
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC

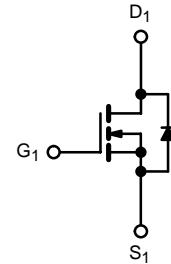


RoHS
COMPLIANT
HALOGEN
FREE

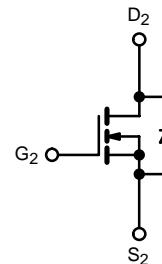


APPLICATIONS

- Set Top Box
- Low Current DC/DC



N-Channel MOSFET



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C)	I_D	10 ^a	A
		8.2	
		7.2 ^{b, c}	
		6.2 ^{b, c}	
Pulsed Drain Current	I_{DM}	40	
Continuous Source-Drain Diode Current	I_S	2.25	
		1.48 ^{b, c}	
Single Pulse Avalanche Current	I_{AS}	30	mJ
Single Pulse Avalanche Energy	E_{AS}	55	
Maximum Power Dissipation	P_D	2.7	W
		4.77	
		2.78 ^{b, c}	
		1.14 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{a, c, d}	R_{thJA}	58	70	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	38	

Notes:

- a. Package limited, $T_C = 25$ °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. $t = 10$ s.
- d. Maximum under Steady State conditions is 110 °C/W.

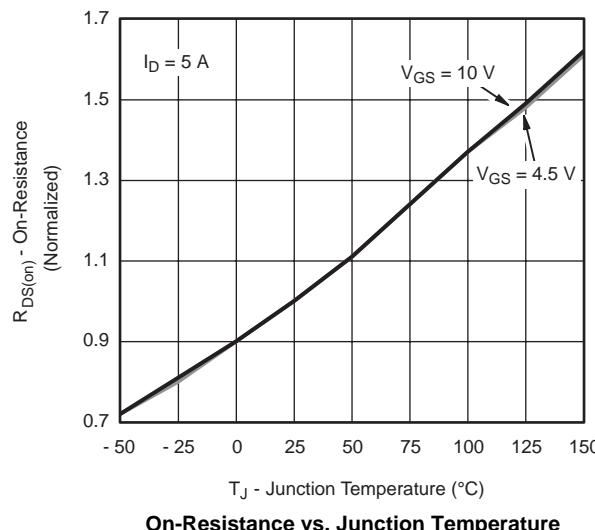
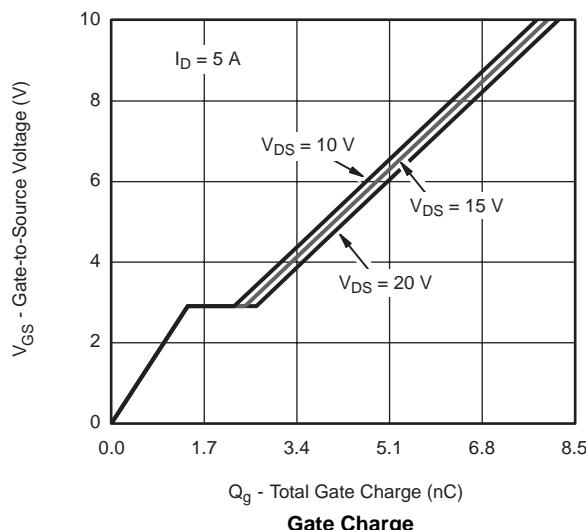
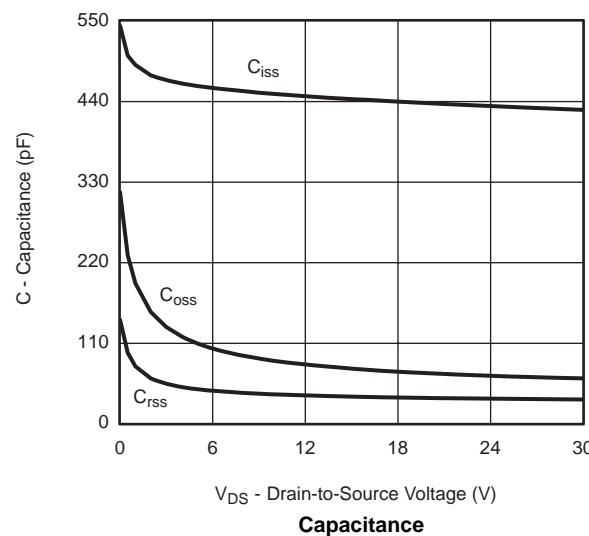
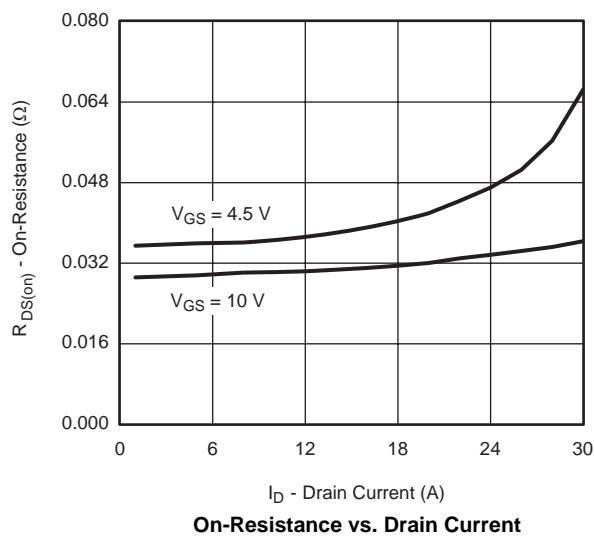
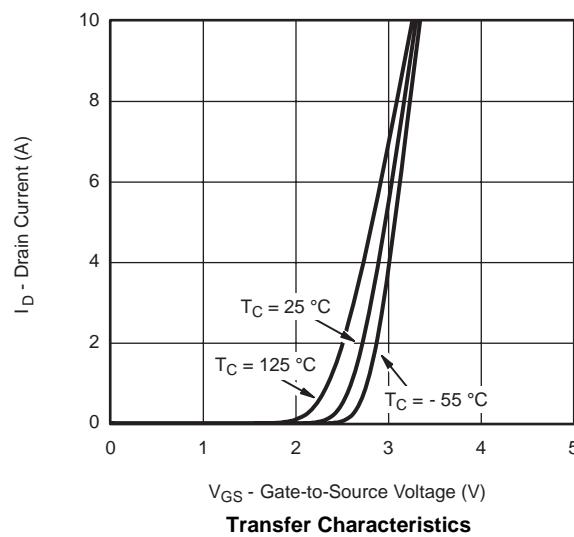
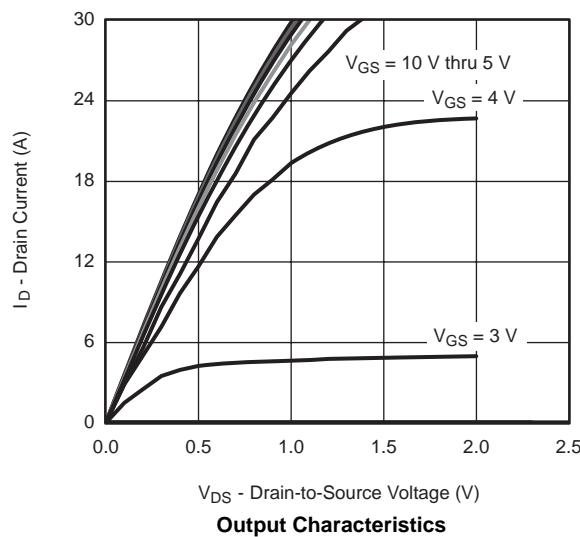
SPECIFICATIONS $T_J = 25^\circ\text{C}$, unless otherwise noted

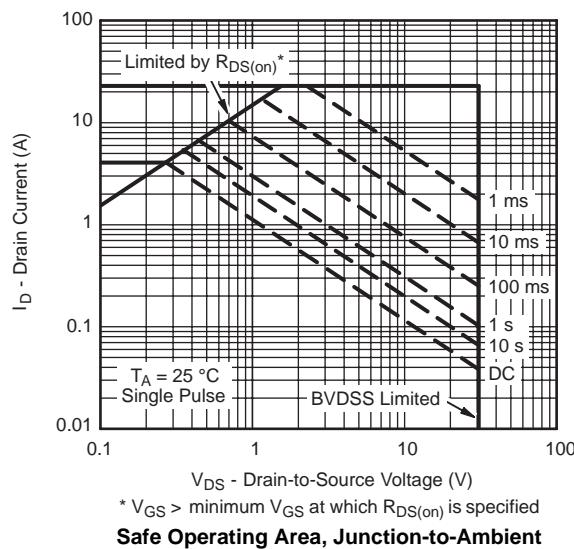
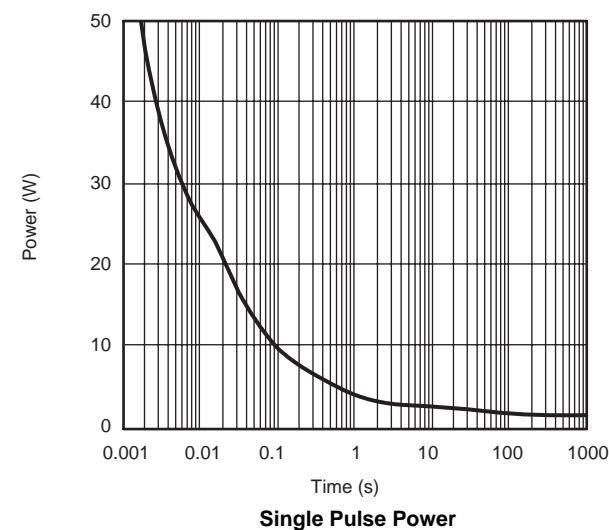
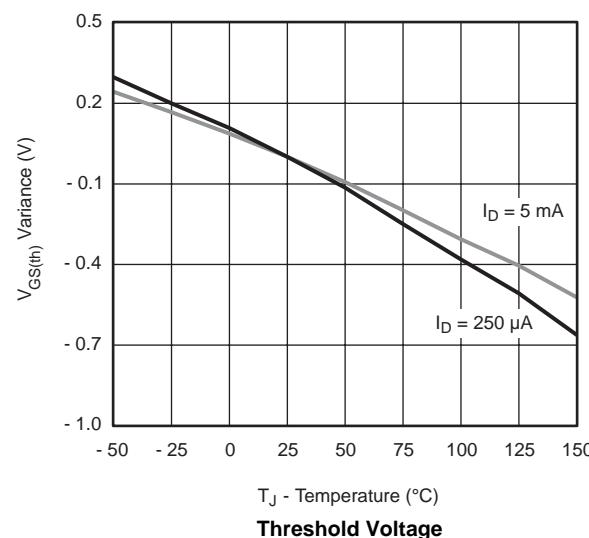
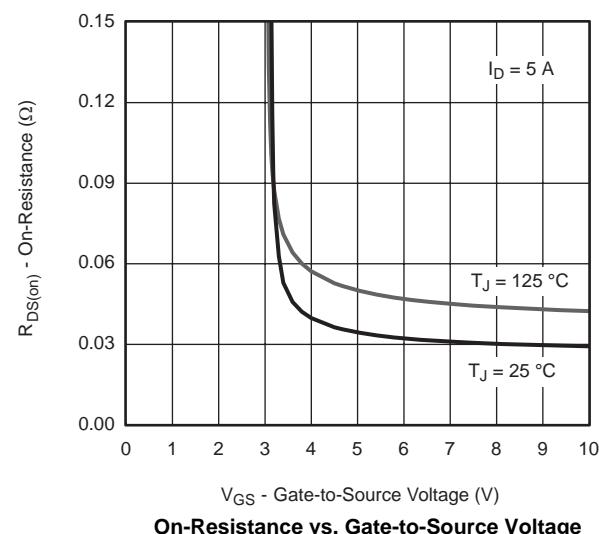
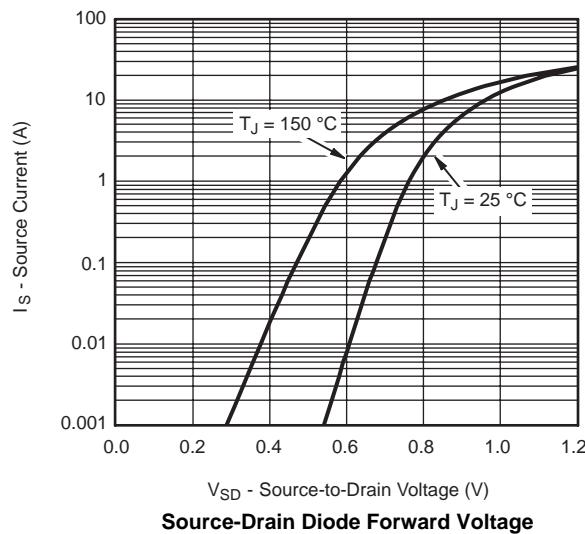
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}$, $I_D = 250 \mu\text{A}$	60			V	
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250 \mu\text{A}$		32		mV/°C	
$V_{GS(\text{th})}$ Temperature Coefficient	$\Delta V_{GS(\text{th})}/T_J$			- 5.0			
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu\text{A}$	1.0		2.5	V	
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}$, $V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 30 \text{ V}$, $V_{GS} = 0 \text{ V}$			1	μA	
		$V_{DS} = 30 \text{ V}$, $V_{GS} = 0 \text{ V}$, $T_J = 55^\circ\text{C}$			10		
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{DS} \geq 5 \text{ V}$, $V_{GS} = 10 \text{ V}$	10			A	
Drain-Source On-State Resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}$, $I_D = 5 \text{ A}$		0.012		Ω	
		$V_{GS} = 4.5 \text{ V}$, $I_D = 4 \text{ A}$		0.015			
Forward Transconductance ^a	g_{fs}	$V_{DS} = 10 \text{ V}$, $I_D = 5 \text{ A}$		16		S	
Dynamic^b							
Input Capacitance	C_{iss}	$V_{DS} = 15 \text{ V}$, $V_{GS} = 0 \text{ V}$, $f = 1 \text{ MHz}$		586		pF	
Output Capacitance	C_{oss}			217			
Reverse Transfer Capacitance	C_{rss}			155			
Total Gate Charge	Q_g	$V_{DS} = 15 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 5 \text{ A}$		15	25	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}$, $V_{GS} = 4.5 \text{ V}$, $I_D = 5 \text{ A}$		6.7	9.6		
Gate-Drain Charge	Q_{gd}			4.4	6.0		
Gate Resistance	R_g		$f = 1 \text{ MHz}$	3.05	4.0		
Turn-On Delay Time	$t_{d(\text{on})}$			0.8	4.3	8.6	Ω
Rise Time	t_r	$V_{DD} = 15 \text{ V}$, $R_L = 3 \Omega$ $I_D \approx 5 \text{ A}$, $V_{GEN} = 4.5 \text{ V}$, $R_g = 1 \Omega$			12	24	ns
Turn-Off Delay Time	$t_{d(\text{off})}$				55	100	
Fall Time	t_f				11	22	
Turn-On Delay Time	$t_{d(\text{on})}$				8	16	
Rise Time	t_r	$V_{DD} = 15 \text{ V}$, $R_L = 3 \Omega$ $I_D \approx 5 \text{ A}$, $V_{GEN} = 10 \text{ V}$, $R_g = 1 \Omega$			4	8	
Turn-Off Delay Time	$t_{d(\text{off})}$				9	18	
Fall Time	t_f				10	20	
					6	12	
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I_S	$T_C = 25^\circ\text{C}$			2.25	A	
Pulse Diode Forward Current	I_{SM}				24		
Body Diode Voltage	V_{SD}	$I_S = 2 \text{ A}$, $V_{GS} = 0 \text{ V}$		0.8	1.2	V	
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 5 \text{ A}$, $di/dt = 100 \text{ A}/\mu\text{s}$, $T_J = 25^\circ\text{C}$			11	20	ns
Body Diode Reverse Recovery Charge	Q_{rr}				4	8	nC
Reverse Recovery Fall Time	t_a				7		ns
Reverse Recovery Rise Time	t_b				4		

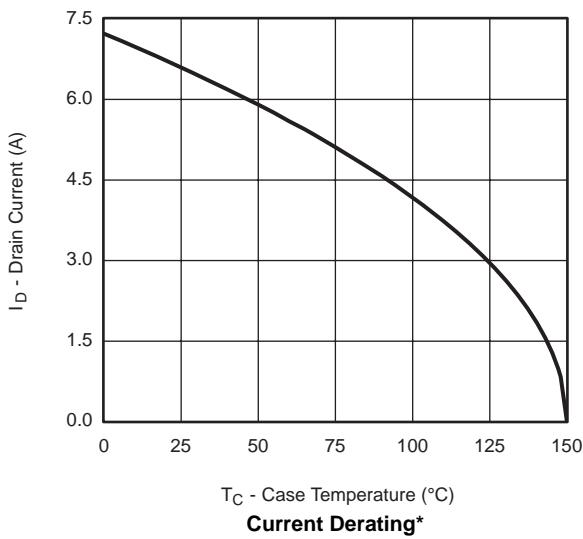
Notes:

a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$
b. Guaranteed by design, not subject to production testing.

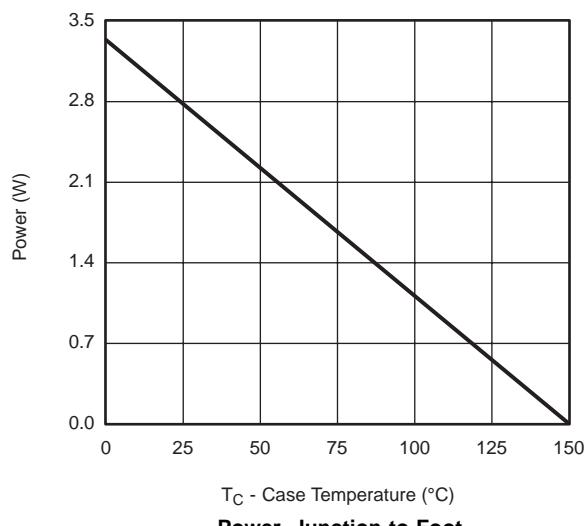
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted


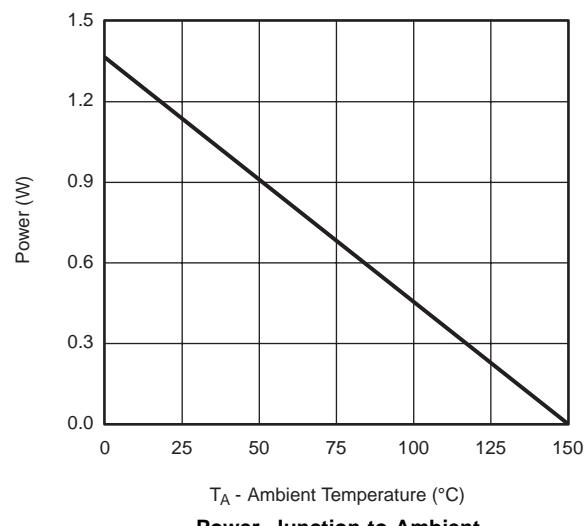
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

T_C - Case Temperature (°C)
Current Derating*

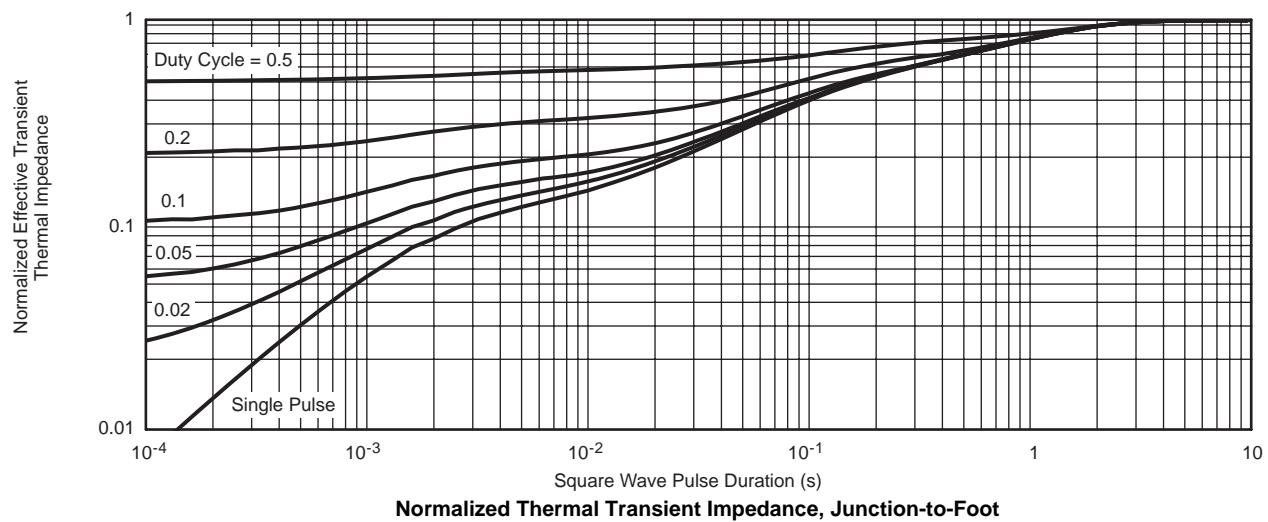
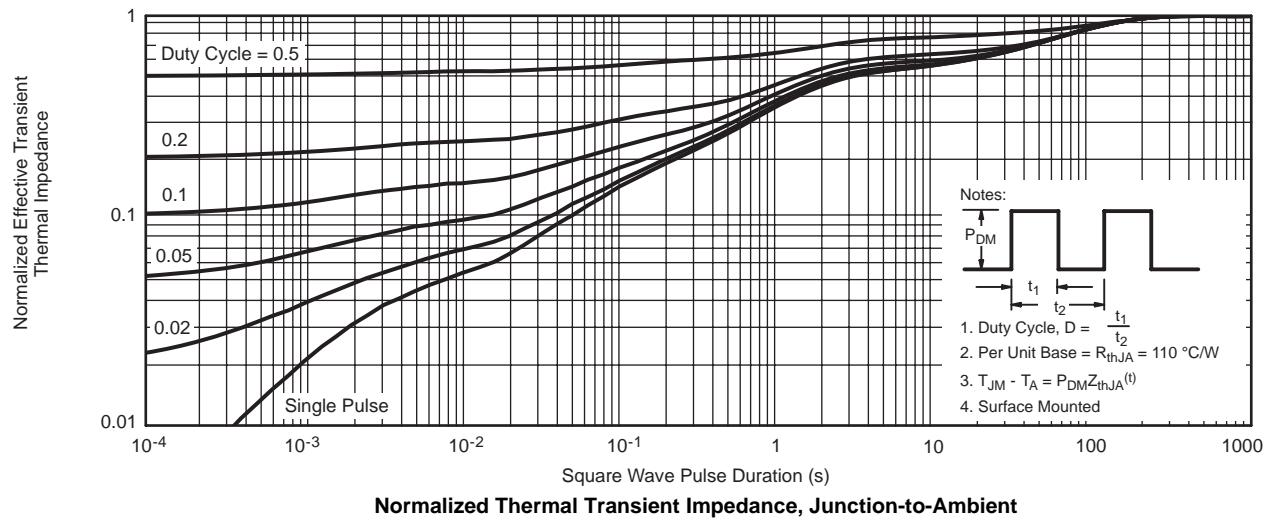


T_C - Case Temperature (°C)
Power, Junction-to-Foot



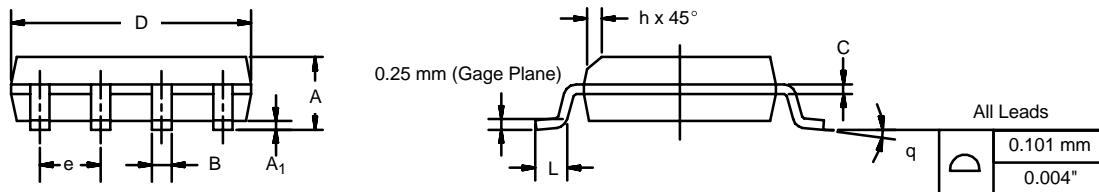
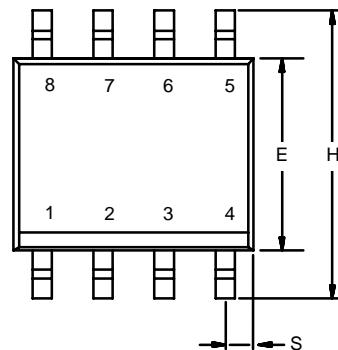
T_A - Ambient Temperature (°C)
Power, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(\max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted


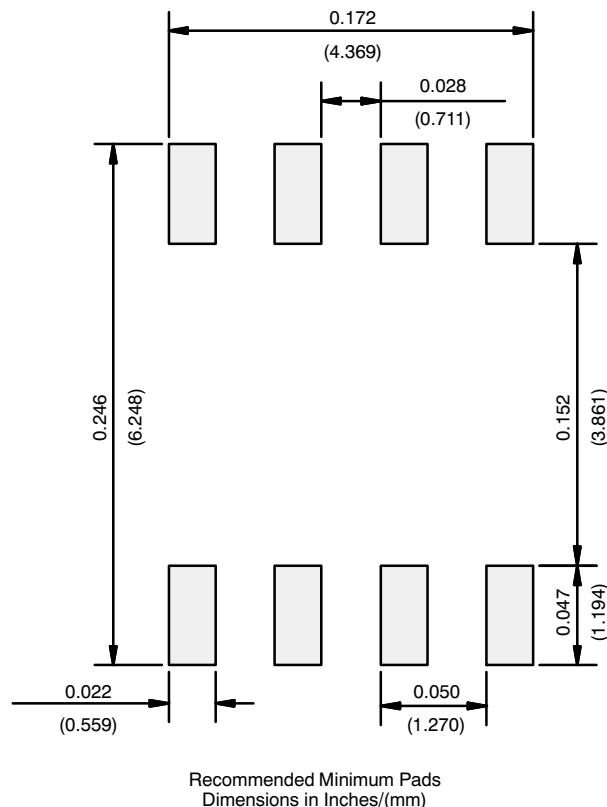
SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A ₁	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06				
DWG: 5498				

RECOMMENDED MINIMUM PADS FOR SO-8

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