

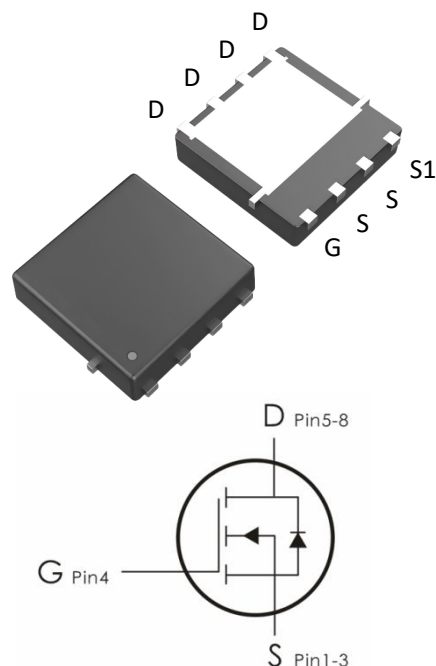
Description:

This N-Channel MOSFET uses advanced SGT technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=30V, I_D=95A, R_{DS(ON)}<2.5m\Omega @V_{GS}=10V$ (Typ: $1.75m\Omega$)
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.
- 6) MSL3



Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DOZ95N03T	95N03T	DFN3*3-8	5000 pcs/Reel

Absolute Maximum Ratings: ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current ¹	95	A
	Continuous Drain Current- $T_c=100^\circ\text{C}$ ¹	66.5	
I_{DM}	Pulsed Drain Current ²	380	
P_D	Power Dissipation	59.5	W
E_{AS}	Single pulse avalanche energy ³	225	mJ
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55-+150	$^\circ\text{C}$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	2.1	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	50	$^\circ\text{C}/\text{W}$

Electrical Characteristics: ($T_c=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	30	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =30V	---	---	1	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	Gate-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	1.2	1.5	2.2	V
R _{DS(ON)}	Drain-Source On Resistance ⁴	V _{GS} =10V, I _D =30A	---	1.75	2.5	m Ω
		V _{GS} =4.5V, I _D =20A	---	2.5	4	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1MHz	---	1980	---	pF
C _{oss}	Output Capacitance		---	1406	--	
C _{rss}	Reverse Transfer Capacitance		---	86	---	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} =20V, I _D =30A, R _G =1.6 Ω ,V _{GS} =10V	---	18	---	ns
t _r	Rise Time		---	4.1	---	ns
t _{d(off)}	Turn-Off Delay Time		---	55	---	ns
t _f	Fall Time		---	11.1	---	ns
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =20V, I _D =30A	---	36	---	nC
Q _{gs}	Gate-Source Charge		---	8	---	nC
Q _{gd}	Gate-Drain “Miller” Charge		---	5.9	---	nC
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _{SD} =30A	---	---	1.2	V
I _S	Continuous Drain Current	V _D =V _G =0V	---	---	95	A
I _{SM}	Pulsed Drain Current		---	---	380	A
T _{rr}	Reverse Recovery Time	I _F =30A, T _J =25 °C	---	85	---	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/us	---	34	---	nC

Notes:

1. Computed continuous current assumes the condition of $T_{j,Max}$ while the actual continuous current depends on the thermal & electro-mechanical application board design
2. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
3. EAS condition : $T_J=25^{\circ}C$, $V_{DD}=20V$, $V_G=10V$, $L=0.5mH$
4. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Characteristics: ($T_c=25^{\circ}C$ unless otherwise noted)

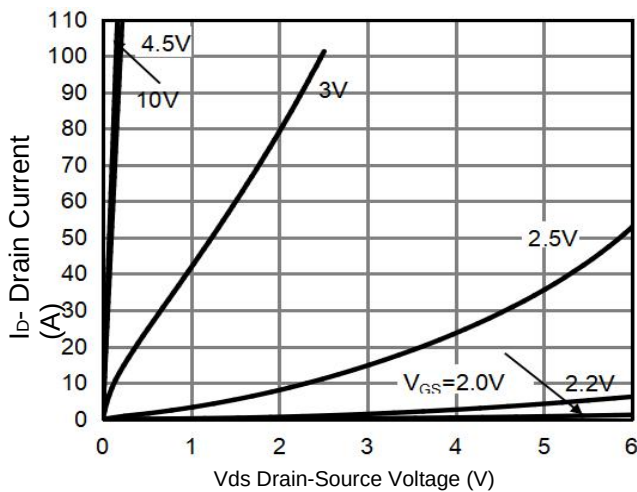


Fig 1: Output Characteristics

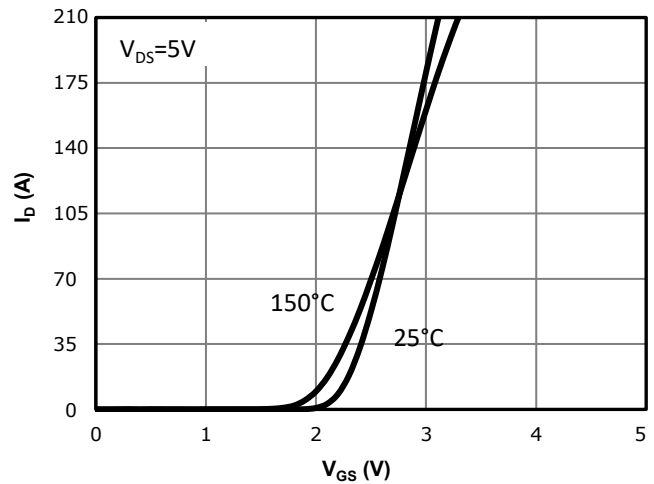


Fig 2: Transfer Characteristics

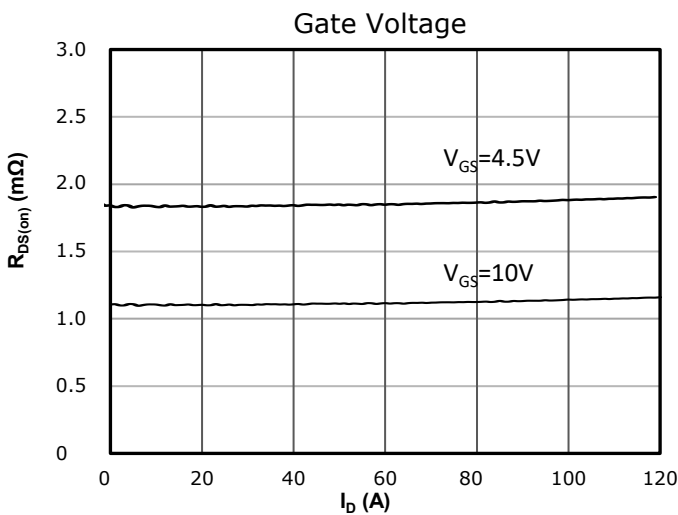


Fig 3: $R_{ds(on)}$ vs Drain Current and

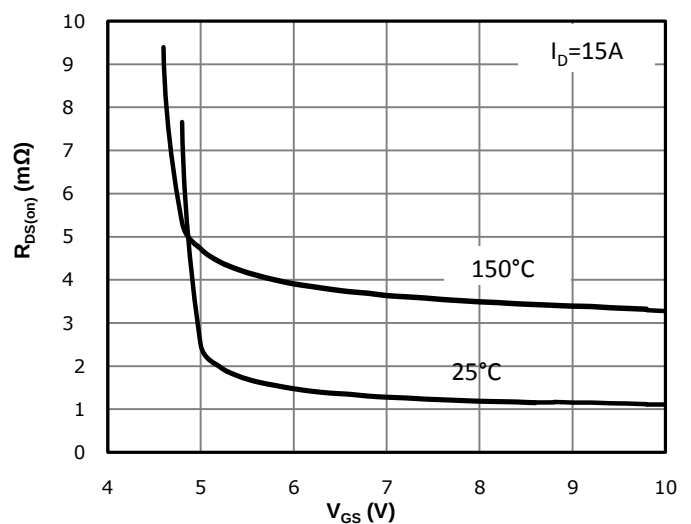


Fig 4: $R_{ds(on)}$ vs Gate Voltage

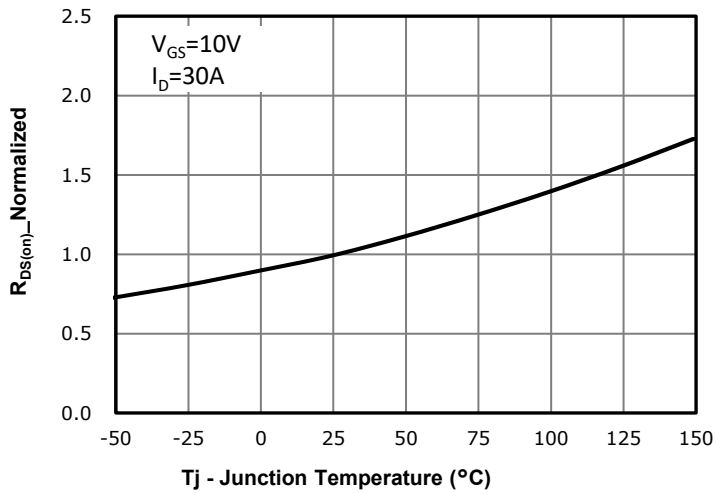


Fig 5: Rds(on) vs. Temperature

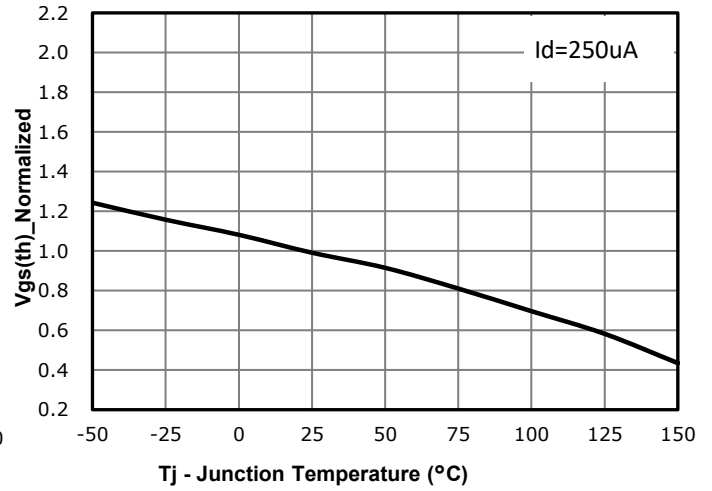


Fig 6: Vgs(th) vs. Temperature

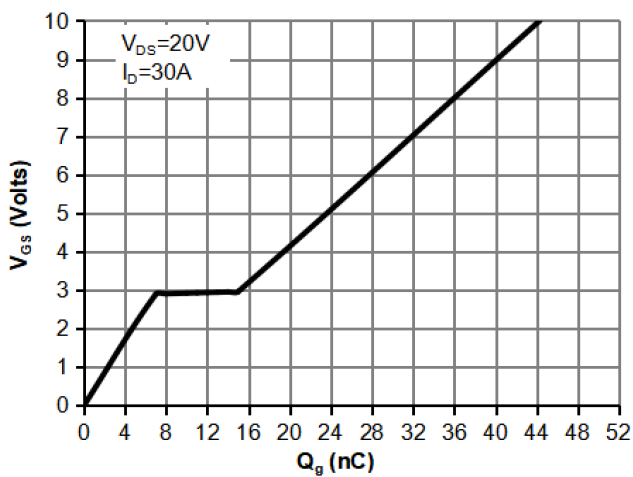


Fig 7: Gate Charge Characteristics

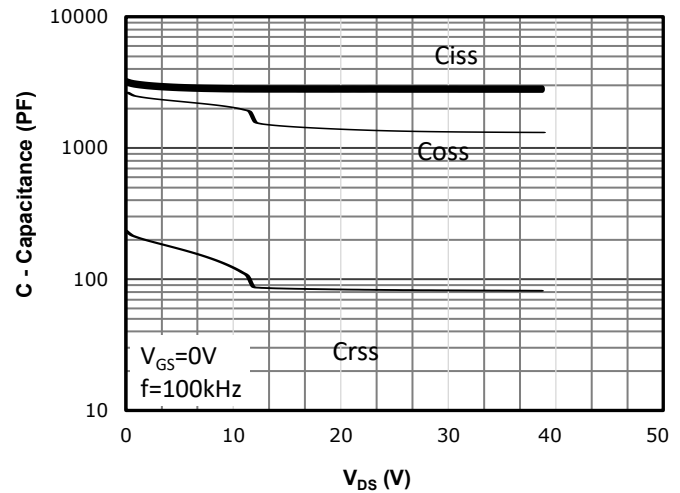


Fig 8: Capacitance Characteristics

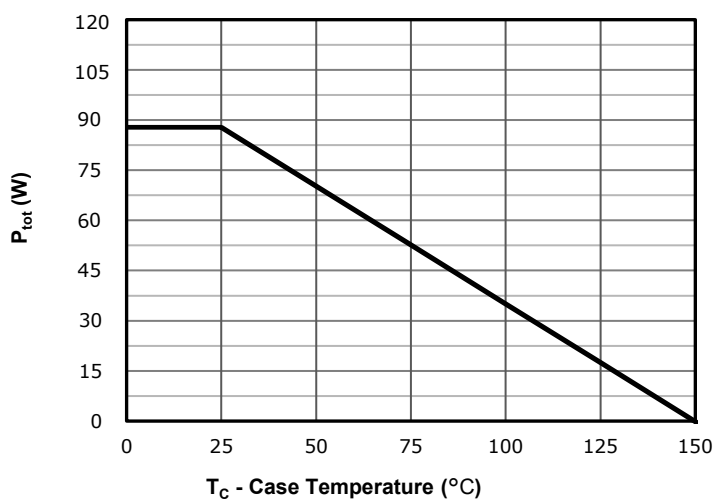


Fig 9: Power Dissipation

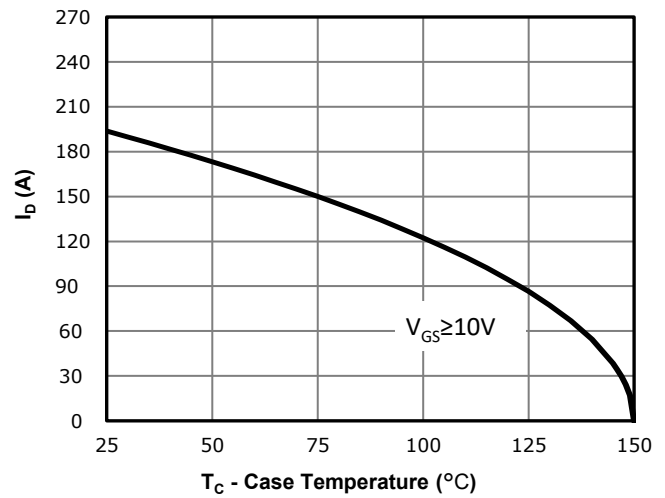


Fig 10: Drain Current Derating

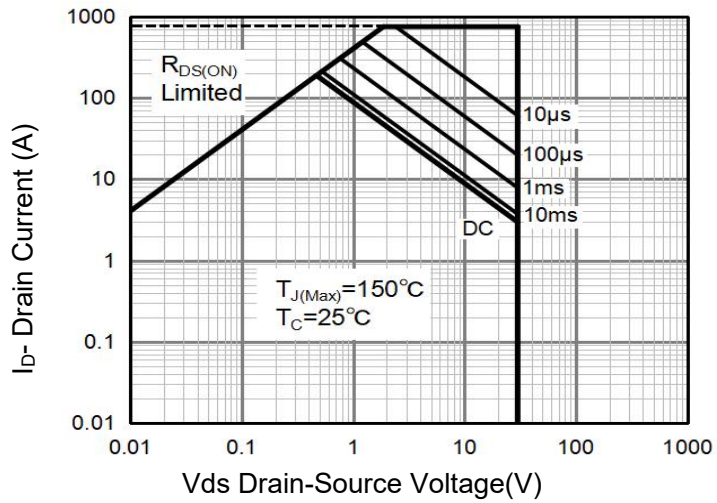
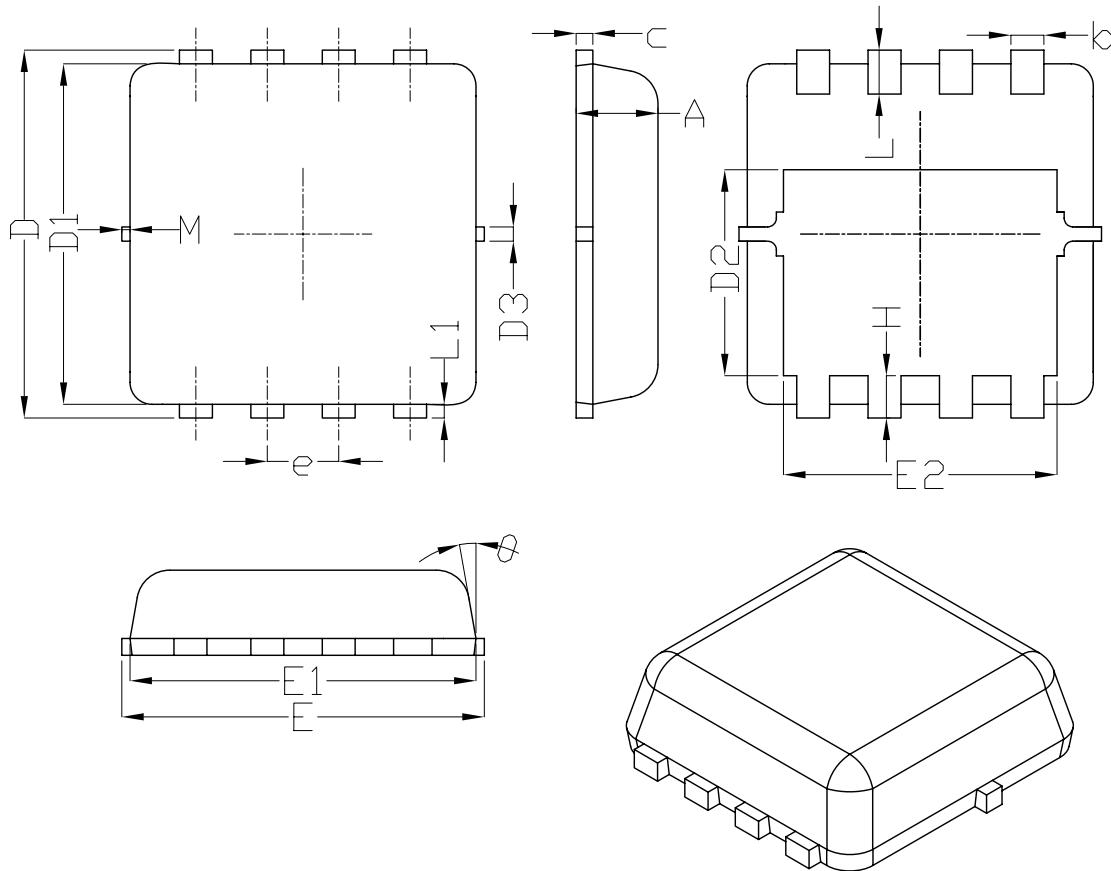
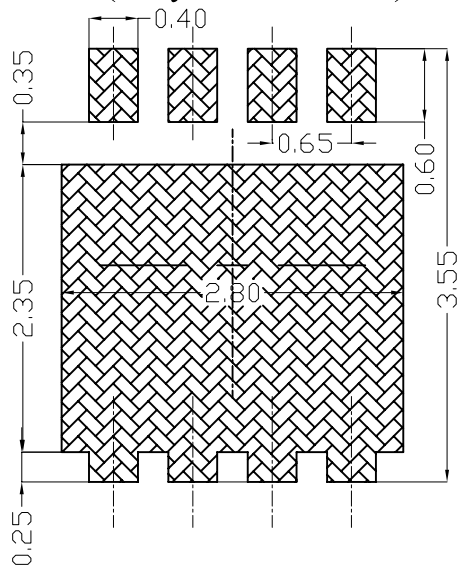


Figure 11 Safe Operation Area

DFN3X3-8 Package Outline Data


**Land Pattern
(Only for Reference)**



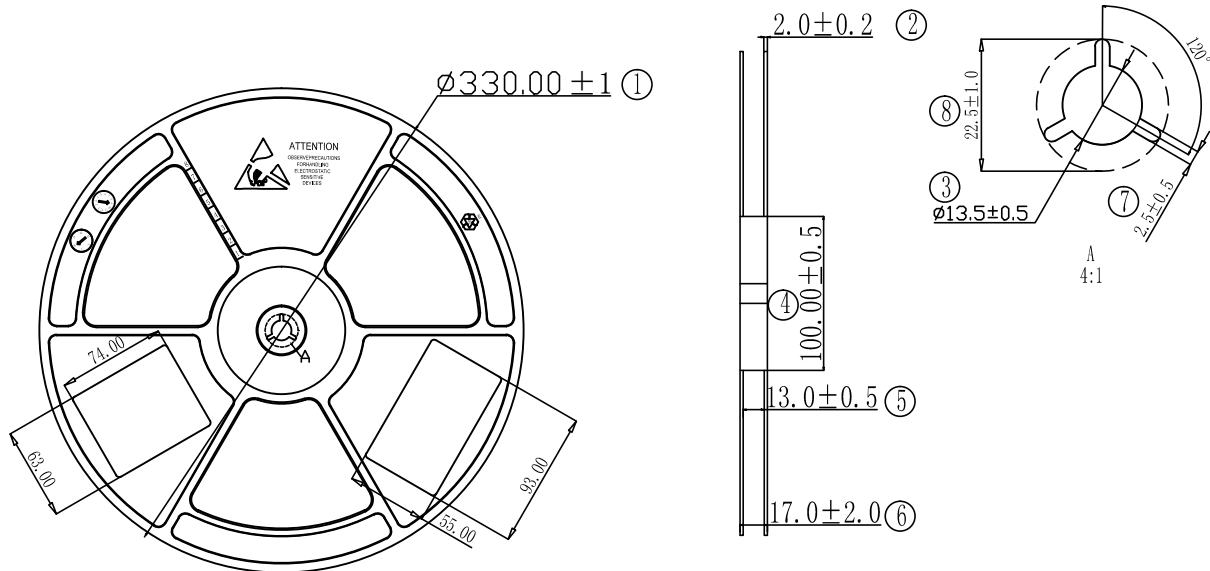
UNIT: mm

SYMBOL	DIMENSIONAL REQMTS		
	MIN	NOM	MAX
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.78	1.88	1.98
D3	---	0.13	---
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	---	0.13	---
θ	---	10°	12°
M	*	*	0.15

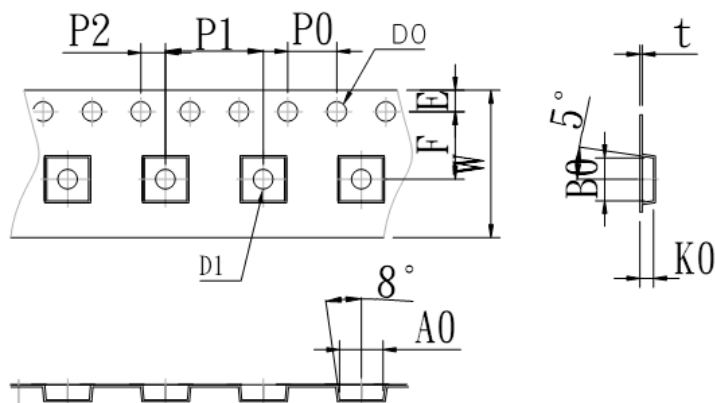
* Not specified

Tape & Reel Information

Dimensions in mm



Symbol	A0	B0	K0	D0	D1	P0	P1	10*P0
Spec	3.55 $\pm$ 0.10	3.45 $\pm$ 0.10	1.13 $\pm$ 0.10	1.55 $\pm$ 0.10	1.55 $\pm$ 0.10	4.00 $\pm$ 0.10	8.00 $\pm$ 0.10	40.0 $\pm$ 0.10
Symbol	W	E	F	P2	t			
Spec	12.00 $\pm$ 0.10	1.75 $\pm$ 0.10	5.50 $\pm$ 0.10	2.00 $\pm$ 0.10	0.20 $\pm$ 0.05			



Pulling direction 

Marking Information:

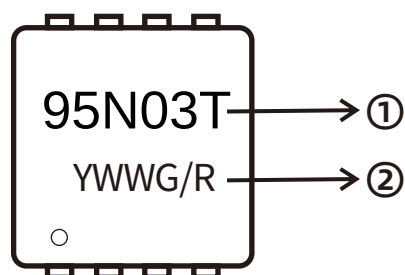
①. Part NO.

②. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)



Previous Version

Version	Date	Subjects (major changes since last revision)
1.0	2025-01-13	Release of final version

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