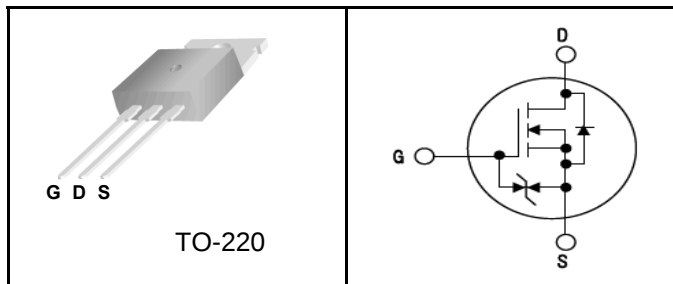


General Description

This Power MOSFET is produced using WPMtek's advanced Super-Junction MOSFET technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies.

Features

- 20A, 650V, $R_{DS(on)} = 180m\Omega @ V_{GS} = 10V$
- Low gate charge (typ. $Q_g = 30.2nC$)
- High ruggedness Ultra
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



Device	Package	Marking	Packaging
WTM20N65CP	TO-220	WPM 20N65CP XXX YYWW	50pcs/Tube

Absolute Maximum Ratings $T_C = 25^\circ C$ unless otherwise noted

Symbol	Parameter	WTM20N65AP	Units
V_{DSS}	Drain-Source Voltage	650	V
I_D	Drain Current - Continuous ($T_C = 25^\circ C$)	20 *	A
	- Continuous ($T_C = 100^\circ C$)	12*	A
I_{DM}	Drain Current - Pulsed (Note 1)	57 *	A
V_{GSS}	Gate-Source Voltage	± 30	V
EAS	Single Pulsed Avalanche Energy (Note 2)	76	mJ
I_{AR}	Avalanche Current (Note 1)	4	A
E_{AR}	Repetitive Avalanche Energy (Note 1)	1.62	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	20	V/ns
	MOSFET dv/dt	100	
P_D	Power Dissipation ($T_C = 25^\circ C$)	36	W
	- Derate above $25^\circ C$	0.29	W/ $^\circ C$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ C$
T_L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	$^\circ C$

* Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	WTM20N65CP	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	3.5	$^\circ C/W$
$R_{\theta JS}$	Thermal Resistance, Case-to-Sink Typ.	-	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	$^\circ C/W$

Electrical Characteristics

$T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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Off Characteristics

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 0.25\text{mA}$	650	--	--	V
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 0.25\text{mA}, T_J = 150^\circ\text{C}$	650	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$	--	--	1	μA
		$V_{DS} = 480\text{ V}, T_C = 125^\circ\text{C}$	--	2	--	μA
I_{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 0.25\text{mA}$	2.5	--	4.5	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 10\text{ A}$	--	150	180	$\text{m}\Omega$

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 400\text{ V}, V_{GS} = 0\text{ V},$ $f = 1\text{MHz}$	--	1240	--	pF
C_{oss}	Output Capacitance		--	34	--	pF
C_{rss}	Reverse Transfer Capacitance		--	--	--	pF

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DS} = 400\text{ V}, I_D = 10\text{ A},$ $R_G = 10\ \Omega, V_{GS} = 10\text{ V}$ (Note 4, 5)	--	12	--	ns
t_r	Turn-On Rise Time		--	8	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	53	--	ns
t_f	Turn-Off Fall Time		--	10	--	ns
Q_g	Total Gate Charge	$V_{DS} = 400\text{ V}, I_D = 10\text{ A},$ $V_{GS} = 10\text{ V}$ (Note 4, 5)	--	30.2	--	nC
Q_{gs}	Gate-Source Charge		--	5.8	--	nC
Q_{gd}	Gate-Drain Charge		--	15.4	--	nC
R_G	Gate Resistance	$f = 1\text{MHz}$		1.3		Ω

Drain-Source Diode Characteristics and Maximum Ratings

I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	20	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	57	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 10 A	--	--	1.2	V
t _{rr}	Reverse Recovery Time	V _{DD} = 400 V, I _S = 10A, dI _F / dt = 100 A/us (Note 4)	--	274	--	ns
Q _{rr}	Reverse Recovery Charge		--	3.33	--	uC

Notes:

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2. $I_{AS} = 4\text{A}, V_{DD} = 50\text{V}, R_G = 25\ \Omega$, Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq 10\text{A}, di/dt \leq 200\text{A/us}, V_{DD} \leq 400$ Starting $T_J = 25^\circ\text{C}$
4. Pulse Test : Pulse width $\leq 300\ \mu\text{s}$, Duty cycle $\leq 2\%$
5. Essentially independent of operating temperature

Typical Characteristics

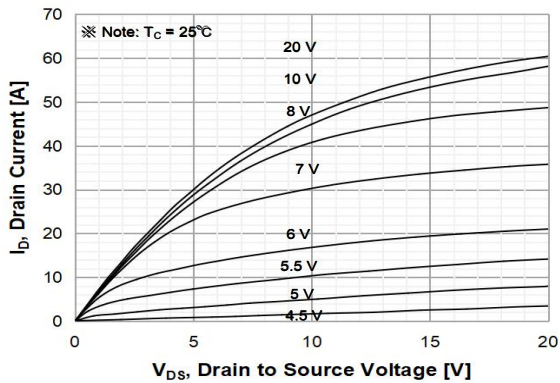


Figure 1. On-Region Characteristics

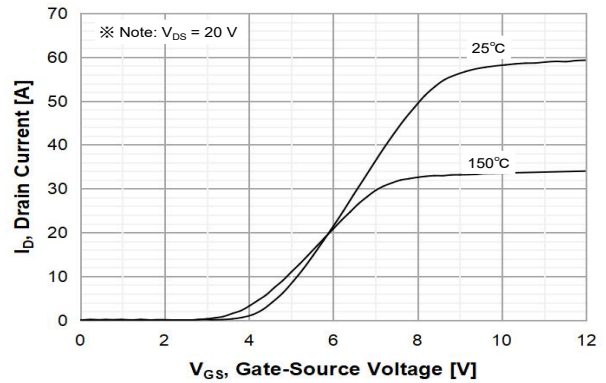


Figure 2. Transfer Characteristics

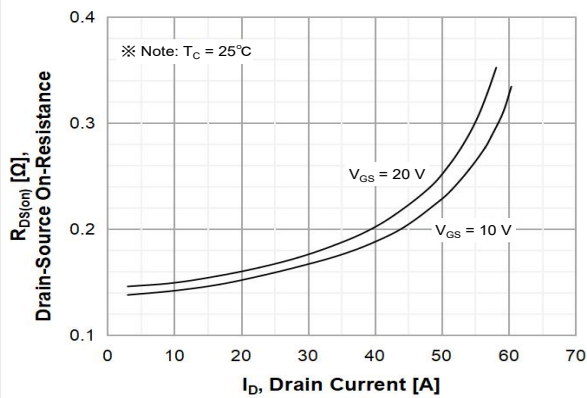


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

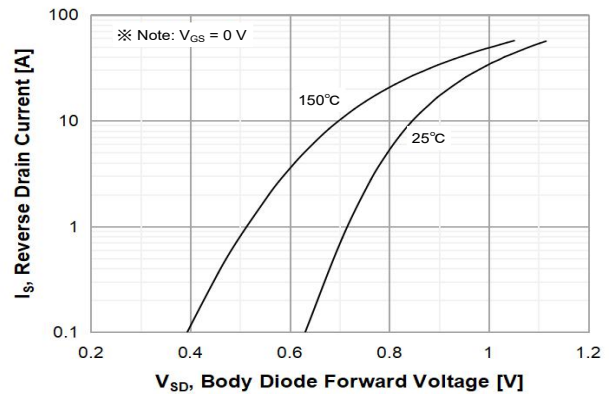


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

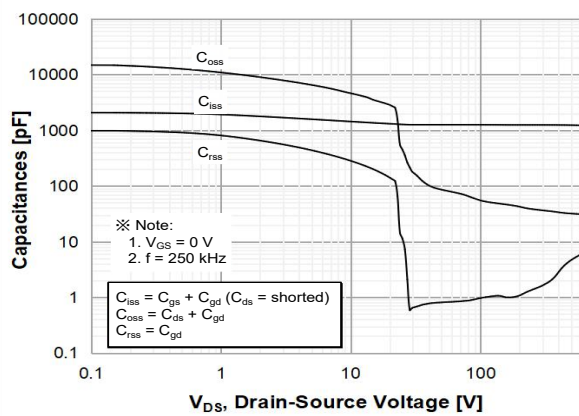


Figure 5. Capacitance Characteristics

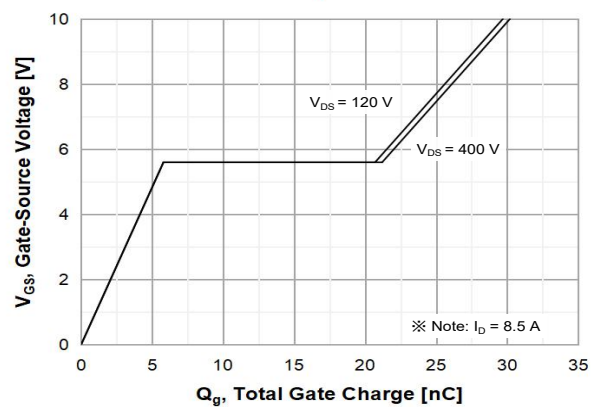


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

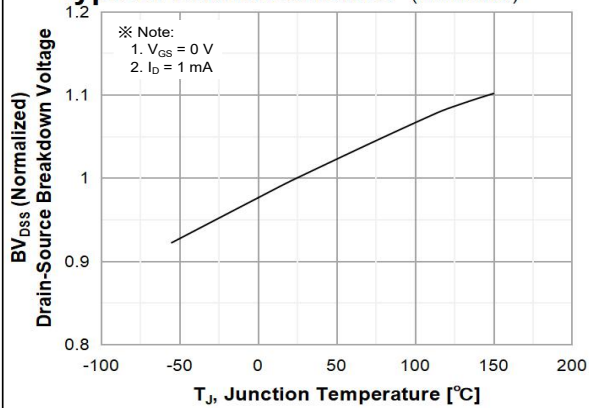


Figure 7. Breakdown Voltage Variation vs Temperature

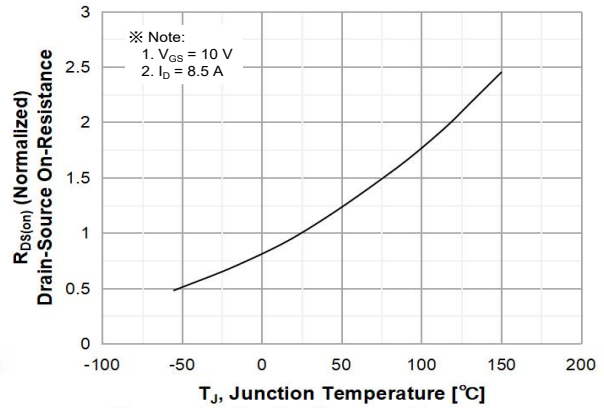


Figure 8. On-Resistance Variation vs Temperature

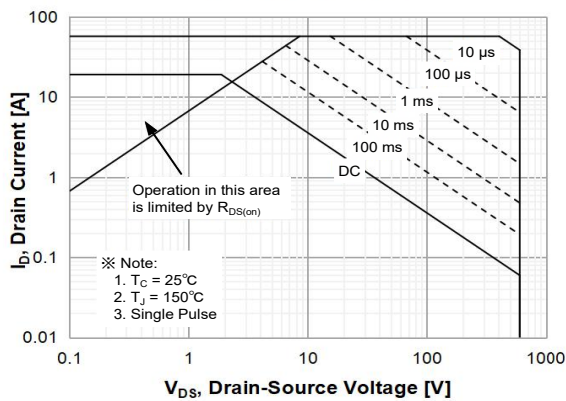


Figure 9. Maximum Safe Operating Area

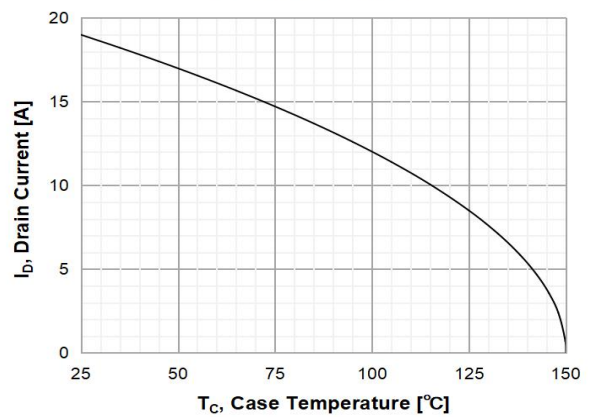


Figure 10. Maximum Drain Current vs. Case Temperature

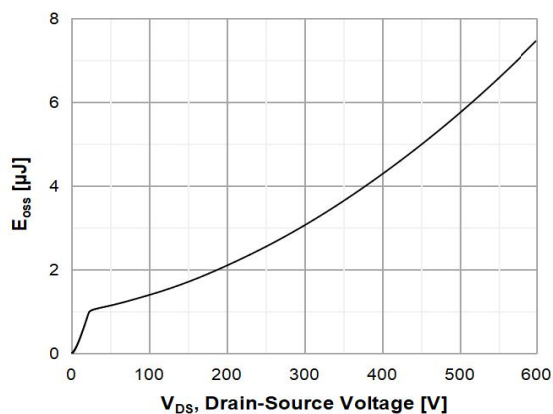


Figure 11. E_{OSS} vs. Drain to Source Voltage

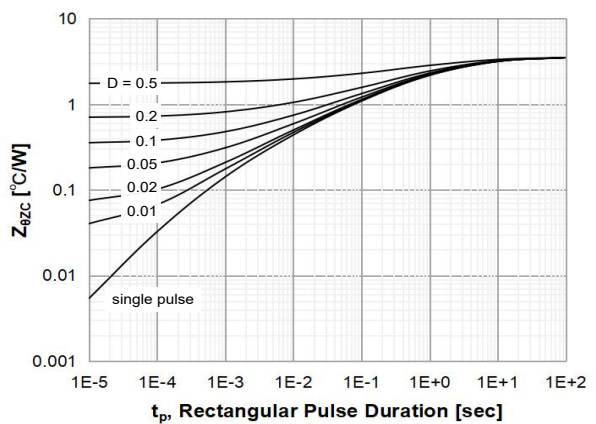
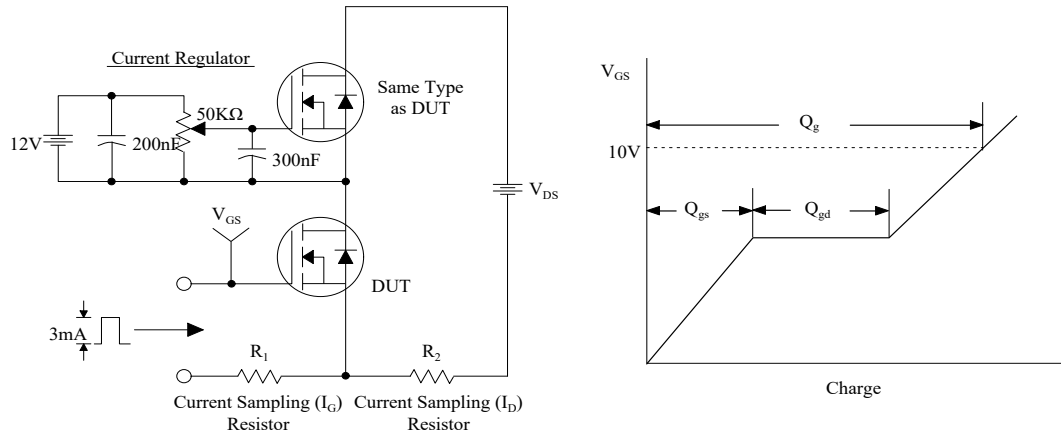
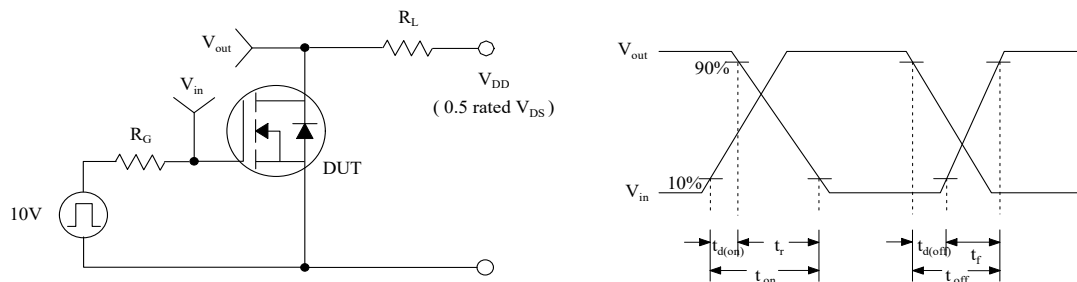


Figure 12. Transient Thermal Response Curve

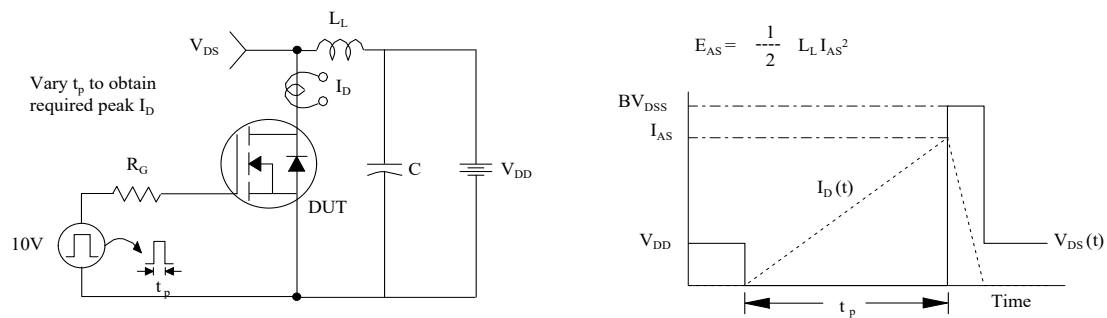
Gate Charge Test Circuit & Waveform



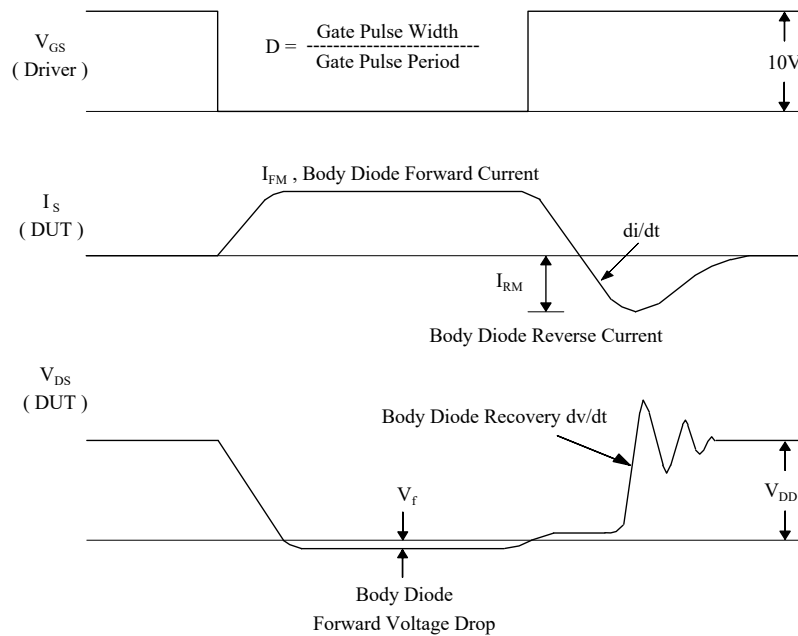
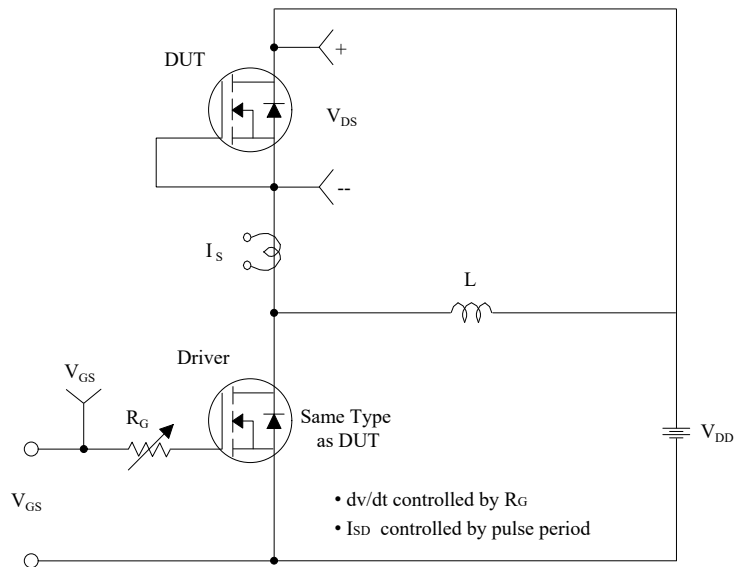
Resistive Switching Test Circuit & Waveforms



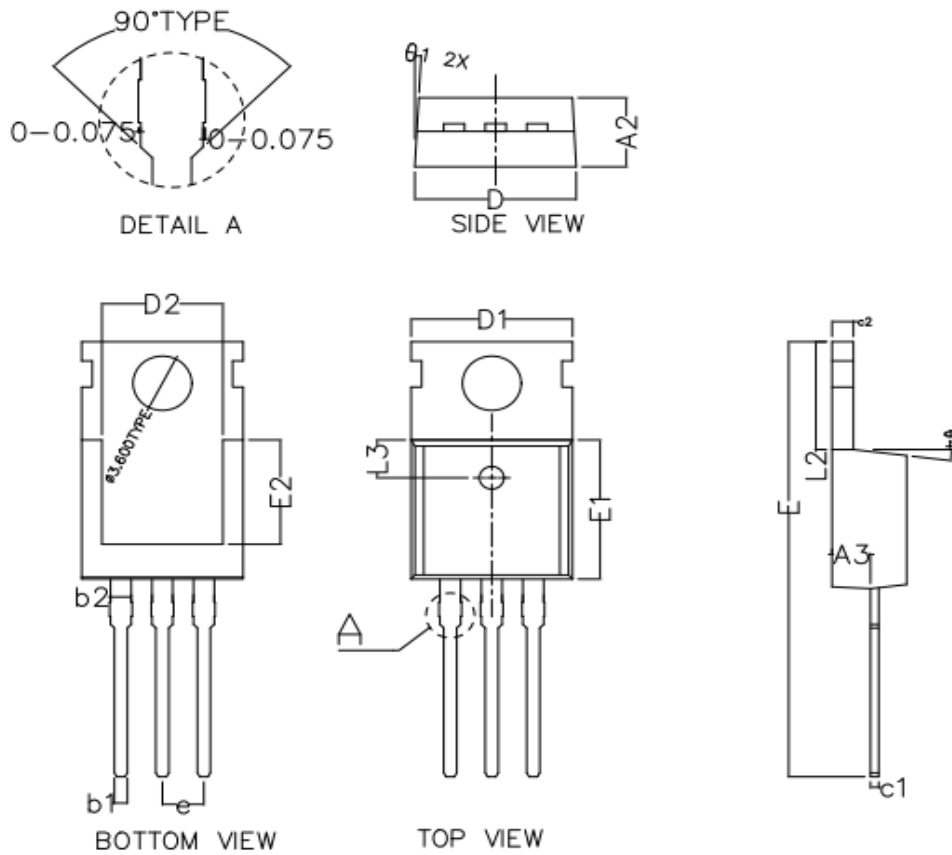
Unclamped Inductive Switching Test Circuit & Waveforms



Peak Diode Recovery dv/dt Test Circuit & Waveforms



TO220 Outline



COMMON DIMENSIONS (UNITS OF MEASURE IS mm)			
	MIN	NORMAL	MAX
A2	4.470	4.570	4.670
A3	2.300	2.350	2.400
b1	0.750	0.800	0.850
b2	1.27 TYPE		
c1	0.450	0.500	0.550
c2	1.250	1.300	1.380
▲ D	9.900	10.000	10.100
▲ D1	10.000TYPE		
▲ D2	8.000TYPE		
▲ E	28.660	28.860	29.060
▲ E1	9.000	9.100	9.200
▲ E2	7.000TYPE		
e	2.540TYPE		
L2	6.350	6.500	6.650
L3	2.50TYPE		
θ1	3° TYPE		
θ2	3° TYPE		
θ3	7° TYPE		
θ4	7° TYPE		

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