

Harvatek 3.1mm Round LED LAMP with Holder**HV-311050/305/SUR-TR1**

Official Product	HV-311050/305/SUR-TR1	Customer Part No.		Data Sheet No.
	*****	*****		HV-311050/305/SUR-TR1
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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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Compliance and Certification

ISO9002, QS9000 and ISO14001 Certified
RoHS Compliant



Orderable Information

H V - 311050 / 305 / SUR - TR1

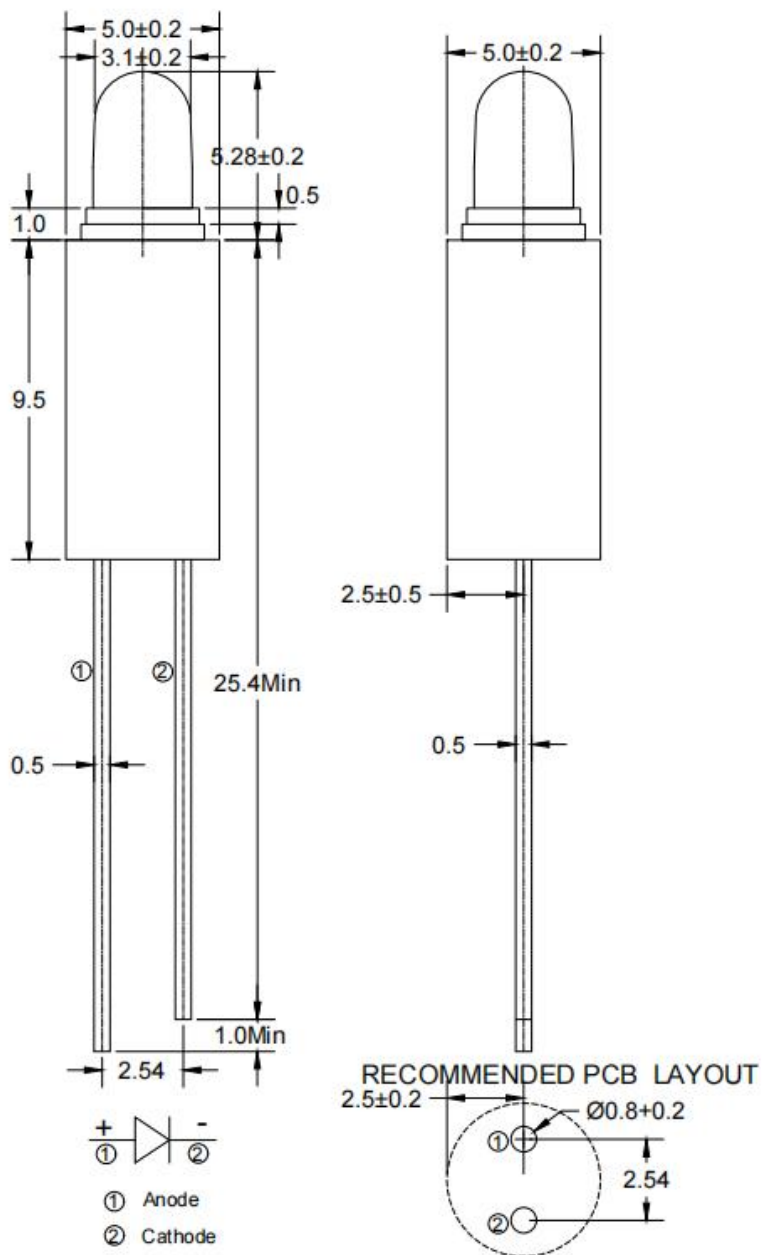
Series Name	Color Code	Remark
HV : HARVATEK	311050:Array 1 Lamp 305: 3.1mm Round LED LAMP. SUR: GaP 620nm Red Chip. TR1:HARVATEK Part No.	

Features:

- Stable Color
- Popular 3.1mm through hole package.
- Red Diffused Lens.

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Package Dimensions:



Notes:

1. All dimensions are millimeters.
2. Tolerance is $\pm 0.25 \text{ mm}$ unless otherwise noted.
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Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Rating	Unit
Forward Current	I_F	30	mA
Operating Temperature	T_{opr}	-40to+85	°C
Storage Temperature	T_{stg}	-40to+85	°C
Soldering Temperature*1	T_{sol}	260±5	°C
Power Dissipation	P_d	75	mW
Reverse Voltage	V_R	5	V
Peak Forward Current*2	I_{FP}	75	mA

*1:Soldering time \leq 5 seconds. *2:Pulse Width \leq 100 μ s and Duty \leq 1%

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Electrical and Optical Characteristic

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward Voltage	V_F	$I_F=10\text{ mA}$	/	2.0	2.4	V
Reverse Current	I_R	$V_R=5\text{ V}$	/	/	10	μA
Luminous Intensity	I_v	$I_F=10\text{ mA}$	10	25	/	mcd
Viewing Angle	$2\theta_{1/2}$	$I_F=10\text{ mA}$	/	60	/	deg
Dominant Wavelength	λ_d	$I_F=10\text{ mA}$	/	620	/	nm
Peak Wavelength	λ_p	$I_F=10\text{ mA}$	/	630	/	nm
Spectrum Radiation Bandwidth	$\Delta\lambda$	$I_F=10\text{ mA}$	/	20	/	nm

Notes:

$\theta_{1/2}$ is the angle from optical centerline where the luminous intensity is 1/2 the optical centerline value.

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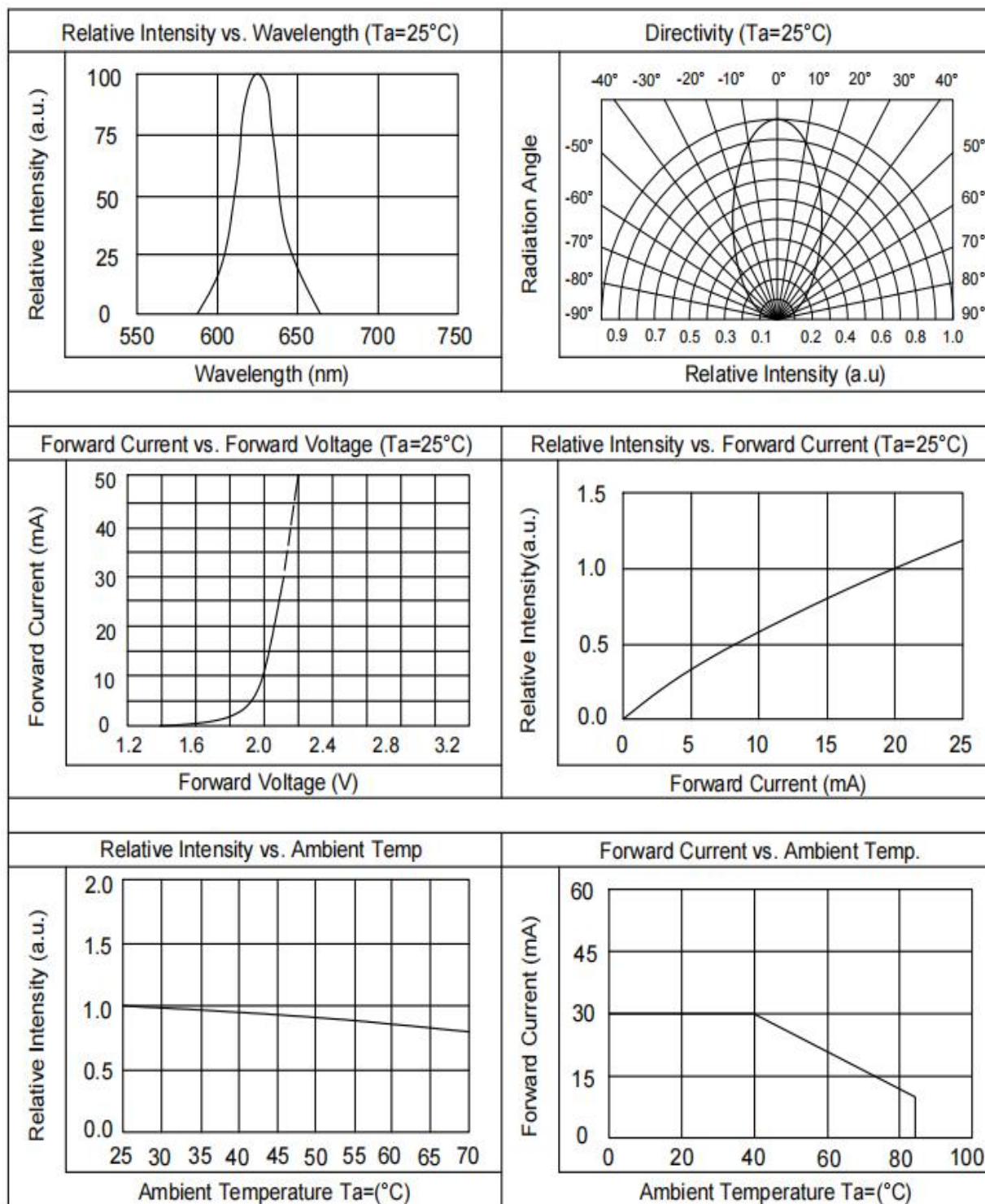
Specifications for Bin Grading

I _v (mcd)		
Grade	Min.	Max.
L	10	20
M	16	32
N	25	50
P	40	80

Notes: Luminous intensity: +/-15%.

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Typical Electrical / Optical Characteristics Curves



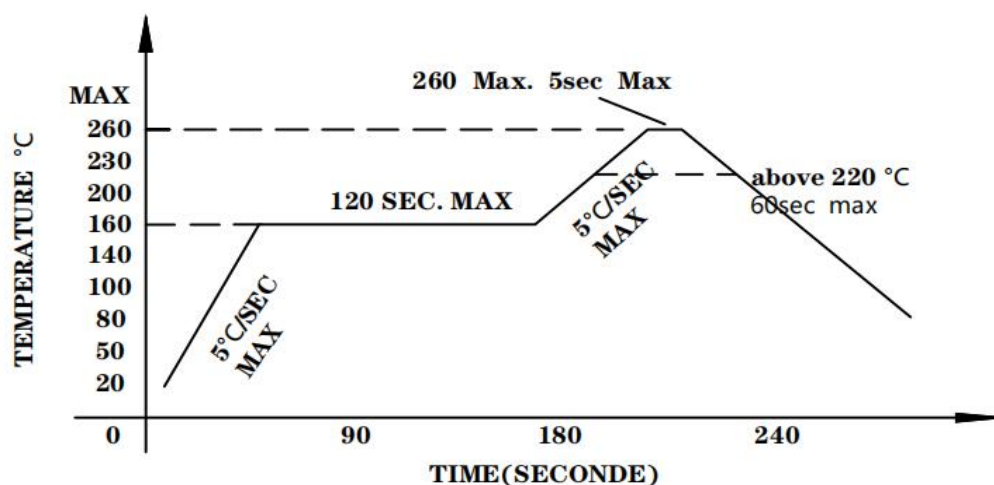
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Soldering condition

- Careful attention should be paid during soldering. When soldering, leave more than 2mm from solder joint to Led, and soldering beyond the base of the tie bar is recommended.
- Avoiding applying any stress to the lead frame while the LED are at high temperature particularly when soldering.
- Dip and hand soldering should not be done more than one time.
- After soldering the LED, the epoxy bulb should be protected from mechanical shock or vibration until the LED return to room temperature.
- A rapid-rate process is not recommended for cooling the LED down from the peak temperature.
- Although the recommended soldering conditions are specified in the above table, dip or hand soldering at the lowest possible temperature is desirable for the LED.
- Wave soldering parameter must be set and maintain according to recommended temperature and dwell time in the solder wave.

• Recommended soldering conditions

Hand Soldering		Wave Soldering	
Temp. at tip of iron	300°C Max. (30W Max.)	Preheat temp.	160°C Max. (120 sec Max.)
Soldering time	3 sec Max.	Bath temp. & time	260 Max., 5 sec Max
Distance	2mm Min.(From solder joint to Led)	Distance	2mm Min. (From solder joint to Led)



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Reliability test items and conditions:

The reliability of products shall be satisfied with items listed below.

Confidence level: 97%

LTPD:3%

No	Item	Test Conditions	Test Hours/Cycle	Sample Size	Failure Judgment Criteria	Ac/Er
1	Solder Heat	TEMP:260°C±5°C	10 SEC	76 PCS	$I_v \leq I_{vt} * 0.5$ or $V_f \geq U$ or $V_f \leq L$	0/1
2	Temperature Cycle	H:+100°C 15min ∫ 5min L:-40°C 15min	300 CYCLES	76 PCS		0/1
3	Thermal Shock	H:+100°C 5min ∫ 10sec L:-10°C 5min	300 CYCLES	76 PCS		0/1
4	High Temperature Storage	TEMP:100°C	1000 HRS	76 PCS		0/1
5	Low Temperature Storage	TEMP:-40°C	1000 HRS	76 PCS		0/1
6	DC Operating Life	TEMP:25°C IF=20mA	1000 HRS	76 PCS		0/1
7	High Temperature / High Humidity	85°C/85%RH	1000 HRS	76 PCS		0/1

Note: I_{vt} : To test I_v value of the chip before the reliability test.

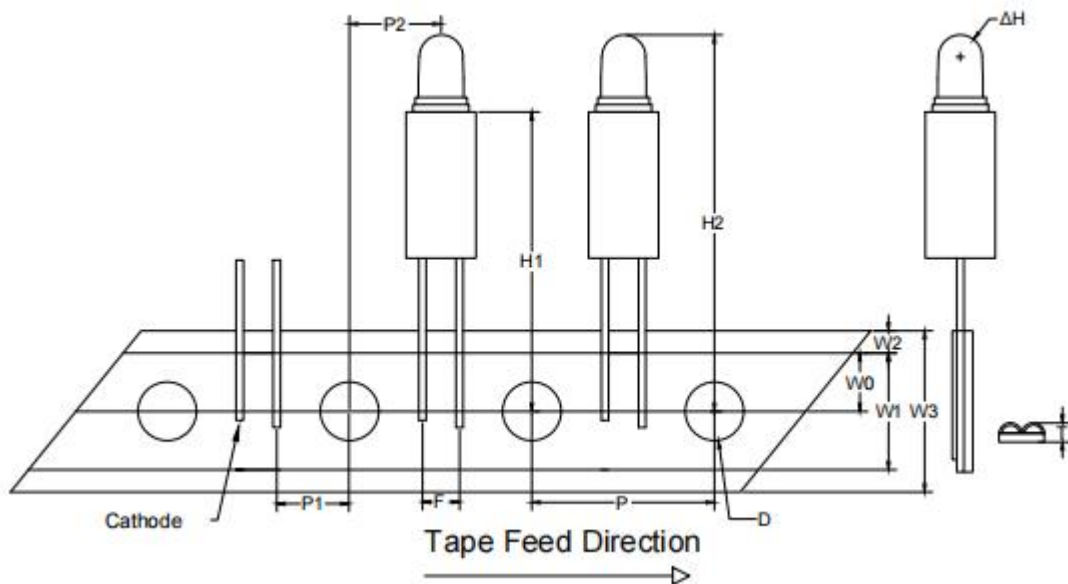
I_v : The test value of the chip that has completed the reliability test

U: Upper Specification Limit

L: Lower Specification Limit

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Carrier Tape Dimensions:



Notes:

Lead spacing is measured where the lead emerge from the package.

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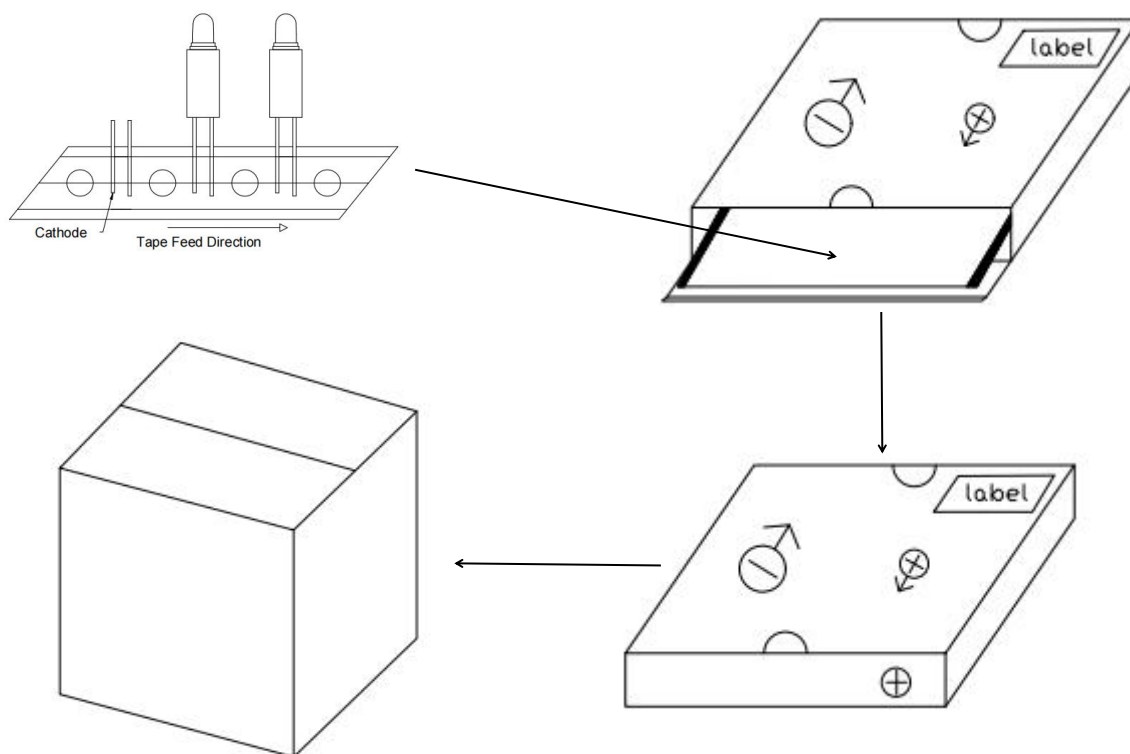
Taping Sizes

Symbol Items	Symbol	Specifications		
		Avg.		Tolerance
		mm	Inch	(mm)
Tape Feed Hole Diameter	D	4	0.157	±0.2
Component Lead Pitch	F	2.54	0.100	±1.0
Front-to-Read Deflectio	ΔH	2	0.079	±5°
Feed Hole to Button of Component	H1	26	1.024	±1.0
Feed Hole to Overall Component Height	H2	31.28	1.231	±1.0
Feed Hole Pitch	P	12.5	0.492	±0.3
Lead Location	P1	5	0.197	±1.0
Center of Component Location	P2	6.25	0.246	±1.2
Overall Taped Package Thickness	T	1.42	0.056	MAX
Feed Hole Location	W0	6.5	0.256	±0.5
Adhesive Tape Width	W1	13	0.512	±1.0
Adhesive Tape Position	W2	2.5	0.098	±1.0
Tape Width	W3	18	0.709	±1.0

Note: Tolerances unless mentioned ±0.1mm. Unit = mm

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Packing Specification:



Packing Quantity: 2500 PCS/In-Carton, 25000PCS/Out-Carton

Note:

After packing into the box, the head is located in the box large negative symbol upward, positive symbol facing themselves

	HARVATEK	
CPN:		RoHS
P/N:		
		
HV-311050/305/SUR-TR1		
QTY:		CAT:
		HUE:
LOT NO:		REF:
		

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Revision History

Revision	Page	Version No.	Revision Date
Initial Release		1.0	12-20-2021

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