

Harvatek 2.95*1.94*4.0mm Square LED LAMP HV-57G6224D-L50-K-U1940

Official Product	HV-57G6224D-L50-K-U1940	Customer Part No.		Data Sheet No.
	*******	*******		HV-57G6224D-L50-K-U1940
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- 2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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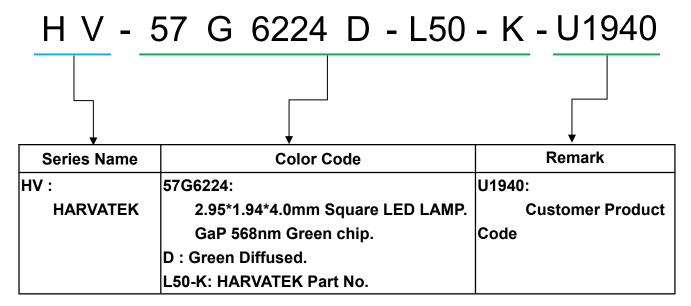


Compliance and Certification

ISO9002, QS9000 and ISO14001 Certified RoHS Compliant



Orderable Information



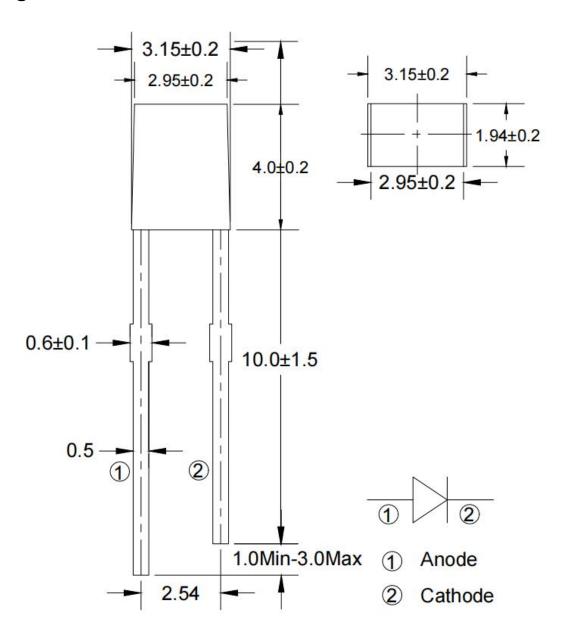
Features:

- Stable Color
- Popular 2.95*1.94mm through hole package, 4.0mm lens height.
- Green Diffused lens.

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Package Dimensions:



Notes:

- 1.All dimensions are millimeters.
- 2.Tolerance is +/-0.25mm unless otherwise noted.
- 3. Specifications are subject to change without notice.
- 4.Use the high temperature glue and the second spot silver glue process.

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Absolute Maximum Ratings at Ta=25℃

Parameter	Symbol	Rating	Unit
Forward Current	l _F	30	mA
Operating Temperature	T_{opr}	-40to+85	$^{\circ}$
Storage Temperature	T_{stg}	-40to+85	$^{\circ}$
Soldering Temperature*1	T _{sol}	260	$^{\circ}$
Power Dissipation	P _d	75	mW
Reverse Voltage	V _R	5	V
Peak Forward Current*2	I _{FP}	75	mA

^{*1:}Soldering time \leq 5 seconds. *2:Pulse Width \leq 100 μ s and Duty \leq 1%.

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Electrical and Optical Characteristic

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Forward Voltage	V _F	I _F =20 mA	/	2.0	2.6	V
Reverse Current	I _R	V _R = 5 V	/	/	10	μΑ
Luminous Intensity	I _V	I _F =20 mA	4	/	8	mcd
Viewing Angle	201/2	I _F =20 mA	/	160	/	deg
Peak Wavelength	λр	I _F =20 mA	/	569	/	nm
Dominant Wavelength	λd	I _F =20 mA	566	568	573	nm
Spectrum Radiation Bandwidth	Δλ	I _F =20 mA	/	20	/	nm

Notes:

 θ 1/2 is the angle from optical centerline where the luminous intensity is 1/2 the optical centerline value.

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Specifications for Bin Grading:

lv (mcd)				
Grade	Min.	Max.		
J	4	8		

λd (nm)				
Grade	Min.	Max.		
5	566	569		
6	568	571		
7	570	573		

Notes:

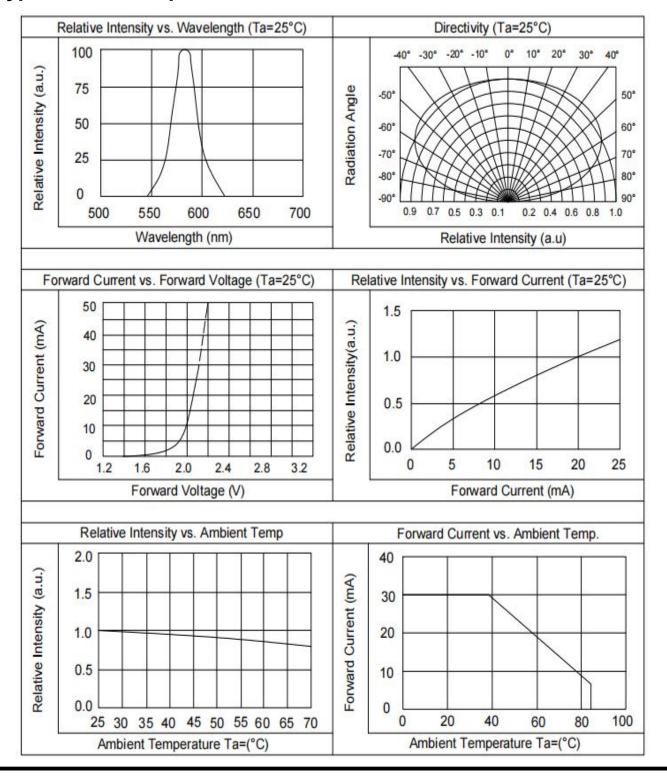
1.Luminous intensity:+/- 15%.

2.Wavelength: +/- 1nm.

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Typical Electro-Optical Characteristics Curve



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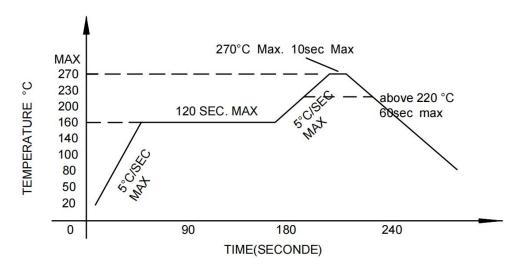


Soldering condition

- 1. Careful attention should be paid during soldering. When soldering, leave more then 2mm from solder joint to Led, and soldering beyond the base of the tie bar is recommended.
- 2. Avoiding applying any stress to the lead frame while the LED are at high temperature particularly when soldering.
- 3. Dip and hand soldering should not be done more than one time.
- 4. After soldering the LED, the epoxy bulb should be protected from mechanical shock or vibration until the LED return to room temperature.
- 5. A rapid-rate process is not recommended for cooling the LED down from the peak temperature.
- 6. Although the recommended soldering conditions are specified in the above table, dip or hand soldering at the lowest possible temperature is desirable for the LED.
- 7. Wave soldering parameter must be set and maintain according to recommended temperature and dwell time in the solder wave.

Recommended soldering conditions

Hand Soldering		Wave Soldering		
Temp. at tip of iron	Temp. at tip of iron 300 °C Max. (30W Max.)		160℃ Max. (120 sec Max.)	
Soldering time	Soldering time 3 sec Max. Bath		270℃ Max., 10sec Max	
Dietanas	2mm Min.(From solder joint to		2mm Min. (From solder joint	
Distance	Led)	Distance	to Led)	



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Reliability test items and conditions:

The reliability of products shall be satisfied with items listed below. Confidence level: 97%.

LTPD:3%.

No	Item	Test Conditions	Test Hours/Cycle	Sample Size	Failure Judgment Criteria	Ac/Er
1	Solder Heat	TEMP:260℃±5℃	10 SEC	76 PCS		0/1
2	Temperature Cycle	H:+100℃ 15min ∫ 5min L:-40℃ 15min	300 CYCLES	76 PCS		0/1
3	Thermal Shock	H:+100℃ 5min ∫ 10sec L:-10℃ 5min	300 CYCLES	76 PCS	lv≦lvt*0.5 or	0/1
4	High Temperature Storage	TEMP:100℃	1000 HRS	76 PCS	Vf≧U or	0/1
5	Low Temperature Storage	TEMP:-40℃	1000 HRS	76 PCS	Vf≦L	0/1
6	DC Operating Life	TEMP:25℃ IF=20mA	1000 HRS	76 PCS		0/1
7	High Temperature / High Humidity	85℃/85%RH	1000 HRS	76 PCS		0/1

Note: Ivt: To test Iv value of the chip before the reliability test.

Iv: The test value of the chip that has completed the reliability test.

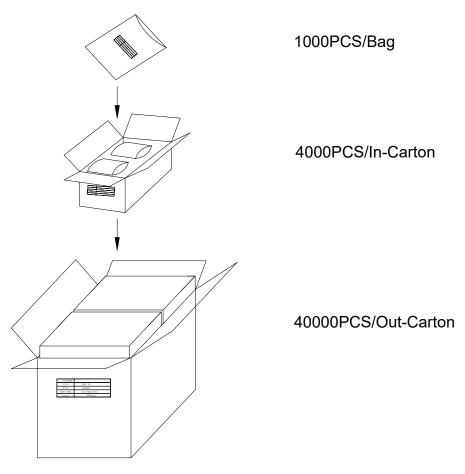
U: Upper Specification Limit.

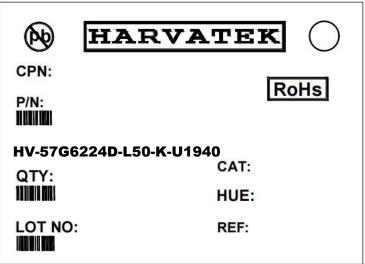
L: Lower Specification Limit.

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Packing Specification:





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Revision History

Revision	Page	Version No.	Revision Date
Initial Release		1.0	03-06-2023
Modify the graphic	4	1.1	03-14-2023
Modify the graphic	4	1.2	03-16-2023

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