



零件承认书

SPECIFICATION FOR APPROVAL

客户名称：0110

客户料号：

增益型号：

规格描述：ZETC201210S 小一体成型电感系列规格书

日期：2022/05/08

版本：A

增益签核：

制订	审核	核准
夏琳		李万

客户签核：

工程	审核	核准



东莞市增益实业有限公司

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物料类型：

小一体成型电感

日期：

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版本：

A



◆特征:

Features:

- 1.1 Metal material for large current and low loss.
- 1.2 High performance (Isat) realized by metal dust core.
- 1.3 Low loss realized with low Rdc.
- 1.4 Closed magnetic circuit design reduces leakage flux.
- 1.5 Vinyl thermal spray, better surface compactness.
- 1.6 Environmental requirements must comply with the QESP-44 document
- 1.7 100% lead (Pb) free meet RoHS2.0 and Halogen ,
Reach and other legal and regulatory requirements standard.

◆用途:

Applications:

- 2.1 DC/DCconverters.
- 2.2 Pad,Smartphone.
- 2.3 Portablegamingdevices,Smartwear,Wi-Fi module.
- 2.4 Notebooks,VR,AR.
- 2.5 LCDdisplays,HDDs,DVCs,DSCs,etc.
- 2.6 Basebandpowersupply,Amplifier,Powermanagement,
Modulepowersupply,Camerapowermanageme.

◆产品型号 :

ProductIdentification :

ZETC 2012 10 S 1R0 M B T -LF

⑤⑥⑦⑧⑨

- ①Series Name: Mini Molding Power Inductors
- ②External Dimensions(L×W):2012=2.0*1.2 mm
- ③External Dimensions(H):10=1.0 mm
- ④Size Tolerance:S=±0.2mm
- ⑤Inductance value:1R0=1.0uH
- ⑥Tolerance:M=±20%
- ⑦Coating color:B=Black
- ⑧package:T=编带
- ⑨Lead free products:环保产品



◆产品外观尺寸：

Shape and Dimensions (dimensions are in mm)：

Outline Dimensions

Recommend Land Pattern Dimensions



SCHEMATIC



Units:mm

Series	L	G/Typ	W	E	T	A/Typ	B/Typ	C/Typ
ZETC201210S	2.0±0.2	0.6±0.2	1.2±0.2	0.70±0.2	1.00Max.	2.10	0.50	1.30

◆产品构造：

Material List



环境：

Environmental Data:

- 工作温度：-55 至+125
(包括线圈自身温升)
- Operating Temperature:-55 to+125 (Including coil self-temperature rise)



ZETC201210S Series

P/N	L0(μH) @0A 1MHz	Rdc(mΩ)		Heat rating current		Saturation current	
		Typical	Max	Typical	Max	Typical	Max
ZETC201210SR10MBT-LF	0.10	8.00	13.00	7.70	7.20	8.70	8.20
ZETC201210SR22MBT-LF	0.22	16.00	22.00	7.30	6.70	7.50	7.00
ZETC201210SR24MBT-LF	0.24	17.00	23.00	7.20	6.60	7.40	6.90
ZETC201210SR33MBT-LF	0.33	24.00	32.00	5.70	5.20	6.70	6.20
ZETC201210SR47MBT-LF	0.47	29.00	36.00	4.90	4.50	5.70	5.20
ZETC201210SR68MBT-LF	0.68	37.00	43.00	4.50	4.20	5.20	4.70
ZETC201210S1R0MBT-LF	1.00	55.00	63.00	4.10	3.70	4.20	3.70
ZETC201210S1R5MBT-LF	1.50	76.00	85.00	3.30	2.80	3.40	2.90
ZETC201210S2R2MBT-LF	2.20	135.00	150.00	2.20	1.90	2.90	2.60
ZETC201210S3R3MBT-LF	3.30	210.00	260.00	2.00	1.70	2.40	2.00
ZETC201210S4R7MBT-LF	4.70	275.00	300.00	1.80	1.60	2.00	1.80
ZETC201210S6R8MBT-LF	6.80	440.00	520.00	1.70	1.50	1.65	1.40
ZETC201210S100MBT-LF	10.00	600.00	660.00	1.30	1.20	1.40	1.20

Test remarks

Note 1.: All test data is referenced to 25 °C ambient.

Note 2.: Test Condition:1MHz, 1.0Vrms.

Note 3.: Irms:DC current (A) that will cause an approximate ΔT of 40 °C.

Note 4.: Isat:DC current (A) that will cause L0 to drop approximately 30%.

Note 5.: Operating Temperature Range -55°C to + 125°C.

Note 6.: The part temperature (ambient + temp rise) should not exceed 125 under °C the worst case operating conditions.

Circuit design, component placement, PCB trace size and thickness, airflow and other cooling provision all affect the part temperature. Part temperature should be verified in the end application.

Note 7.: The rated current as listed is either the saturation current or the heating current depending on which value is lowe.



INDUCTANCE(μH) TEMP.RISE($^{\circ}\text{C}$)

INDUCTANCE(μH) TEMP.RISE($^{\circ}\text{C}$)

DC BIAS(Amps)

DC BIAS (Amps)	INDUCTANCE (uH)	TEMP. RISE (°C)
0	0.22	0
2	0.20	5
4	0.19	10
5	0.18	15
6	0.17	20
7	0.16	25
8	0.15	35
9	0.14	45
10	0.13	65

INDUCTANCE(μ H) TEMP.RISE($^{\circ}$ C)

INDUCTANCE(μ H)

TEMP.RISE($^{\circ}$ C)

DC BIAS(Amps)

DC BIAS (Amps)	INDUCTANCE (uH)	TEMP. RISE (°C)
0	0.32	0
1	0.31	5
2	0.30	10
3	0.29	15
4	0.28	20
5	0.27	25
5.5	0.26	30
6	0.25	35
6.5	0.24	40
7	0.23	45
7.5	0.21	65

DC BIAS (Amps)	INDUCTANCE (uH)	TEMP. RISE (°C)
0	0.5	0
1	0.48	5
2	0.46	10
3	0.44	15
4	0.42	20
4.5	0.40	25
5	0.39	30
5.5	0.38	35
6	0.37	40
6.5	0.36	50
7	0.33	80

The graph displays two trends as DC bias increases from 0 to 6.5 A. The inductance (red line) starts at approximately 0.65 μH and decreases to about 0.4 μH . The temperature rise (blue line) starts at 0 $^{\circ}\text{C}$ and increases to approximately 75 $^{\circ}\text{C}$.

DC BIAS (Amps)	INDUCTANCE (μH)	TEMP. RISE ($^{\circ}\text{C}$)
0	0.65	0
1	0.62	5
2	0.59	10
3	0.56	15
4	0.53	25
4.5	0.50	35
5	0.48	45
5.5	0.46	55
6	0.44	65
6.5	0.40	75

A line graph with two y-axes. The x-axis is labeled 'DC BIAS(Amps)' and ranges from 0 to 6. The left y-axis is labeled 'INDUCTANCE[μH]' and ranges from 0 to 1.2. The right y-axis is labeled 'TEMP. RISE(°C)' and ranges from 0 to 120. There are two data series: 'INDUCTANCE[μH]' represented by a red line and 'TEMP. RISE(°C)' represented by a blue line. The inductance line starts at approximately 1.0 μH at 0 Amps and decreases to about 0.55 μH at 6 Amps. The temperature rise line starts at 0°C at 0 Amps and increases to about 65°C at 6 Amps.

DC BIAS(Amps)	INDUCTANCE[μH]	TEMP. RISE(°C)
0	1.0	0
1	0.95	5
2	0.9	10
3	0.8	20
4	0.75	30
5	0.65	45
6	0.55	65

INDUCTANCE(μ H) TEMP.RISE($^{\circ}$ C)

DC BIAS(Amps)

DC BIAS (Amps)	INDUCTANCE (μ H)	TEMP. RISE ($^{\circ}$ C)
0	1.45	0
1	1.35	5
2	1.25	15
3	1.15	30
4	1.05	50
4.5	0.85	65

INDUCTANCE(μ H) TEMP.RISE($^{\circ}$ C)

DC BIAS (Amps)	INDUCTANCE (μ H)	TEMP. RISE ($^{\circ}$ C)
0	2.2	0
1	2.0	10
1.5	1.8	18
2	1.6	28
2.5	1.4	40
3	1.2	55
3.5	1.0	85

INDUCTANCE(μ H) TEMP. RISE($^{\circ}$ C)

DC BIAS(Amps)	INDUCTANCE(μ H)	TEMP. RISE($^{\circ}$ C)
0	3.2	0
0.5	3.1	2
1	3.0	10
1.5	2.8	25
2	2.5	45
2.5	2.1	60

INDUCTANCE(μ H) TEMP.RISE($^{\circ}$ C)

DC BIAS(Amps)	INDUCTANCE(μ H)	TEMP.RISE($^{\circ}$ C)
0	4.5	0
0.5	4.3	2
1.0	4.0	5
1.5	3.7	15
2.0	3.3	35
2.5	2.7	90

INDUCTANCE(μ H) TEMP. RISE($^{\circ}$ C)

DC BIAS(Amps)

INDUCTANCE(μH)

TEMP. RISE($^{\circ}\text{C}$)

DC BIAS(Amps)



可靠性测试

No.	Item	Requirements	Test Methods and Remarks	Reference	Sample Size
1	Solderability	(1) No case deformation or change in appearance. (2) Terminal area must have 95% min. Solder coverage.	①Temperature: $245 \pm 5^{\circ}\text{C}$. ②Solder Composition: Sn/Ag3.0/Cu0.5(Pb-Free). ③Sample immersion tin furnace $5 \pm 0.5\text{s}$.	AEC-Q200 (J-STD 002)	32
2	Adhesion of teral electrode	(1) Strong bond between the pad and the core, without come off PCB.	①Preconditioning: 245°C Reflow 3 times ②Inductors shall be subjected to $(260+0/-5^{\circ}\text{C})^{\circ}\text{C}$ for $(10 \pm 5)\text{s}$ Soldering in the base whit 0.3mm solder. ③Aplombelectrode way plus tax 12 N for (10 ± 1) seconds. 	AEC-Q200 (AEC-Q200-006)	32
3	Reflow test	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① The peak temperature: $260+0/-5^{\circ}\text{C}$. ② Reflow: 3 times. ③ Temperature curve is as below 	AEC-Q200 (MIL-STD-202 Method 210)	32
4	High temperature	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Preconditioning: Bake at $125+5^{\circ}\text{C}$ for $24 \pm 0.5\text{H}$, 245°C Reflow 3 times ②Temperature: $125 \pm 2^{\circ}\text{C}$. ③ Time : 1000 hours. ④Measurement at 24 ± 4 hours after test conclusion 	AEC-Q200 (MIL-STD -202 Method 108)	77
5	Low temperature	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	①Preconditioning: Bake at $125+5^{\circ}\text{C}$ for $24 \pm 0.5\text{H}$, 245°C Reflow 3 times ②Temperature: $-55 \pm 2^{\circ}\text{C}$. ③Time : 1000 hours. ④ Measurement at 24 ± 4 hours after test conclusion 	JESD22-A119A	77
6	Thermal shock	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	①Preconditioning: Bake at $125+5^{\circ}\text{C}$ for $24 \pm 0.5\text{H}$, 245°C Reflow 3 times ②Repeat 500 cycle as follow : ($-55 \pm 2^{\circ}\text{C}$, $30 \pm 3\text{minutes}$) 、 (Room temperature, 5 minutes)、 ($+125 \pm 2^{\circ}\text{C}$, $30 \pm 3\text{minutes}$)、 (Room temperature, 5 minutes) ③Measurement at 24 ± 4 hours after test conclusion	MIL-STD -202 Method 107	77



可靠性测试

No.	Item	Requirements	Test Methods and Remarks	Reference	Sample Size
7	Resistance to Soldering Heat	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Solder Composition: Sn/Ag3.0/Cu0.5(Pb-Free). ② Solder Temperature: $260 \pm 5^\circ\text{C}$. ③ Immersion Time: 10 ± 1 sec.	AEC-Q200 (MIL-STD-202 Method 210)	32
8	Static Humidity	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Preconditioning: Bake at $125 \pm 5^\circ\text{C}$ for $24 \pm 0.5\text{H}$, 245°C Reflow 3 times ② 1000 hours, $85^\circ\text{C}/85\%\text{RH}$. ③ Unpowered. ④ Measurement at 24 ± 4 hours after test conclusion 	AEC-Q200 (MIL-STD-202 Method 103)	77
9	Board Flex	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Preconditioning: 245°C Reflow 3 times ② Part mounted on a $100\text{mm} \times 40\text{mm}$ FR4 PCB board, which is 1.6 ± 0.2 mm thick and as a Layer-thickness $35 \mu\text{m} \pm 10 \mu\text{m}$. ③ Bending speed is 1mm/s. ④ Keeping the P.C Board 2 mm minimum for 60 seconds.  Unit: mm	AEC-Q200 (AEC-Q200-005)	30
10	Vibration	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Preconditioning: 245°C Reflow 3 times ② Frequency range : 10~2000Hz. ③ Amplitude: 1.5mm or 20g. ④ Sweep time and duration: 10~2000~10Hz for 20 minutes. ⑤ Each four hours in X,Y,Z direction, 12hours in total.	AEC-Q200 (MIL-STD-202 Method 204)	32
11	Mechanical Shock	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Preconditioning: 245°C Reflow 3 times ② Peak acceleration: 100G/S ③ Duration of pulse: 6ms ④ 3times in each of 6($\pm X, \pm Y, \pm Z$) axes.	AEC-Q200 (MIL-STD-202 Method 213)	32
12	Loading at High Temperature	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Preconditioning: Bake at $125 \pm 5^\circ\text{C}$ for $24 \pm 0.5\text{H}$, 245°C Reflow 3 times ② Temperature: $85 \pm 2^\circ\text{C}$. ③ Time : 1000 hours. ④ Applied Current : Rated current. ⑤ Measurement at 24 ± 4 hours after test conclusion	AEC-Q200 (MIL-STD-202 Method 108)	77



Soldering Condition

(This is for recommendation, please customer perform adjustment according to actual application)

Recommend Reflow Soldering Profile : (solder : Sn96.5 / Ag3 / Cu0.5)



Profile Feature	Lead (Pb)-Free solder
Preheat:	
Temperature Min (T_{smin})	150°C
Temperature Max (T_{smax})	200°C
Time (T_{smin} to T_{smax}) (t_s)	60 -120 seconds
Average ramp-up rate: (T_{smax} to T_p)	3°C / second max.
Time maintained above :	
Temperature (T_L)	217°C
Time (t_L)	60-150 seconds
Peak Temperature (T_p)	260°C
Time within $+0_{-5}^{\circ}\text{C}$ of actual peak Temperature (t_p) ²	10 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8minutes max.

Allowed Re-flow times : 2 times

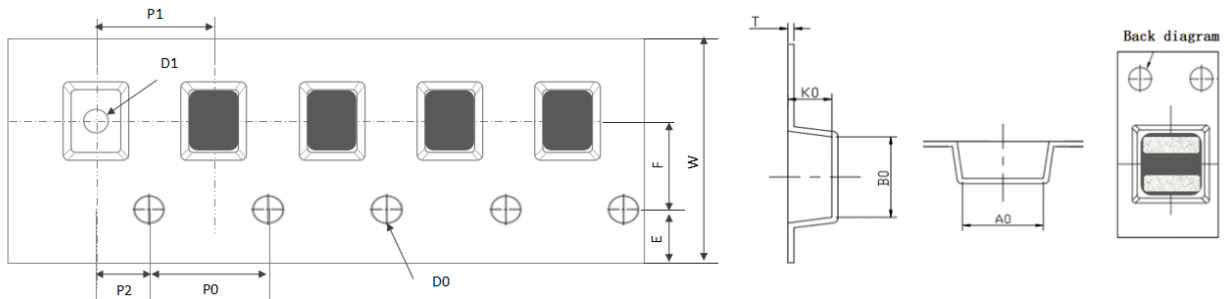
Remark : To avoid discoloration phenomena of chip on terminal electrodes, please use N₂ Re-flow furnace .



Packing

1 Dimension of plastic taping: (Unit: mm)

The following dimensions are related to the actual fit of the machine, for reference only.



Series	W	A0	B0	D0	D1	E
Tolerance	±0.10	+0.10/-0.05	+0.10/-0.05	+0.1/-0	±0.20	±0.10
201210	8.0	1.50	2.35	1.5	1.0	1.75

Series	F	K0	P0	P2	P1	T	包装 数量
Tolerance	±0.10	±0.10	±0.10	±0.10	±0.10	±0.05	
201210	3.5	1.20	4.0	2.0	4.0	0.22	3K

2 Dimension of Reel : (Unit: mm)

Type	A ±2.0	B ±2.0	C ±2.0
All	178	60	13

