

DESCRIPTION

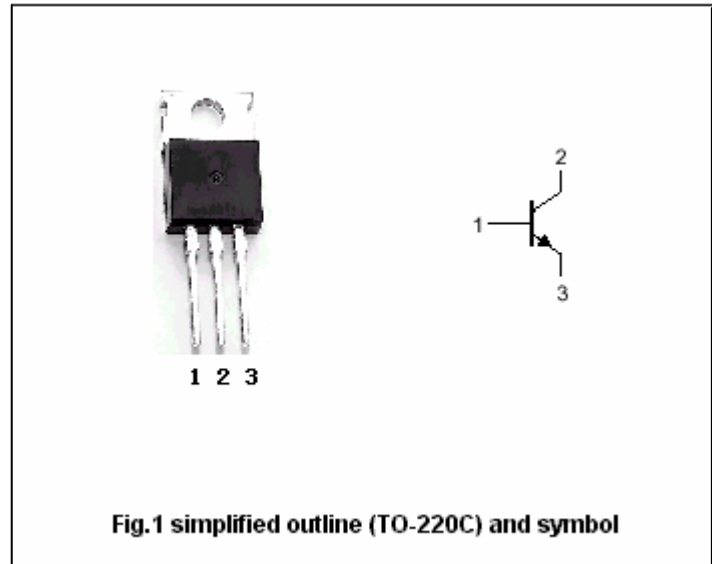
- With TO-220C package
- Fast switching speeds
- Low collector saturation voltage

APPLICATIONS

- For general purpose power amplification and switching regulators, converters and power amplifiers applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector; connected to mounting base
3	Emitter

**Absolute maximum ratings (T_c=25°C)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	80	V
V _{CEO}	Collector-emitter voltage	Open base	100	V
V _{EBO}	Emitter-base voltage	Open collector	7	V
I _C	Collector current (DC)		15	A
I _{CM}	Collector current-Peak		20	A
P _D	Total power dissipation	T _c =25°C	83	W
		T _a =25°C	0.67	
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-55~150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R _{th j-c}	Thermal resistance from junction to case	2.5	°C/W

SPTECH Silicon NPN Power Transistors

D44VH10

CHARACTERISTICS

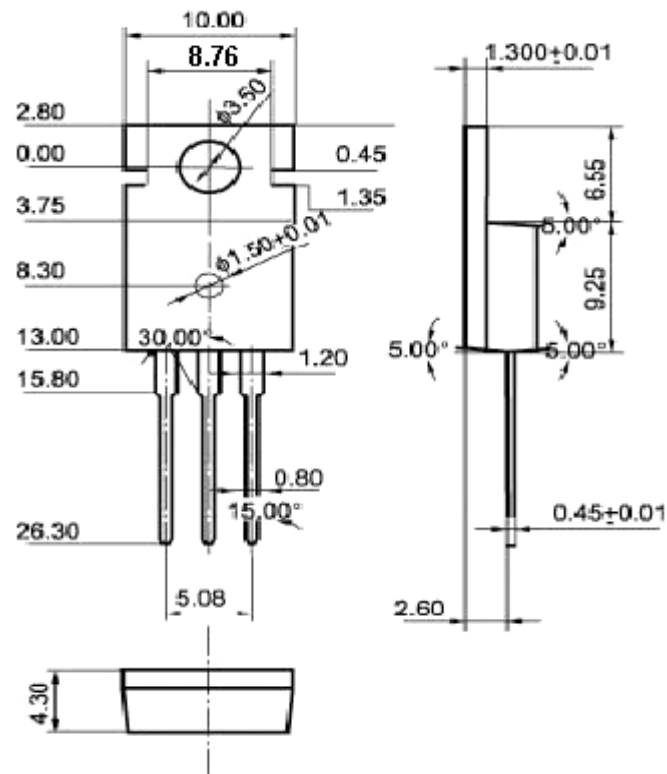
 $T_j=25^{\circ}\text{C}$ unless otherwise specified

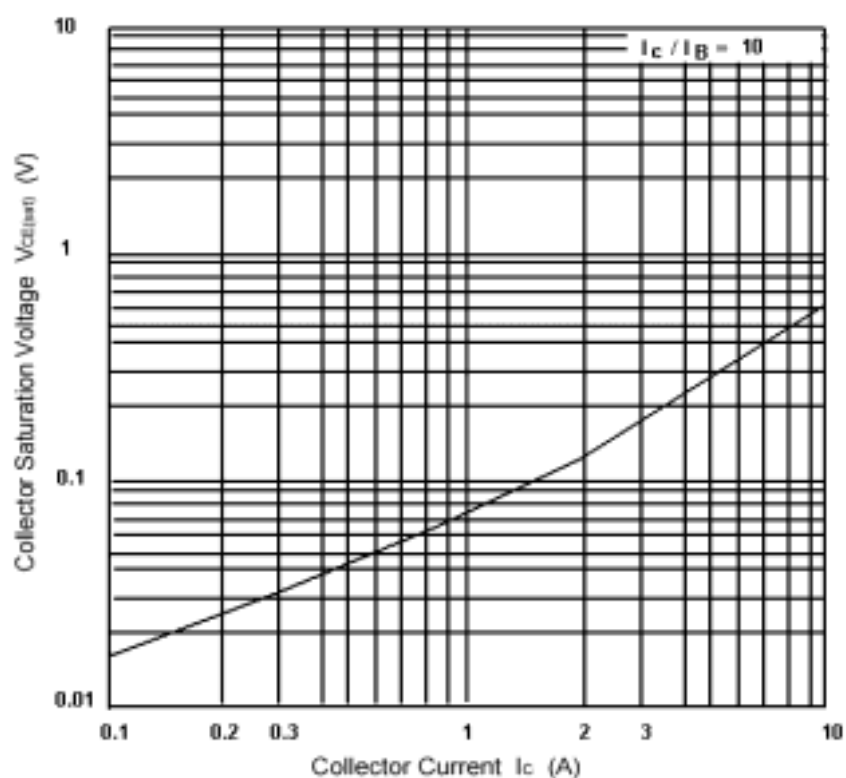
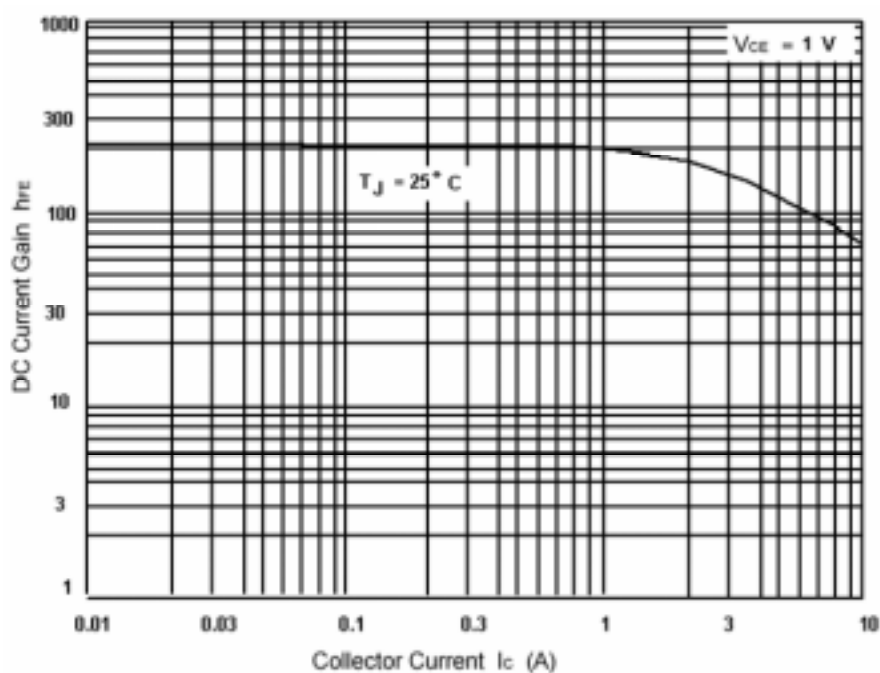
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CEO(SUS)}$	Collector-emitter sustaining voltage	$I_C=10\text{mA}$ $I_B=0$,	60			V
V_{CEsat}	Collector-emitter saturation voltage	$I_C=8\text{A}$; $I_B=0.4\text{A}$			1.0	V
V_{BEsat}	Base-emitter saturation voltage	$I_C=8\text{A}$; $I_B=0.8\text{A}$			1.5	V
I_{CES}	Collector cut-off current	$V_{CE}=60\text{V}$; $V_{BE}=0$			10	μA
I_{EBO}	Emitter cut-off current	$V_{EB}=5\text{V}$; $I_C=0$			0.1	mA
h_{FE-1}	DC current gain	$I_C=2\text{A}$; $V_{CE}=1\text{V}$	35			
h_{FE-2}	DC current gain	$I_C=4\text{A}$; $V_{CE}=1\text{V}$	20			
f_T	Transition frequency	$I_C=0.5\text{A}$; $V_{CE}=10\text{V}$		50		MHz
C_{cb}	Collector capacitance	$f=1\text{MHz}$; $V_{CB}=10\text{V}$		130		pF

Switching times

t_{on}	Turn-on time	$I_C=5\text{A}$ $I_{B1}=-I_{B2}=0.5\text{A}$		0.3		μs
t_s	Storage time			0.5		μs
t_f	Fall time			0.14		μs

PACKAGE OUTLINE

Fig.2 Outline dimensions (unindicated tolerance: $\pm 0.10\text{mm}$)



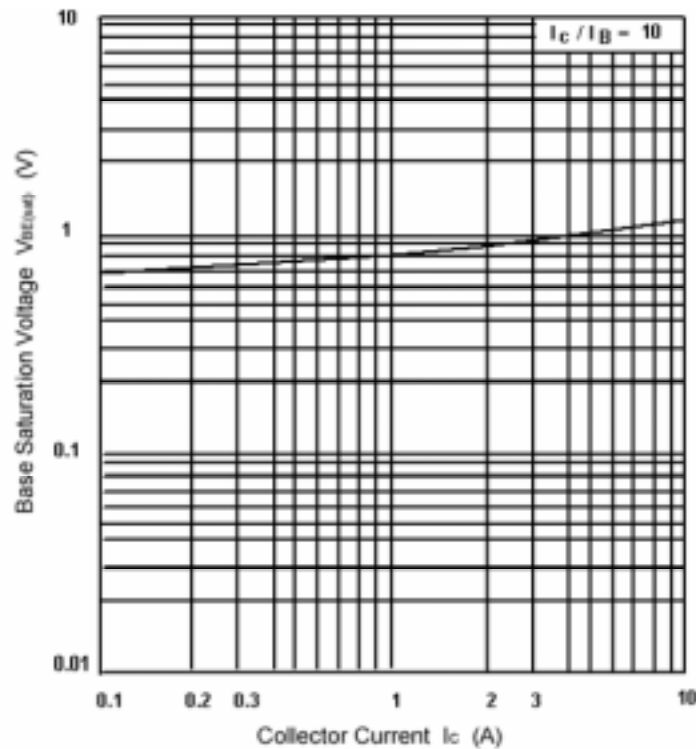


Fig.5 Base-Emitter Saturation Voltage

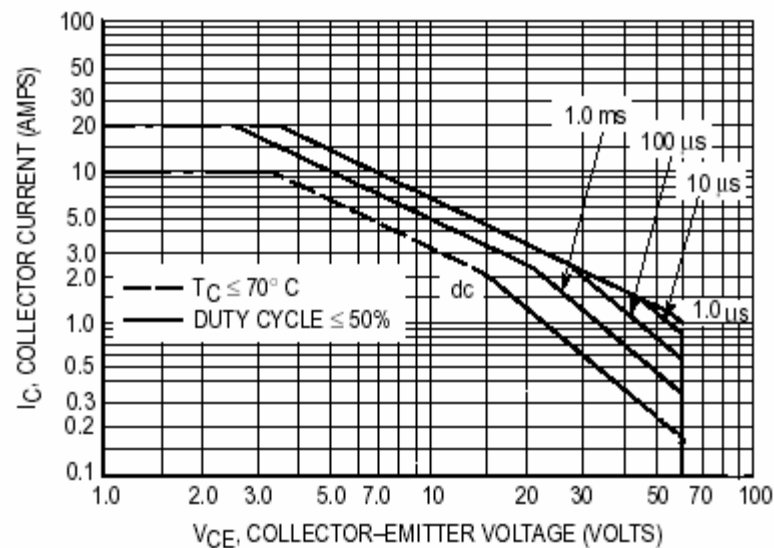


Fig.6 Safe Operating Area