



SPECIFICATION FOR APPROVAL

File No.: Q/FRK 0.GS.E.C24-C13

| | |
|---------------|---|
| Product Name | Box-type Metallized Polyester Film Capacitor(Stacked version) |
| Product Type: | C24(CL23B Series) |
| Product Code | C242E223J2SC322 |
| Customer | |
| Customer Code | |
| Issue Date | 2023-11 |

| Xiamen Faratronic Co. Ltd. | | | Approved by Customer |
|---|---|---|----------------------|
| Drafted | Checked | Approved | |
|  |  |  | |



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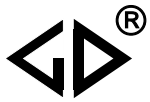
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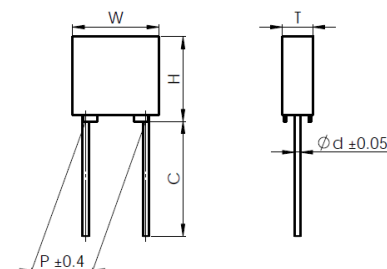


Version history

| Current version | Date | Author | Change description |
|-----------------|------|--------|--------------------|
| | | | |
| | | | |
| | | | |

Box-type metallized polyester film capacitor (Stacked version)

■ Outline Drawing



■ Features

- Metallized polyester film, stacked construction
- Plastic case (UL94 V-0), Epoxy resin sealing
- High dv/dt ability

■ Typical Applications:

- By-passing, blocking, coupling, decoupling,
- Pulse logic, timing, compact fluorescent lamps.
- Inverter for LCD monitors, automotive DC motor suppression

■ Specifications

| | | | |
|---|---|---|--------------------|
| Reference Standard | GB 7332(IEC 60384-2) | | |
| Climatic Category | 55/125/56 | | |
| Rated temperature | 85°C | | |
| Operating temperature | -55°C~125°C (+85°C to +125°C: decreasing factor 1.25% per °C for U_R) | | |
| Rated Voltage | 50/63V, 100V, 250V, 400V, 500V, 630V, 700V | | |
| Capacitance Range | 0.0010μF ~ 2.2μF | | |
| Capacitance Tolerance | ±5%(J), ±10%(K), ±20%(M) | | |
| Voltage Proof | 1.4 U_R (5s) | | |
| Dissipation Factor | Frequency | $C_N \leq 0.1\mu F$ | $C_N > 0.1\mu F$ |
| | 1kHz | ≤1.0% | ≤1.0% |
| | 10kHz | ≤1.5% | ≤1.5% |
| | 100kHz | ≤3.0% | - |
| Insulation Resistance | $U_R > 100V$ | $\geq 3\ 0000M\Omega$, $C_N \leq 0.33\mu F$ $\geq 10\ 000s$, $C_N > 0.33\mu F$ | (20°C, 100V, 1min) |
| | $U_R \leq 100V$ | $\geq 15\ 000M\Omega$, $C_N \leq 0.33\mu F$ $\geq 5\ 000s$, $0.33\mu F < C_N \leq 1\mu F$ $\geq 1\ 000s$, $C_N > 1\mu F$ | (20°C, 10V, 1min) |
| Maximum Pulse Rise Time(dV/dt) If the working voltage(U) is lower than the rated voltage(U_R),the capacitor can be worked at a higher dV/dt. In this case, the maximum allowed dV/dt is obtain by multiplying the right value with U_R/U . | U_R (V) | dV/dt (V/μs) | |
| | | pattern I | pattern II |
| | 50/63 | 250 | 75 |
| | 100 | 300 | 85 |
| | 250 | 400 | 100 |
| | 400 | 600 | 150 |
| | 500 | 700 | 200 |
| | 630 | 800 | |
| | 700 | - | 250 |

■ Part number system

The 15 digits part number is formed as follow:

C24 Pattern I (High performance)

| | | | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|----|----|----|----|----|----|
| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 |
| C | 2 | 4 | | | | | | | 2 | 0 | | | | |

C24 Pattern II (Reduced size)

| | | | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|----|----|----|----|----|----|
| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 |
| C | 2 | 4 | | | | | | | 2 | S | | | | |

Digit 1 to 3 Series code

C24=CL23B

Digit 4 to 5 DC rated voltage

1H=50V 1J=63V 2A=100V 2E=250V

2G=400V 2H=500V 2J=630V 1V=700V

Digit 6 to 8 Rated capacitance value

For example : 103=10×10³pF=0.01uF

Digit 9 Capacitance tolerance

J=±5%,K=±10%, M=±20%

Digit 10 Lead pitch

2=5.0

Digit 11 Internal use

S=pattern II

Digit 12 to 15 Lead form and packaging code

Table 1 lead dimensions and packaging code

| Digit 12 | | Digit 13 | | Digit 14 | | Digit 15 | |
|----------|--|----------|-------------------------------------|----------|-------------|----------|---|
| code | explanation | code | explanation | code | explanation | code | explanation |
| A | ammo-pack | 2 | F=5.0mm | 0 | straight | 1 | each cap. among two consecutive holes P3=12.7mm,H=18.5mm (For pitch=5.0mm) |
| C | straight lead “C”in the figure above | code | explanation | | | 0 | Length tolerance ±0.5mm Or standard length |
| | | 00 | standard lead length (14mm~22mm) | | | | |
| | | 45 | lead length 4.5mm | | | | |

Note: Recommend short lead due to long lead could deform easily.



■ Dimensions(mm)

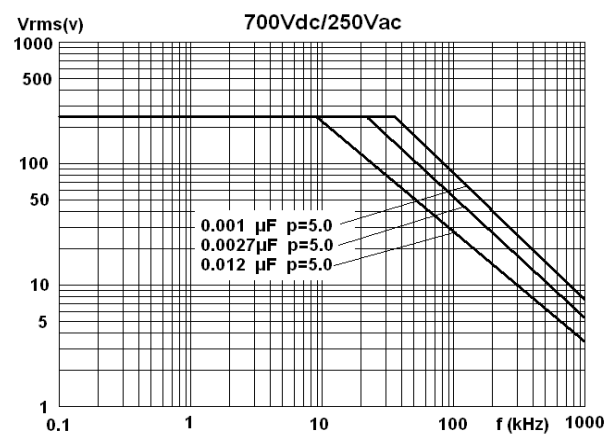
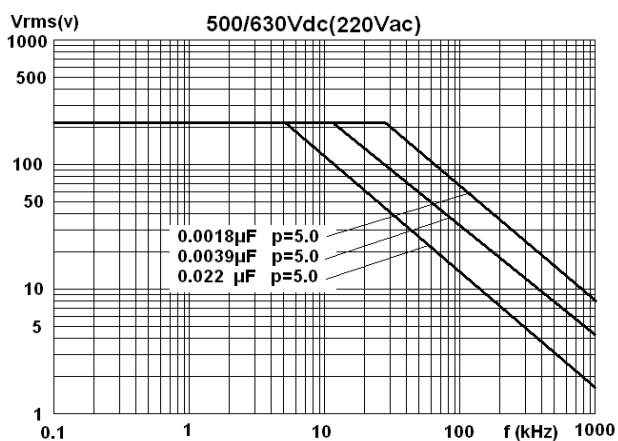
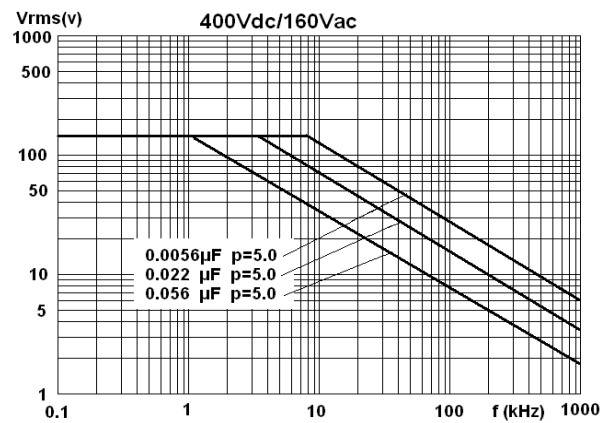
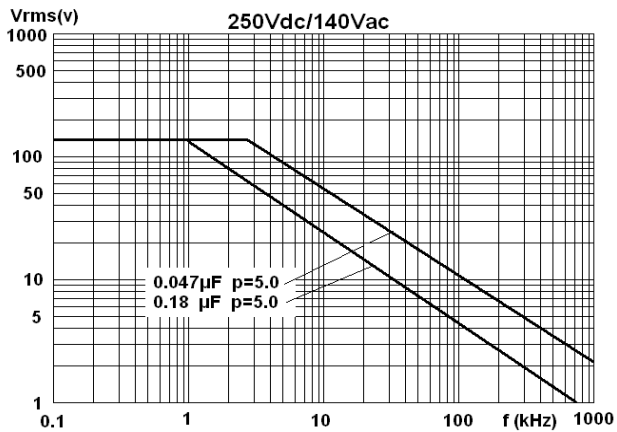
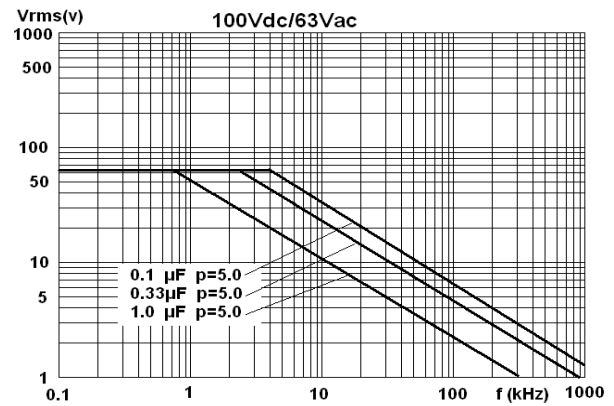
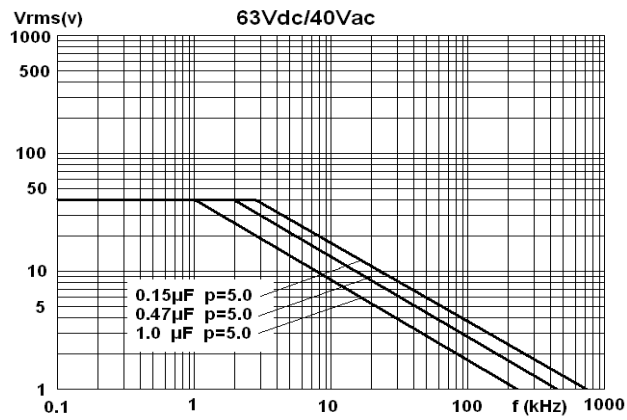
| | | |
|-------------------------------|------------|-----------|
| Capacitor Thickness: T | ≤ 3.5 | > 3.5 |
| Dimension Tolerance (W, H, T) | ± 0.2 | ± 0.4 |

Pattern I (High performance)

| 50Vdc (30Vac)/63Vdc (40Vac) # | | | | | | |
|-------------------------------|-----|-----|-----|-----|-----|-----------------|
| C _N (μ F) | W | H | T | P | d | Part number |
| 0.022 | 7.2 | 6.5 | 2.5 | 5.0 | 0.5 | C242E223J2SC322 |

■ MAX. VOLTAGE($V_{r.m.s}$) VERSUS FREQUENCY

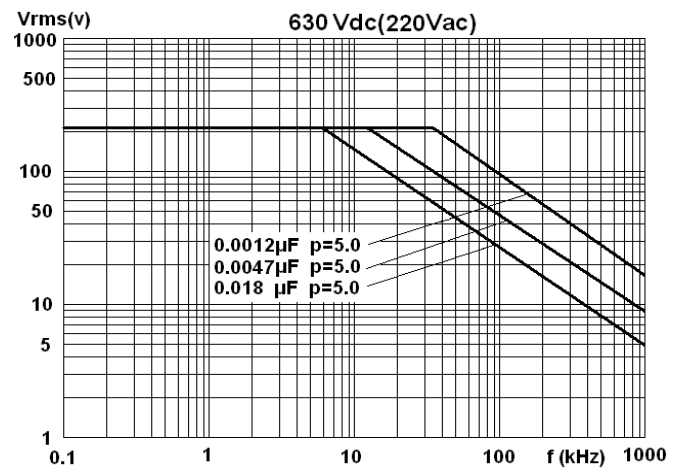
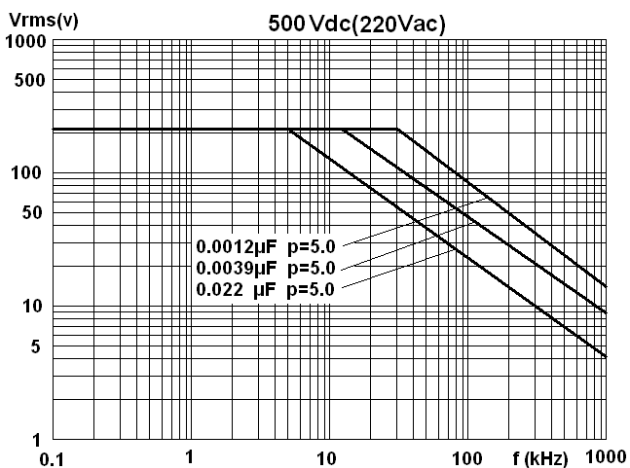
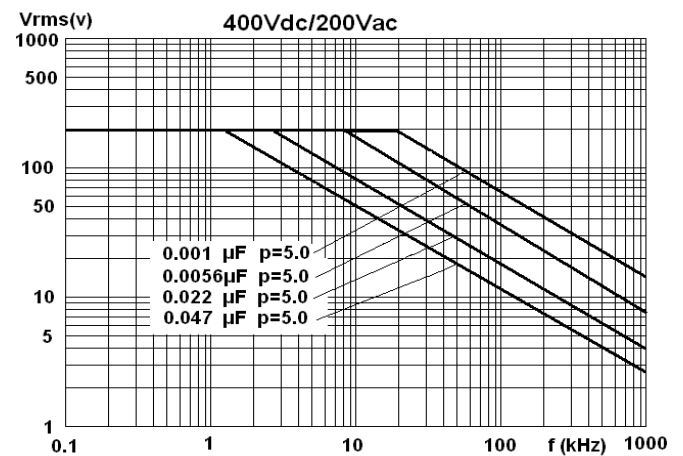
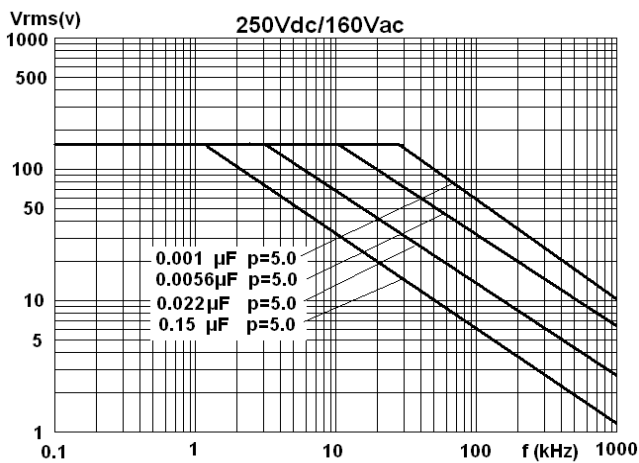
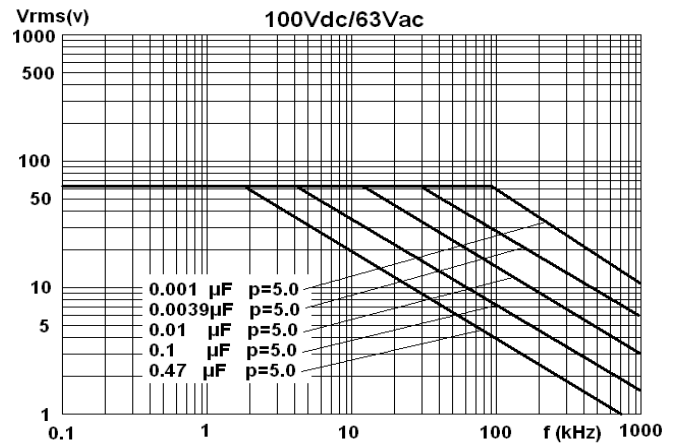
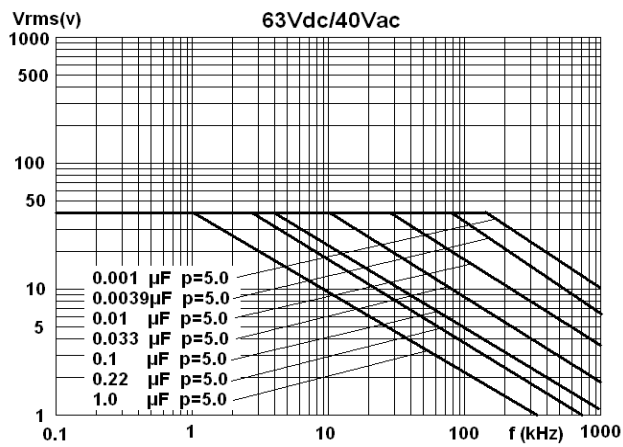
Pattern II (Reduced sized)



Note: sinusoidal wave-form、environment temperature $\leq 85^{\circ}\text{C}$, internal temperature rise $\Delta T=15^{\circ}\text{C}$, p (pitch) in mm..

■ MAX. VOLTAGE($V_{r.m.s}$) VERSUS FREQUENCY

Pattern I (High performance)



Note: sinusoidal wave-form、environment temperature $\leq 85^{\circ}\text{C}$, internal temperature rise $\Delta T = 15^{\circ}\text{C}$, p (pitch) in mm..

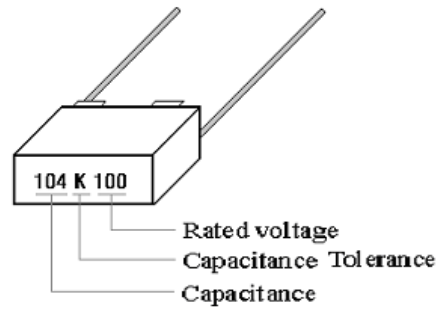
■ Test Method And Performance

| No. | Item | | Performance | Test method GB/T 7332 (IEC60384-2) |
|-----|------------------------------------|---------------------|---|--|
| 1 | Capacitance tolerance | | J($\pm 5\%$), K($\pm 10\%$), M($\pm 20\%$) | 1kHz, 3% $U_R(V_{rms})$ max. |
| 2 | Tangent of the loss angle | | $\tan\delta \leq 0.010(1\text{kHz})$ $\tan\delta \leq 0.015(10\text{kHz})$ $\tan\delta \leq 0.030(100\text{kHz}, C < 0.1\mu\text{F})$ | 1kHz or 10 kHz or 100 kHz $\leq 3\% U_R(V_{rms})$ or 1 V_{rms} (whichever is the minor) |
| 3 | Dielectric strength | | There shall be no breakdown or flashover. | 1.4 U_R , 5s |
| 4 | Insulation resistance | | $U_R \leq 100\text{V}$ $\geq 15\ 000\text{M}\Omega$, $C_N \leq 0.33\mu\text{F}$ $\geq 5\ 000\text{s}$, $0.33\mu\text{F} < C_N \leq 1\mu\text{F}$ $\geq 1\ 000\text{s}$, $C_N > 1\mu\text{F}$ $U_R > 100\text{V}$ $\geq 3\ 000\text{M}\Omega$, $C_N \leq 0.33\mu\text{F}$ $\geq 10\ 000\text{s}$, $C_N > 0.33\mu\text{F}$, | $U_R \leq 100\text{V}$, Charging voltage 10V $U_R > 100\text{V}$, Charging voltage 100V 20°C, measuring after applying voltage for 1 minute |
| 5 | Solderability | | Good quality of tinning | Solder temperature: 245°C $\pm 5^\circ\text{C}$ Immersion time: 2.0s $\pm 0.5\text{s}$ |
| 6 | Initial measurement | | Capacitance, Tan δ (10kHz) | |
| | Terminal Strength (straight lead) | | There shall be no visible damage | Tension U_{a1} : Pull: $\phi d = 0.5\text{mm}, 5\text{N}$; $\phi d = 0.6\text{mm}, 10\text{N}$ Bend U_b : The pull of bend: $\phi d = 0.5\text{mm}, 2.5\text{N}$ $\phi d = 0.6\text{mm}, 5\text{N}$ The terminals shall be bent 2 times in each direction. |
| | Resistance to solder heat | | There shall be no visible damage | Solder temperature: 260°C $\pm 5^\circ\text{C}$ Immersion time: 10s $\pm 1\text{s}$ |
| | Final measurement | | $\Delta C/C \leq \pm 2\%$ (relative to the initial value) Increase of tan δ : $\leq 0.003(10\text{kHz})$ | |
| 7 | Component's resistance of solvents | | The dimensions shall reach the requirement of Table 1, and the change of capacitor weight shall not beyond 1%. | Solvent: Industrial isopropanol. Solvent temperature: 23°C $\pm 5^\circ\text{C}$ Immersion time: 5min $\pm 0.5\text{min}$ Reverting time: 48h |
| 8 | Initial measurement | | Capacitance, Tan δ (10kHz) | |
| | Rapid change of temperature | | There shall be no evidence of deterioration. | $\theta_A = -55^\circ\text{C}$, $\theta_B = +125^\circ\text{C}$ 5 cycles, Duration: t=30min |
| | Vibration(straight lead) | | There shall be no evidence of deterioration. | Amplitude 0.75mm or acceleration 98m/s ² (whichever is the smaller severity), f: 10Hz to 500Hz. Three directions, 2h foreach direction, total 6h. |
| | Bump(straight lead) | | There shall be no evidence of deterioration. | 4 000 times, Acceleration: 390m/s ² , Pulse duration, 6ms |
| | Final measurement | | $\Delta C/C \leq \pm 5\%$ (relative to the initial value) Increase of tan δ : $\leq 0.003(10\text{kHz})$ IR: $\geq 50\%$ of the rated value | |
| 9 | climate sequence | Initial measurement | Capacitance, Tan δ (10kHz) | |
| | | Dry heat | | +125°C, 16h |

| No. | Item | | Performance | Test method GB/T 7332 (IEC60384-2) |
|-----|-----------------------------------|----------------------------------|--|---|
| 9 | climate sequence (continue) | Damp heat, Cyclic | | Test Db, Severity: b, the first cycle |
| | | Cold | | -55°C, 2h |
| | | Low air pressure | There shall be no permanent break down, flashover or other harmful deformation when applying U_R at the last 1 minute. | 15°C~35°C, 8.5kPa, 1h, |
| | | Damp heat, cyclic other | | Test Db, Severity b, the other cycles, Applying U_R for 1 minute after the test finished. |
| | | Final measu rement | There shall be no evidence of deterioration and the marking shall be legible. $\Delta C/C \leq \pm 5\%$ (relative to the initial value) Increase of $\tan \delta$: ≤ 0.005 (10kHz) IR: $\geq 50\%$ of the rated value | |
| 10 | Damp heat steady state | | There shall be no evidence of deterioration and the marking shall be legible. $\Delta C/C \leq \pm 5\%$ (relative to the initial value) Increase of $\tan \delta \leq 0.005$ (10kHz) IR: $\geq 50\%$ of the rated value | Temperature: 40°C $\pm 2^\circ\text{C}$ Humidity: $93 \pm 3\%$ RH Duration: 56 days |
| 11 | Endurance | | There shall be no evidence of deterioration and the marking shall be legible. $\Delta C/C \leq \pm 5\%$ (relative to the initial value) Increase of $\tan \delta$: ≤ 0.003 (10kHz) IR: $\geq 50\%$ of the rated value | Temperature: +85°C Voltage: $1.25 \times U_R$ Duration: 2 000h or Temperature: +125°C Voltage: $1.25 \times U_c$ ($U_c = 0.5 U_R$) Duration: 2 000h |
| 12 | Temperature characteristic | | Measuring capacitance at test point b, d, f: Characteristic at lower category temperature -55°C: $-10\% \leq (C_b - C_d)/C_d \leq 0\%$ Characteristic at upper category temperature +125°C: $0\% \leq (C_f - C_d)/C_d \leq +18\%$ I.R. (test at point f): $U_R \leq 100V$: $\geq 15M\Omega$ ($C \leq 0.33\mu F$) $\geq 5s$ ($C > 0.33\mu F$) $U_R > 100V$: $\geq 30M\Omega$ ($C \leq 0.33\mu F$) $\geq 10s$ ($C > 0.33\mu F$) | Static method: The Capacitors should be kept at the following temperature in turn: a(20 ± 2) °C, b(-55 ± 3) °C, d(20 ± 2) °C, f(+125 ± 2) °C, g(20 ± 2) °C |
| 13 | Charging and discharging | | $\Delta C/C \leq \pm 5\%$ (relative to the initial value) Increase of $\tan \delta$: ≤ 0.003 (10kHz, $C \leq 1.0\mu F$) ≤ 0.002 (1kHz, $C > 1.0\mu F$) IR: $\geq 50\%$ of the rated value | Times: 10 000 Duration of charging: 0.5s Duration of discharging: 0.5s Charging voltage: rated voltage Charging resistance: $220/C_N(\Omega)$ Discharging resistance: $R = 10/C_N(\Omega)$ or 20Ω (whichever is the greater) C_N : rated capacitance (μF) |

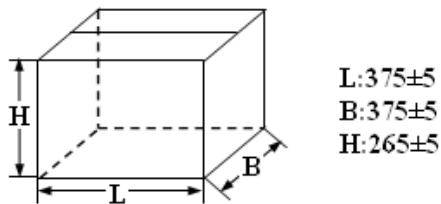
■ Marking

For example:

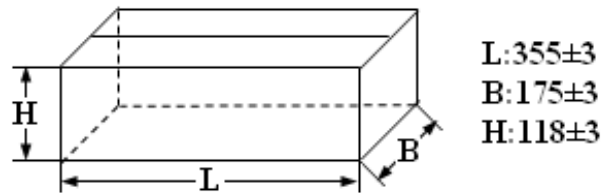


■ Packing box sizes(mm)(example)

1. Out packing box for bulk



2. Inner packing box for bulk



3. Box sizes for Ammo-pack

