

SPTECH Silicon NPN Power Transistor

2SD850

DESCRIPTION

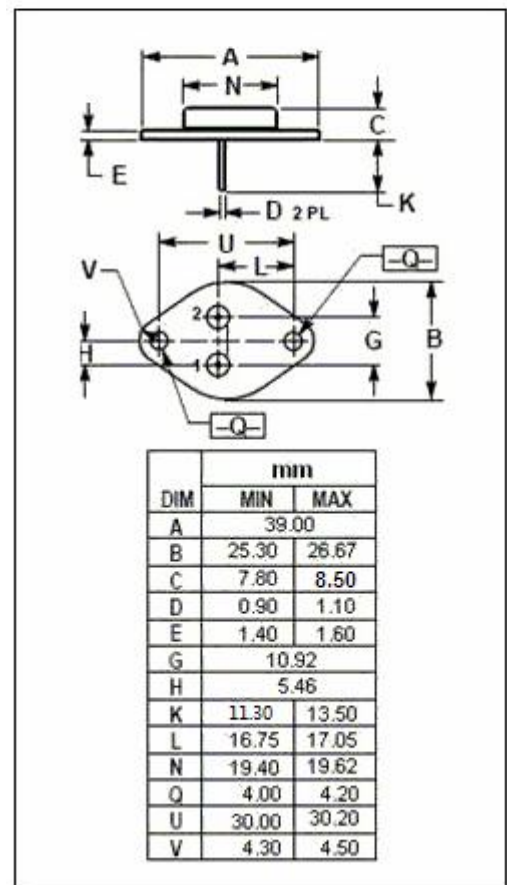
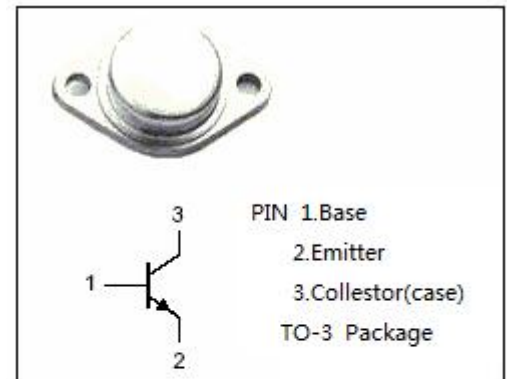
- High Breakdown Voltage-
: $V_{CBO} = 1500V$ (Min)
- High Reliability

APPLICATIONS

- Designed for line-operated horizontal deflection output applications.

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	1500	V
V_{CEO}	Collector-Emitter Voltage	700	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current- Continuous	3	A
I_{CP}	Collector Current-Pulse	5	A
P_C	Collector Power Dissipation @ $T_c \leq 90^\circ C$	25	W
T_J	Junction Temperature	130	$^\circ C$
T_{stg}	Storage Temperature Range	-65~130	$^\circ C$



SPTECH Silicon NPN Power Transistor**2SD850****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 2.5A; I _B = 0.8A			4.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 2.5A; I _B = 0.8A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 750V ; I _E = 0			50	μ A
		V _{CB} = 1500V ; I _E = 0			1	mA
h _{FE}	DC Current Gain	I _C = 2.5A ; V _{CE} = 10V	4		15	
t _f	Fall Time	I _C = 2.5A , I _{Bend} = 0.8A, L _B = 5 μ H			1	μ s
t _{stg}	Storage Time			13		μ s