

Surface-Mount Safety Capacitors SMC-X1Y2 Series Product Specification

CUSTOMER:	
CUSTOMER PAR	RT NO.:
STE PART NO.:	SMCY21815Y5P101K0
SPECS OF STE:	

Drafted by	For Customer Approval
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Http://www.songtian.cn

Revision: 2025-03-26



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Design Change Record

No.	Date	Version	Reason For Change	Description
1	2024.08.19	А		First Acknowledgment
2				
3				
4				
5				
6				
7				
8				

Revision: 2025-03-26



1. Characteristics

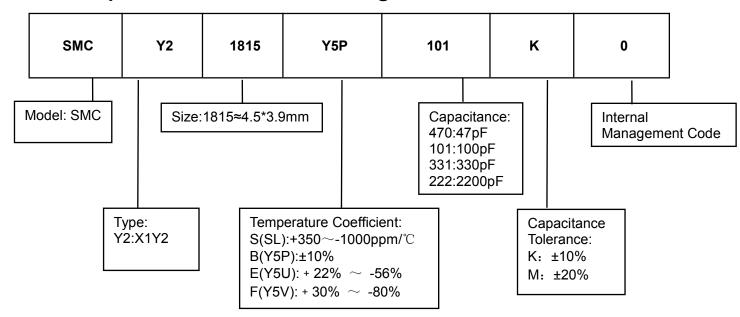
- The product height is 2.4mm, significantly reduced compared to traditional lead-type capacitor;
- The product is tape-packaged and suitable for SMT (Surface Mount Technology) automatic insertion soldering;
- SMC ceramic capacitors enable comprehensive surface mounting and miniaturization of end products;
- The product is coated using flame-retardant epoxy resin (compliant with UL 94V-0 flame retardant rating).

2. Application

Revision: 2025-03-26

- Filtering in AC circuits, primary and secondary coupling in switch-mode power supplies and AC converters;
- The D-A isolation and noise reduction of the transformerless DDA modem.

3. Principles of Part Number Coding





4. Technical Information

Capacitor Type	Y2 Class
Climatic Category	40/125/21 B
Operating Temperature Range	-40℃~125℃
Rated Voltage	300VAC
Capacitance Range	10pF∼4700pF
Dissipation Factor	S(SL) :D.F.≤0.15% B(Y5P) / E(Y5U) / F(Y5V): D.F.≤2.5%
Withstanding Voltage	2600Vac (r.m.s.)/60sec.
Insulation Resistance	>10000MΩ(Charging for 60±5 seconds under 500VDC)

5. Product imprinting

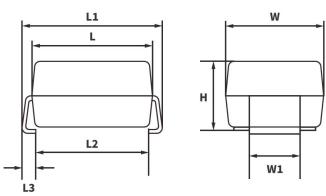
Example	Description			
	1	<u>_</u> \$ ⊒E	SongTian Logo	
	2	SMC	Product Model: SMC	
प ् _® ेय≡ Р	3	101	Capacitance: 100pF	
SMC101K	4	K	Capacitance Tolerance: K (±10%)	
B X1440~ Y2300~	5	В	Temperature Characteristics: B(Y5P)	
	6	X1Y2	Capacitor Type	
	7	440~ 300~	Rated Voltage: 440VAC 300VAC	

Revision: 2025-03-26



6. Physical Dimensions (for 1815)

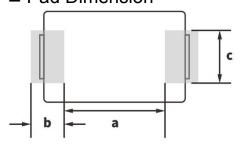
■ Product Dimension



Product Dimension(mm)							
L	4.5±0.3	L1	5.1±0.3				
W	3.9±0.3	L2	4.0±0.2				
Н	2.2±0.3	L3	0.5±0.3				
W1 1.8±0.3							

■ Pad Dimension

Revision: 2025-03-26



Pad Dimension (mm)				
a 4.0 Min				
b 2.2±0.1				
С	3.2±0.2			

7. Specification List

Temperature Characteristics	Nominal Capacitance (pF)	STE Part Number	Temperature Characteristics	Nominal Capacitance (pF)	STE Part Number
	10	SMCY21815SL100K0		82	SMCY21815Y5P820K0
	12	SMCY21815SL120K0	_	100	SMCY21815Y5P101K0
	15	SMCY21815SL150K0		120	SMCY21815Y5P121K0
	18	SMCY21815SL180K0		150	SMCY21815Y5P151K0
	20	SMCY21815SL200K0	Y5P	180	SMCY21815Y5P181K0
	22	SMCY21815SL220K0	136	200	SMCY21815Y5P201K0
SL	27	SMCY21815SL270K0		220	SMCY21815Y5P221K0
	30	SMCY21815SL300K0		270	SMCY21815Y5P271K0
	33	SMCY21815SL330K0		300	SMCY21815Y5P301K0
	39	SMCY21815SL390K0		330	SMCY21815Y5P331K0
	47	SMCY21815SL470K0		390	SMCY21815Y5U391M0
	56	SMCY21815SL560K0		470	SMCY21815Y5U471M0
	68	SMCY21815SL680K0	Y5U	560	SMCY21815Y5U561M0
	1200	SMCY21815Y5V122M0	150	680	SMCY21815Y5U681M0
	1500	SMCY21815Y5V152M0		820	SMCY21815Y5U821M0
Y5V	1800	SMCY21815Y5V182M0		1000	SMCY21815Y5U102M0
	2000	SMCY21815Y5V202M0			
	2200	SMCY21815Y5V222M0			



Revision: 2025-03-26

8. Basic Characteristics and Reliability Experiments

No.	Item	Standard	Test Method		
1	Appearance and Dimensions	No obvious defects in appearance and dimensions within the standard range.	Capacitor must be visually inspected for any obvious defects Measure the dimensions using a vernier caliper		
2	Marking	Clear and easily recognizable	Visual inspection		
3	Capacitance	Within the tolerance range	S(SL): The dissipation factor must be measured at 25℃, using a frequency of		
4	Dissipation Factor	S(SL) :D.F.≤0.15% B(Y5P), E(Y5U), F(Y5V): D.F.≤2.5%	1±0.1MHz and a voltage of 1.0V. B(Y5P), E(Y5U), F(Y5V): The capacitance and dissipation factor must be measured at 25℃, using a frequency of 1±0.1KHz and a voltage of 1.0V.		
5	Insulation Resistance	>10000ΜΩ	The insulation resistance must be tested after charging at 500VDC for 60±5 seconds.		
6	Dielectric Strength (Between terminals)	No breakdown or arcing	The capacitor withstands the test voltage from Table 1 for 60 seconds between the two leads without damage. Table 1> Type Test Voltage X1Y2 AC2600V (r.m.s.) Recommended voltage rise time>0.3s.		
7	Solderability	The lead surface requires solder coverage on over 75% of the area.	Solder temperature: 245±3°C Dipping time: 3±0.3 seconds Solder composition: Sn98Ag2		



Revision: 2025-03-26

Surface-Mount Safety Capacitors SMC-X1Y2 Series

No.	. Item		Standard	Test Method
		Appearance	No visual damage	Pre-treatment: The product is placed at 150
		Capacitance Change Rate	Within ±10%	+0/-10 °C for 60±5 minutes, followed by a 24±2 hours placement at room temperature.
8	Solder Heat Resistance	Withstanding Voltage	2.6KVAC/60S Pass	(applicable for B/E/F material). After the above pretreatment, the capacitor is immersed in molten solder at 260 ℃ ±5 ℃ for 10±1 seconds, and then allowed to stand at room temperature for 24±2 hours before testing. Capacitor Molten Solder
		Appearance	No visual damage	Solder the capacitor to the test fixture and
9	Vibration	Capacitance	Within tolerance range	subject it to vibrations at 10Hz-55Hz-10Hz with a total amplitude of 1.5mm, repeating the vibration cycle within 1 minute. Unless
9	Vibration	Dissipation Factor	S(SL):D.F.≤0.15% B(Y5P)/E(Y5U)/F(Y5V): D.F.≤2.5%	otherwise specified, check for mechanical damage after operating in mutually perpendicular directions for a total of 6 hours (2 hours each direction)
10	Plate flexura	al test	no significant abnormalities b 4.5 100 100 1:1.6 Dimension (mm) a b c d 9.6 11.7 2.7 1.0	Weld the capacitor to the test fixture as shown in the diagram and apply the specified test force. Use reflow soldering with care to prevent heat-related damage to the capacitor. Pressurizing speed: 1.0mm/s Pressing Speed: 1.0mm/s Exit Speed: 5.0mm/s Duration: 20.0s
11	Welding Stro (Cutting test		No pin misalignment or other adverse events	Weld the capacitor onto the test fixture as shown in the diagram, apply a 5N pushing force in the direction of the arrow. Solder the capacitor using reflow soldering and handle with care to avoid damage from heat shocks. 5N, 10±15 Glass Epoxy Board
		Appearance	No visual damage	
	Steady- State	Capacitance Change Rate	S(SL)/B(Y5P)/ E(Y5U)/F(Y5V): ≤±15%	Capacitor kept at 40±2°C, 90-95% RH for
12	Humidity- Heat	Insulation Resistance	>5000ΜΩ	500±12 hours. Post-test: Store capacitor at room temperature for 1-2 hours.
		Withstanding Voltage	2.6KVAC/60S Pass	



Revision: 2025-03-26

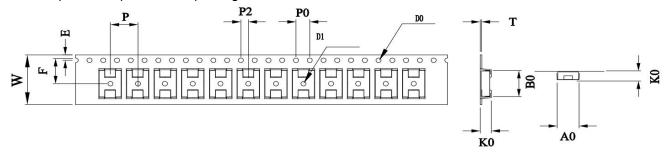
Surface-Mount Safety Capacitors SMC-X1Y2 Series

No.	Ite	em	Standard			Te	est Metho	od		
		Appearance	No visual damage	Capacito	r mainta	ined at a t	emperatu	re of 40±2	°C ar	nd a relative
			S(SL)/B(Y5P)/ E(Y5U)					age for 500		
	Llumiditu	Change Rate	/F(Y5V): ≤±15%	Pre-treatment: The product is placed at 150 +0/-10℃ for 60±5						
	Humidity Resistance	Insulation	>5000MΩ	minutes,	minutes, followed by a 24±2 hours placement at room			m		
	resistance	Resistance	>3000IVI22	temperature. (applicable for B/E/F material).						
		Withstanding	2.6KVAC/60S Pass	Pass Post-test handling: The capacitor must be stored at room				room		
		Voltage	2.01(1/10/0001 000	-		-2 hours.				
		Appearance	No visual damage	Peak Voltage: Each test capacitor withstands three instances of 8KVDC peak voltage, followed by a life test.						
		Capacitance Change Rate	Within ±20%	Conduct	a 1000+	-48/-24-ho relative hu	ur test at	30 Z	T2	, t
		Lea La Cara		exceedir	ng <u>50%, i</u>	using volta	ages spec	ified in the	table	<u>).</u>
111	Durability	Insulation Resistance	>5000MΩ				Voltage			
14	Durability	Resistance			1	.7*U _R , ind	crease the	e voltage b	У	
					AC1	000V ever	y hour fo	r 0.1 secor	nds."	
						•	•	l at 150 +0		
		Withstanding	2.6KVAC/60S Pass					lacement a	at roo	m
		Voltage				plicable fo				
								ust be store	ed at	room
				temperature for 1 to 2 hours. Capacitors under test should be positioned at the			he m	net		
			The duration of flame	flammable location in the flame and exposed once, meeting						
			application on the	Class B			arrio arra	onpooda o	, .	
			tested capacitor must			Panacitor Vo	olume (mm	3)		
		not exceed the specified values in the		capacitor must Capacitor Volume (mm eed the Duration of Flame (S						
15	Flame Retard			Class	Volume	250<	500<	Volume>		ning time
			table. Burning droplets		<250	Volume	Volume	1750		(s)
			or falling hot particles should not ignite a	A	15	≤500 30	≤1750 60	120		3
			tissue paper	В	10	20	30	60		10
					5	10	20	30		30
				Test the	capacito			ified in the	table	
		Appearance	No visual damage					otal of 5 cy		
				Tempera	ture Cyc	ele				
		Capacitance	Within ±20%		<u> </u>			- , ,		
		Change Rate			Order	Tempera		Time (m	iin)	
	I limba I acce			1 -	1	-40 +		30		
	High-Low Temperature	La contagra de			2		+25 +0/-3 125 +3/-0		3	
	Shock	Insulation Resistance	>5000MΩ		3 4	125 - +25 -		30		
	CHOOK	Nesisidille			4	+20 -	ru/-3	<u> </u>		
				Pre-treat	tment: Th	ne product	is placed	l at 150 +0	/-10°	of for 60±5
				1		•	•	lacement a		
		Withstanding	Withstanding 2.6KVAC/60S Pass			plicable fo				
		Voltage	2.51(1/10/0001 833					be stored	at roc	om
				tempera	ture for 2	24±2 hours	S.			



9, **Packing Instructions (for 1815)**

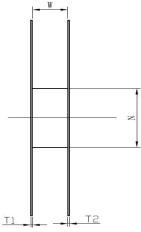
■ Description of tape and reel package method

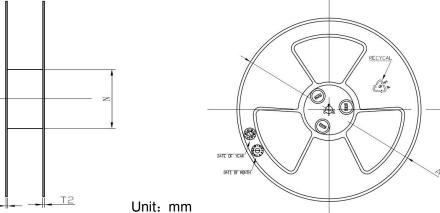


unit: mm

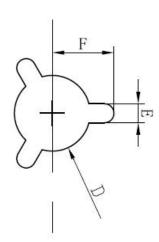
A0	В0	K0	Р	P0	P2	Т
4.2±0.1	5.7±0.1	2.7±0.1	8.0±0.1	4.0±0.1	2.0±0.1	0.3±0.1
W	E	F	D0	D1	PCS/REEL	
12.0±0.3	1.75±0.1	5.5±0.1	1.5+0.1/-0	1.50+0.1/-0	3000	Opcs

■ 13-inch reel size



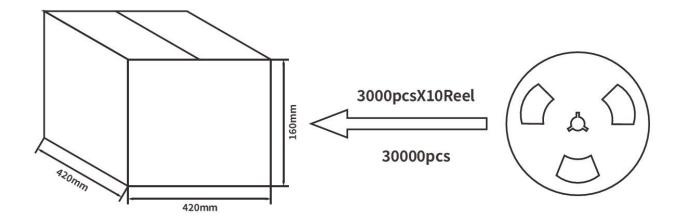


SPEC	12		
E±0.5	2.6		
F±0.5	10.8		
W±0.2	12.5		
T1±0.3	2.0		
T2±0.3	2.0		
A+0/-2	ф 330		
N±3.0	Ф 100		



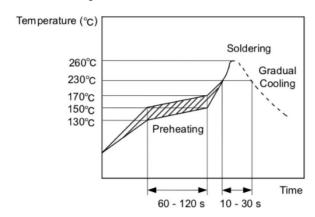


■ Packing Carton

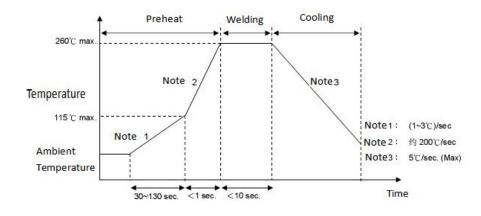


10. Soldering Instruction

■ Reflow Soldering Curve



■ Wave Soldering Curve



■ Soldering Conditions

Item	Condition	
Soldering Iron Tip Temperature	400°C (max.)	
Soldering Time	3.5 sec(max.)	
Soldering Iron Power	50W(max.)	



11. Storage Environment

- The insulation coating of the capacitor cannot form a perfect seal; therefore, avoid using or storing the capacitor in corrosive environments, especially where chloride gas, sulfide gas, acids, alkalis, salts, or similar substances are present, and minimize exposure to moisture. Verify that cleaning, soldering, or forming processes do not affect the product quality before these processes are performed.
- This is an MSL3 product. Hence,to prevent moisture absorption,the capacitor is packaged in a moisture-proof sealed bag.
- The capacitor should be stored and used within the following conditions for up to 6 months after delivery:

Temperature: Below 30°C

Humidity: 60%RH max

- After opening the moisture-proof packaging, solder the capacitor within 168 hours. Post-opening, store the capacitor in a moisture-proof bag with desiccant, along with the information card, and maintain the aforementioned conditions.
- If the storage period exceeds 6 months or the sealed bag is opened, perform baking (60°C, 168 hours) before soldering.

12. Usage Precaution



■ Operating Voltage:

Ensure that the applied voltage (Vp-p or Vo-p with DC bias) stays within the rated voltage range when using DC-rated capacitors in AC or ripple current circuits. Temporary abnormal voltages may occur during start-up or shutdown due to resonance or switching. Use capacitors within the rated voltage range to accommodate such conditions.

Voltage	VDC	VDC+VAC	VAC	Pulse Voltage (1)	Pulse Voltage (2)
Position Measurement	Vo-p	Vo-p	Vp-p	Vp-p	Vp-p

■ Operating Temperature and Self-Heating (Applicable to B/E Characteristics)

The capacitor's surface temperature should be kept below the upper limit of its rated operating temperature range. Consider the self-heating of the capacitor, which may occur in high-frequency

Surface-Mount Safety Capacitors SMC-X1Y2 Series

currents, pulse currents, etc. External voltage should not allow the temperature rise due to self-heating to exceed a range of 20°C around 25°C. Use a φ0.1mm low heat capacity (K) thermocouple for measurements, and ensure that the capacitor is not influenced by heat dissipation from other components or fluctuations in ambient temperature. Overheating may lead to a decrease in capacitor characteristics and reliability.

(Do not conduct measurements when the cooling fan is running, as it may affect the accuracy of the measurement).

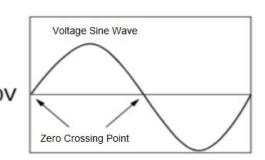
■ Test Conditions for Withstand Voltage Test Equipment:

The AC withstand voltage test equipment should be capable of generating a sine wave similar to 50/60Hz. Applying deformed sine waves or overload voltages exceeding the specified voltage may result in failure.

Voltage Application Method:

When applying the withstand voltage, the leads or terminals of the capacitor should be securely connected to the output terminals of the withstand voltage test equipment. Gradually increase the voltage from near zero to the test voltage. If the test voltage is not gradually increased from near zero but directly applied to the capacitor, it should include *zero crossing during application. At the end of the test, the test voltage should be reduced to near

zero before removing the capacitor leads or terminals from the output terminals of the withstand voltage test equipment. If the test voltage is not gradually increased from near zero but directly applied to the capacitor, surges may occur, leading to failure.



*Zero crossing refers to the position where the sine wave voltage passes through 0V. See the figure on the right.

- Repeated withstand voltage tests conducted by users may damage the capacitor, so capacitors tested after the test should not be used as qualified products again.
- Fail-Safe Design

Revision: 2025-03-26

If the capacitor is damaged, it can lead to a short circuit fault. Be sure to provide appropriate automatic fault protection functions, such as fuses, on the product to prevent electric shock, fire, or smoke.

Surface-Mount Safety Capacitors SMC-X1Y2 Series

■ Vibration and Shock

During use, avoid excessive shocks or vibrations that may expose the capacitor or pins, and prevent any crushing, bending, or external impact.

■ Bonding, Molding, or Coating

Before bonding, molding, or coating this product, verify through testing the performance of bonding, molding, or coating the product in the designated equipment to ensure that these processes do not affect the quality of the capacitor.

If there are drying/adhesive hardening conditions and the molding resin contains organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.), SMC is not suitable. Organic solvents may cause damage to the resin on the outer layer of the capacitor, resulting in cases of damage or short circuits.

During temperature cycling, changes in the thickness of adhesives, molding resins, or coatings may lead to cracking of the outer shell resin and/or cracking of ceramic components.

- Capacitors mounted on PCBs require the PCB pads to align with the capacitor pins for proper soldering. Otherwise, poor soldering between the capacitor and PCB may occur, leading to deformation of the capacitor pins or damage to the body, resulting in capacitor damage. Capacitors soldered to PCBs should not be forcibly moved or have the body tilted.
- Consult our technical personnel in advance when performing resin molding on capacitors.

■ Restricted Applications

Contact us before using our products in the following applications that require exceptionally high reliability to prevent defects that could directly cause harm to third parties' life, body, or property.

Aircraft Equipment

Aerospace Equipment

Submersible Equipment

Power Plant Control Equipment

Medical Equipment

Revision: 2025-03-26

Transportation Equipment

Traffic Signal Equipment

Disaster Prevention/Crime Prevention Equipment

Data Processing Equipment affecting the public

Applications with similar complexity and/or reliability requirements.