

TDSEMIC

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電源管理



顯示驅動



二三極管



LDO穩壓器



觸摸芯片



MOS管



運算放大器



存儲芯片



MCU



串口通信

D6KB10-TD

產品規格說明書

D6KB05 thru D6KB100

GLASS PASSIVATED BRIDGE RECTIFIERS

REVERSE VOLTAGE - 50 to 1000Volts

FORWARD CURRENT - 6.0 Amperes

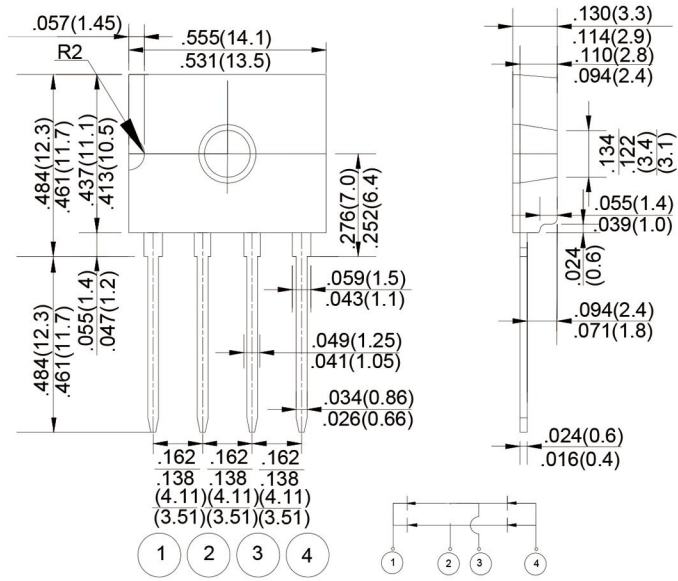
D3K

FEATURES

- Glass passivated chip junction
- High case dielectric strength
- High surge current capability
- Ideal for printed circuit board

MACHANICAL DATA

- Terminal:Plated leads solderable per MIL-STD 202E, Method 208C
- Case:UL-94 Class V-0 recognized Flame Retardant Epoxy
- Polarity:Polarity symbol marked on body
- Mounting position:any



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave ,60Hz, resistive or inductive load.

For capacitive load, derate current by 20%

CHARACTERISTICS	SYMBOL	D6KB05	D6KB10	D6KB20	D6KB40	D6KB60	D6KB80	D6KB100	UNI	
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	50	100	200	400	600	800	1000	V	
Maximum RMS Voltage	V _{RMS}	35	70	140	280	420	560	700	V	
Maximum DC Blocking Voltage	V _{DC}	50	100	200	400	600	800	1000	V	
Maximum Average Forward Rectified Output Current @ T _C =138℃ (with heatsink)	I _(AV)	6							A	
Peak Forward Surge Current 8.3ms Single Half Sine-Wave Super Imposed on Rated Load (JEDEC Method)	I _{FSM}	135							A	
Maximum Forward Voltage at 2.0A DC	V _F	1.0							V	
Maximum Forward Voltage at 4.0A DC	V _F	1.1							V	
I ² t Rating for Fusing (t<8.3ms)	I ² t	75.63							A ² s	
Typical Thermal Resistance	without heatsink	R _{θJa}	55							℃/W
	with heatsink	R _{θJC}	1.5							
	without heatsink	R _{θJL}	15							
Maximum DC Reverse Current at Rated DC Blocking Voltage	@ T _a =25℃ @ T _a =125℃	I _R	10.0							μA
			500							
Operating Temperature Range	T _J	-55 to +150							℃	
Storage Temperature Range	T _{STG}	-55 to +150							℃	

NOTES:The typical data above is for reference only(典型值仅供参考).

FIG.1-DERATING CURVE OUTPUT
RECTIFIED CURRENT

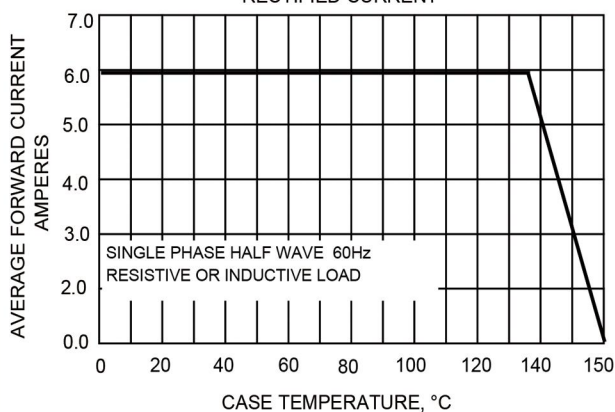


FIG.2-MAXIMUM NON-REPETITIVE SURGE CURRENT

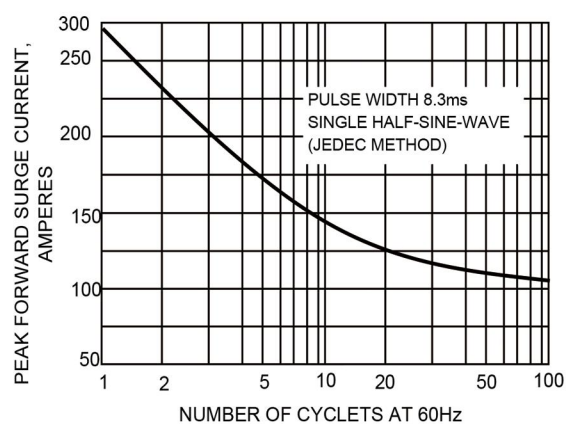


FIG.3-TYPICAL JUNCTION CAPACITANCE

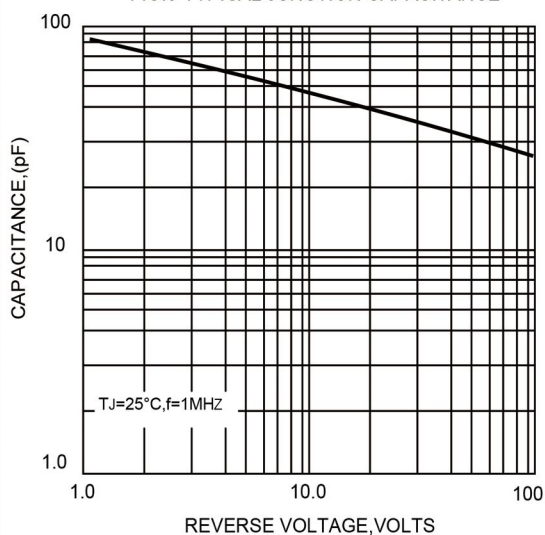


FIG.4-TYPICAL FORWARD CHARACTERISTICS

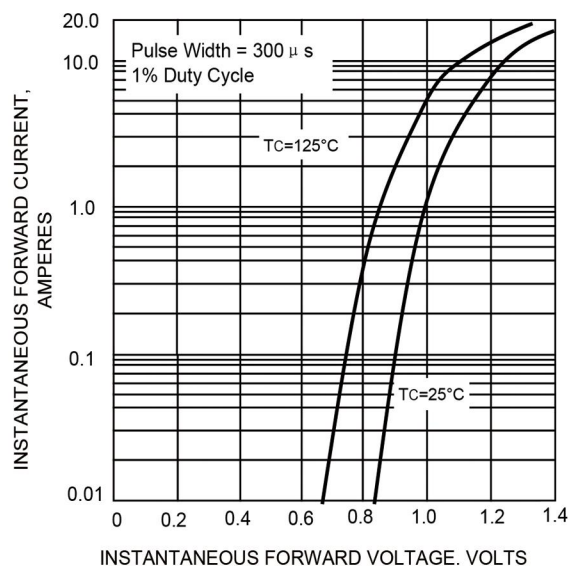
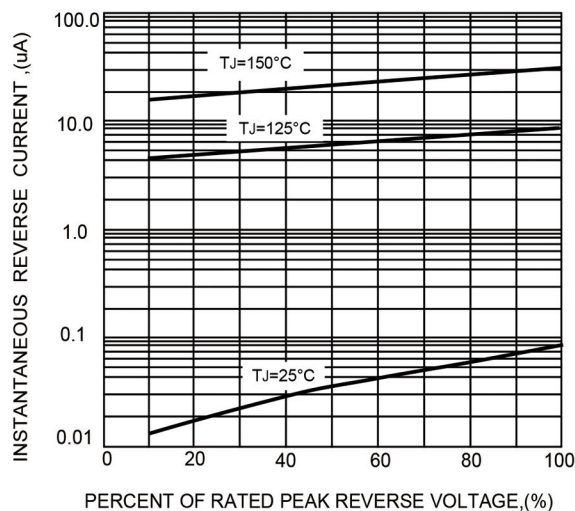


FIG.5-TYPICAL REVERSE CHARACTERISTICS



The cruve graph is for reference only, can't be the basis for judgment(曲线图仅供参考)!

REV. 3, 30-Dec-2018