

TDS:EMIC

拓 電 半 導 體

自主封測 品質把控 售後保障

WEB | WWW.TDSEMIC.COM



電源管理



顯示驅動



二三極管



LDO穩壓器



觸摸芯片



MOS管



運算放大器



存儲芯片



MCU



串口通信

ABS10

產品規格說明書

Single Phase 2.0Amp Glass passivated Bridge Rectifiers

Features

- The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- Idea for printed circuit board
- Glass passivated Junction chip
- Low reverse leakage
- High forward surge current capability
- High temperature soldering guaranteed 250°C/10 seconds at terminals

Mechanical Data

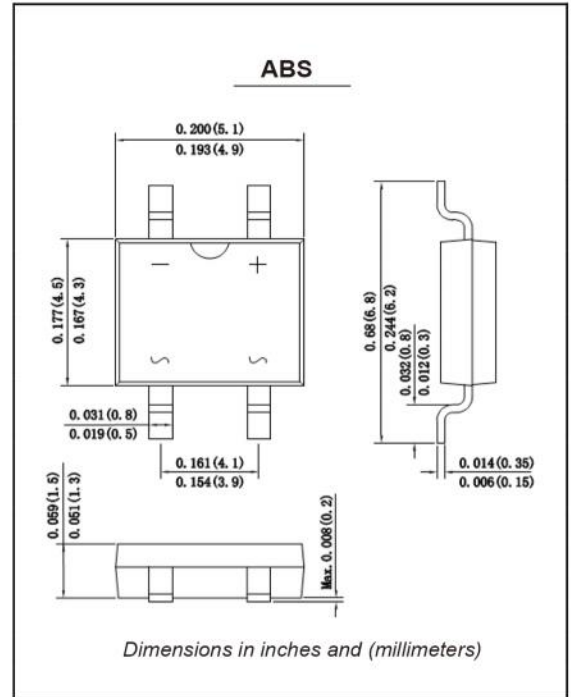
Case: Molded plastic body

Terminals: Solder plated, solderable per MIL-STD-750, Method 2026

Polarity: Polarity symbol marking on body

Mounting Position: Any

Weight : 0.004 ounce, 0.12 grams



Maximum Ratings And Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified. Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

	SYMBOLS	ABS22	ABS24	ABS26	ABS28	ABS210	UNITS
Maximum repetitive peak reverse voltage	V_{RRM}	200	400	600	800	1000	VOLTS
Maximum RMS voltage	V_{RMS}	140	280	420	560	700	VOLTS
Maximum DC blocking voltage	V_{DC}	200	400	600	800	1000	VOLTS
Maximum average forward rectified current at $T_A=30^\circ\text{C}$ On glass-epoxy P.C.B (Note 1)	$I_{(AV)}$	2.0					Amp
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	50.0					Amps
Maximum instantaneous forward voltage at 2.0A	V_F	1.1					Volts
Maximum DC reverse current $T_A=25^\circ\text{C}$ at rated DC blocking voltage $T_A=125^\circ\text{C}$	I_R	5.0 500					μA
Typical junction capacitance (Note 3)	C_J	15.0					pF
Typical thermal resistance	R_{qJA}	75.0					$^\circ\text{C/W}$
Operating junction and storage temperature range	T_J, T_{STG}	-55 to +150					$^\circ\text{C}$

Note: 1. Mounted on glass epoxy PC board with 1.3*1.3mm solder pad
 2. Mounted on aluminum substrate PC board with 1.3*1.3mm solder pad
 3. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

Ratings And Characteristic Curves

ABS210

FIG. 1- DERATING CURVE OUTPUT RECTIFIED CURRENT

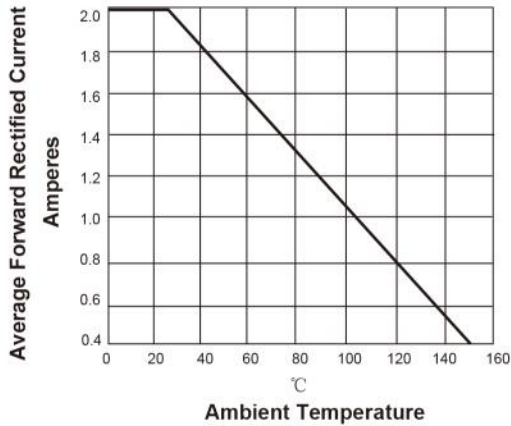


FIG. 2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT PERLEG

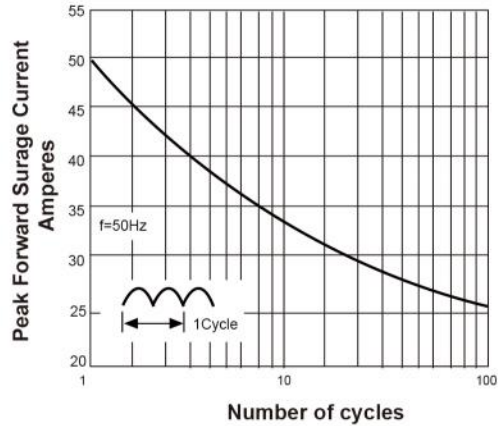


FIG. 3-TYPICAL FORWARD VOLTAGE CHARACTERISTICS

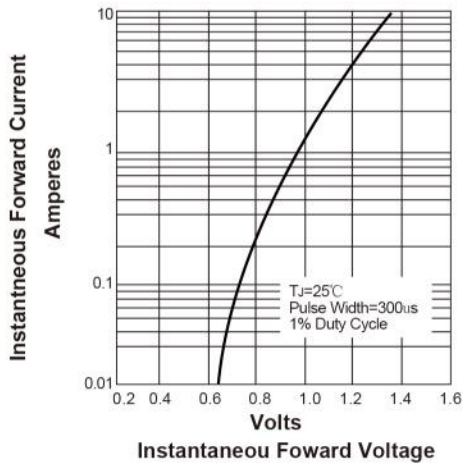


FIG. 4-TYPICAL REVERSE LEAKAGE CHARACTERISTICS

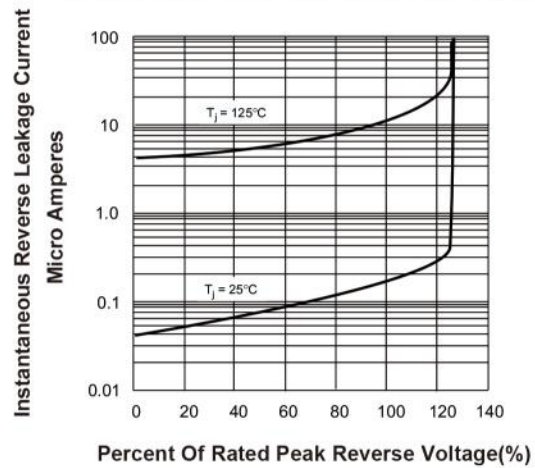


FIG. 5-TYPICAL JUNCTION CAPACITANE

