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電源管理



顯示驅動



二三極管



LDO穩壓器



觸摸芯片



MOS管



運算放大器



存儲芯片



MCU



串口通信

D8KB10-TD

產品規格說明書

6A Single-Phase Bridge Rectifier

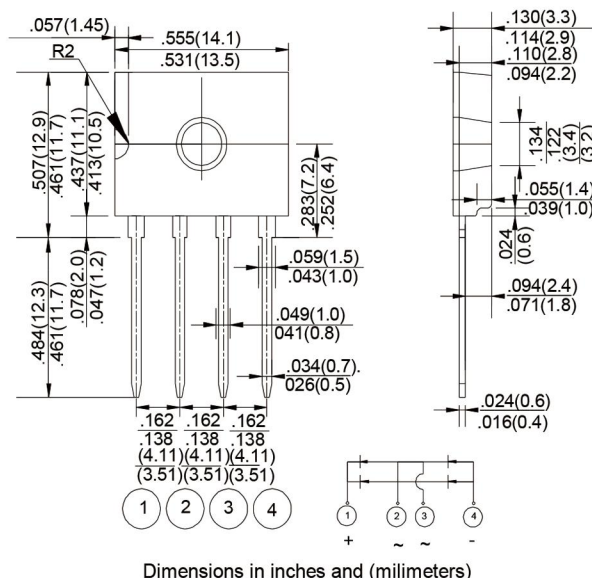
Rectifier Reverse Voltage 50 to 1000V

Features

- This series is UL listed under the Recognized Component Index, file number E142814
- Ideal for printed circuit board mounting
- The plastic material used carries Underwriters Laboratory flammability recognition 94V-0
- Built-in printed circuit board stand-offs
- High case dielectric strength
- High temperature soldering guaranteed 265°C /10 seconds at 5 lbs (2.3kg) tension

Mechanical Data

Case: Reliable low cost construction utilizing molded plastic technique
 Terminals: Plated leads solderable per MIL-STD-202, Method 208
 Mounting Position: Any



Maximum Ratings & Thermal Characteristics

Rating at 25°C ambient temperature unless otherwise specified, Resistive or Inductive load, 60 Hz.
 For Capacitive load derate current by 20%.

Parameter	Symbol	D8KB001	D8KB010	D8KB020	D8KB040	D8KB060	D8KB080	D8KB100	unit
Maximum repetitive peak reverse voltage	VRRM	50	100	200	400	600	800	1000	V
Maximum RMS bridge input voltage	VRMS	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	VDC	50	100	200	400	600	800	1000	V
Maximum average forward rectified output current at TA=50°C	IF(AV)	6							A
Peak forward surge current single sine-wave superimposed on rated load (JEDEC Method)	IFSM	150							A
Rating for fusing (t<8.3ms)	I ² t	15.0							A ² sec
Typical thermal resistance per element (1)	RthJA	10.0							°C / W
Typical junction capacitance per element (2)	Cj	25.0							pF
Operating junction and storage temperature range	TJ, TSTG	-55 to + 150							°C

Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified. Resistive or Inductive load, 60Hz.
 For Capacitive load derate by 20 %.

Paramet	Symbol	D8KB001	D8KB010	D8KB020	D8KB040	D8KB060	D8KB080	D8KB100	Unit
Maximum instantaneous forward voltage drop per leg at 6.0A	VF	1.1							V
Maximum DC reverse current at rated TA =25°C DC blocking voltage per element TA =125°C	IR	5.0 500							μA

Notes: (1)Thermal resistance from Junction to Ambient on P.C.board mounting.
 (2)Measured at 2.0MHz and applied reverse voltage of 4.0 volts.

Rating and Characteristic Curves ($T_A=25^{\circ}\text{C}$ Unless otherwise noted)

Fig. 1 Derating Curve for Output Rectified Current

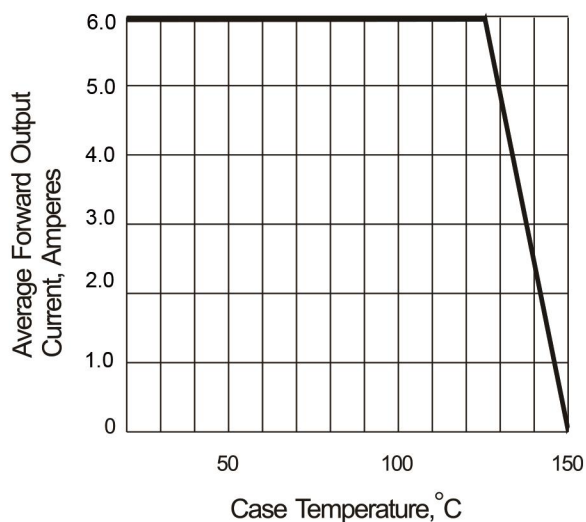


Fig. 2 Maximum Non-repetitive Peak Forward Surge Current

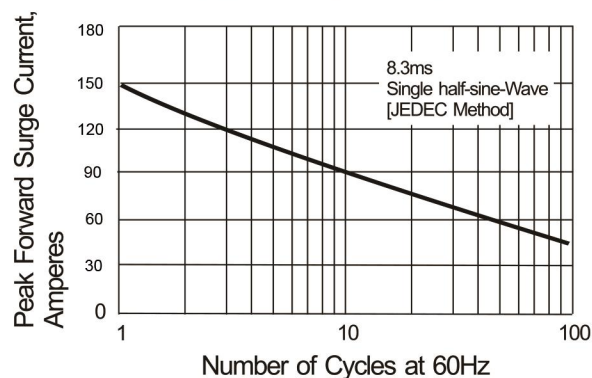


Fig. 3 Typical Instantaneous Forward Characteristics

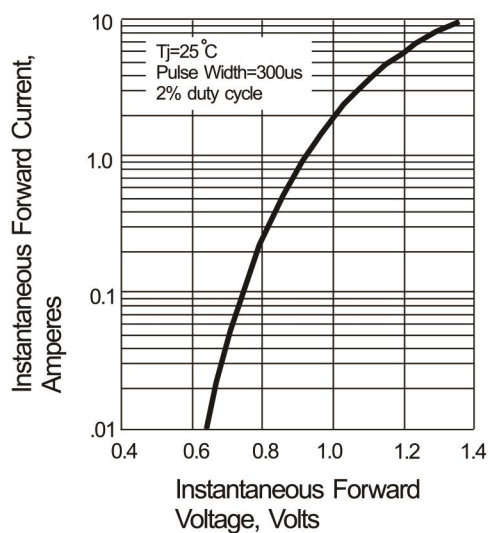


Fig. 4 Typical Reverse Characteristics

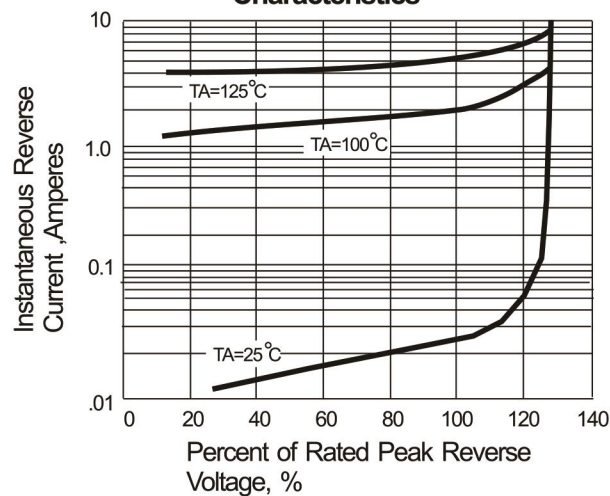


Fig. 5 Typical Junction Capacitance

