

TDS:EMIC

拓 電 半 導 體

自主封測 品質把控 售後保障

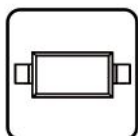
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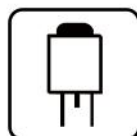
電源管理



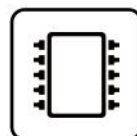
顯示驅動



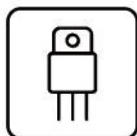
二三極管



LDO穩壓器



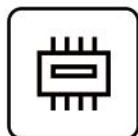
觸摸芯片



MOS管



運算放大器



存儲芯片



MCU



串口通信

E2J-TD

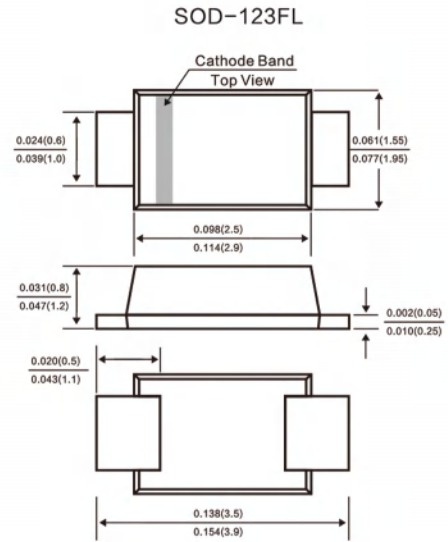
產品規格說明書

Features

- Low Power Loss, High Efficiency
- Ideally Suited for Automatic Assembly
- Guard Ring Die Construction
- Plastic Case Material has UL Flammability Classification Rating 94V- 0

Mechanical Data

- Case: Molded plastic SOD-123FL
- Terminals: Plated leads solderable per MIL-STD-750, Method 2026 guaranteed
- Polarity: Color band denotes cathode end
- Mounting Position: Any
- Marking: Type Number



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load

For capacitive load derate current by 20%

Type Number	SYMBOL	E2A	E2B	E2D	E2G	E2J	Unit
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	50	100	200	400	600	V
Maximum RMS Voltage	V _{RMS}	35	70	140	280	420	V
Maximum DC Blocking Voltage	V _{DC}	50	100	200	400	600	V
Average Rectified Output Current @T _L =90°C	I _{F(AV)}	2.0					A
Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	60					A
I ² t Rating for Fusing (t < 8.3ms)	I ² t	14.940					A ² s
Forward Voltage @IF=2.0A	V _{FM}	0.95			1.25	1.7	V
Peak Reverse Current @T _A =25 °C	I _R	5.0					uA
At Rated DC Blocking Voltage @T _A =125 °C		150					
Maximum Reverse Recovery Time (Note 1)	T _{rr}	35					ns
Typical Junction Capacitance (Note 2)	C _J	25					pF
Typical Thermal Resistance Junction to Ambient(Note 3)	R _{θ JA}	34					°C/W
Operating Temperature Range	T _J	-55 to +150					°C
Storage Temperature Range	T _{STG}	-55 to +150					°C

Note: 1. Reverse Recovery Test Conditions: $I_F = 0.5\text{A}$, $I_R = 1.0\text{A}$, $IRR = 0.25\text{A}$.

2. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C

3. 8.0MM² (.013mm Thick) Land Areas.

