

TDSEMIC

拓電半導體

自主封測 品質把控 售後保障

WEB | WWW.TDSEMIC.COM



電源管理



顯示驅動



二三極管



LDO穩壓器



觸摸芯片



MOS管



運算放大器



存儲芯片



MCU



串口通信

KBL610-TD

產品規格說明書

Reverse Voltage - 50 to 1000 V olts Forward Current - 6.0Amperes SILICON BRIDGE RECTIFIERS

Features

- ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ◆ Ideal for printed circuit boards
- ◆ Low reverse leakage
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed:
260°C/10 seconds, 0.375" (9.5mm) lead length, 5 lbs. (2.3kg) tension

Mechanical Data

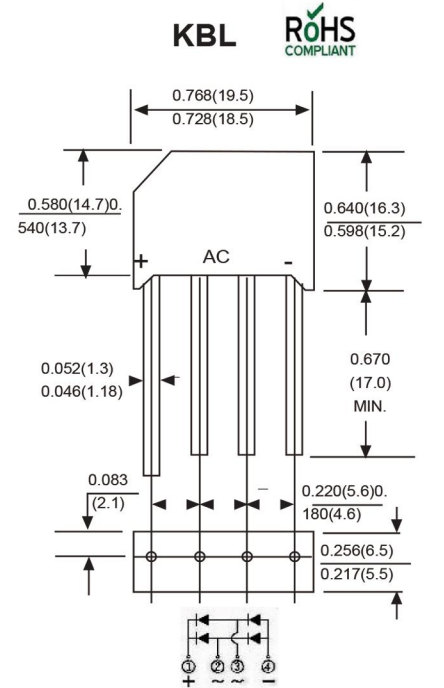
Case : JEDEC KBL Molded plastic body

Terminals : Solder plated, solderable per MIL-STD-750, Method 2026

Polarity : Polarity symbol marking on body

Mounting Position : Any

Weight : 0.22ounce , 6.21 grams



Dimensions in inches and (millimeters)

Maximum Ratings And Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified.

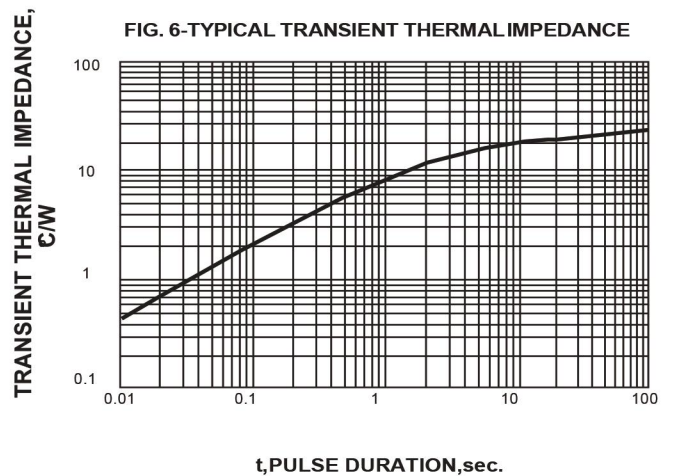
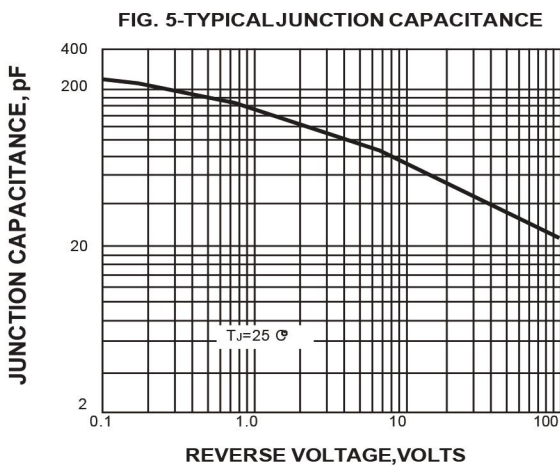
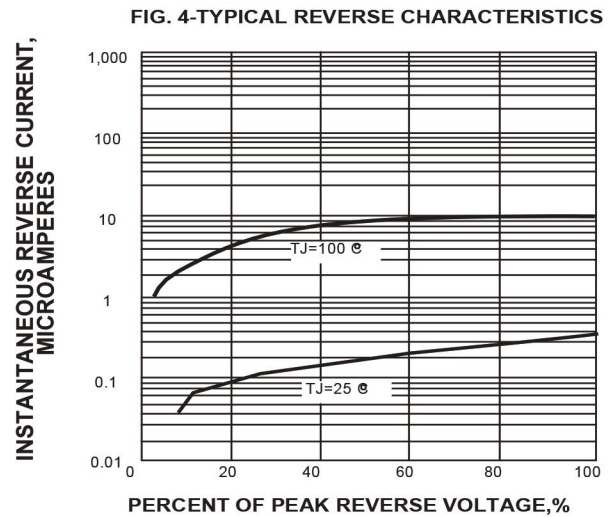
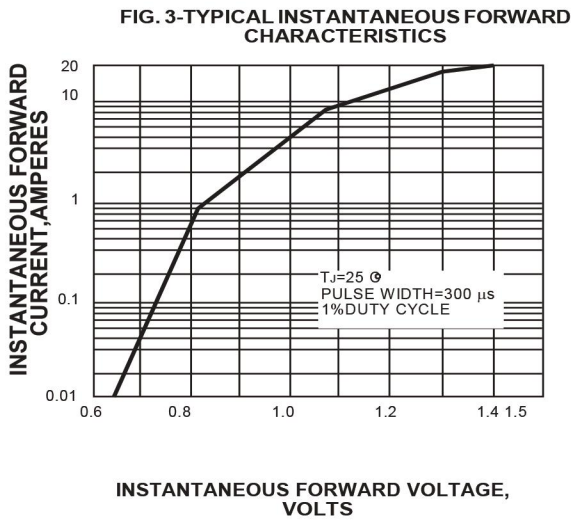
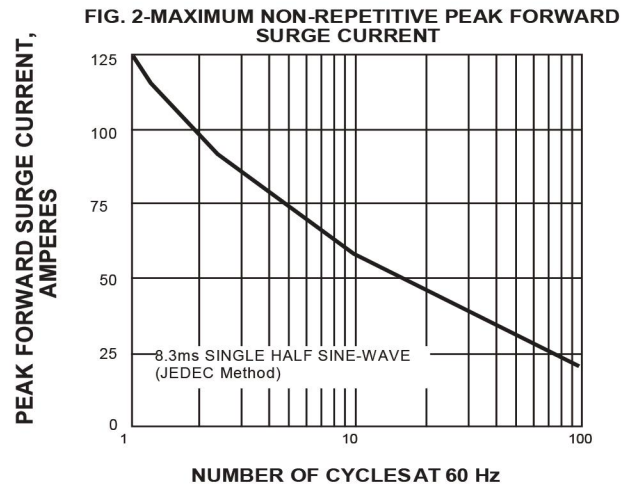
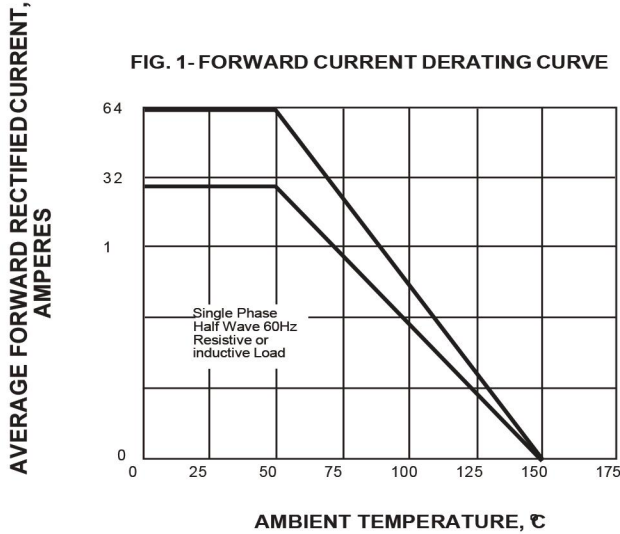
Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

Parameter	SYMBOLS	CRK KBL6005	CRK KBL601	CRK KBL602	CRK KBL604	CRK KBL606	CRK KBL608	CRK KBL610	UNITS
Marking Code									
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum average forward output rectified current at $T_C=50^\circ\text{C}$ (Note 2) at $T_A=50^\circ\text{C}$ (Note 3)	I_{AV}	6.0 3.8							A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	125							A
Maximum instantaneous forward voltage drop per bridge element at 6.0A	V_F	1.1							V
Maximum DC reverse current at rated DC blocking voltage $T_A=25^\circ\text{C}$ $T_A=100^\circ\text{C}$	I_R	10 1.0							μA mA
Typical Junction Capacitance (Note 1)	C_J	105							pF
Typical Thermal Resistance (Note 2)	$R_{\theta JA}$	20							$^\circ\text{C/W}$
Operating junction temperature range	T_J	-55 to +150-							$^\circ\text{C}^\circ$
storage temperature range	T_{STG}	55 to +150							C

NOTES:

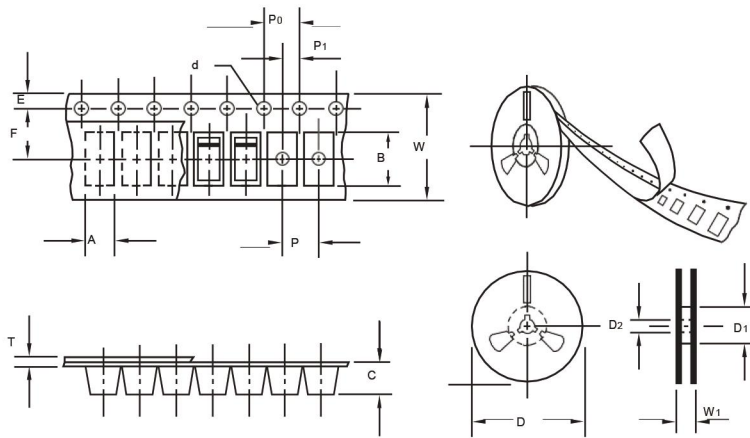
1. Measured at 1.0 MHz and applied reverse voltage of 4.0 Volts.
2. Unit mounted on 3.0" x 3.0" x 0.11" thick (7.5x7.5x0.3cm) Al. plate.
3. P.C. Board mounted with 0.5" x 0.5" (12x12mm) copper pads, 0.375" (9.5mm) lead length.

Reverse Voltage - 50 to 1000 V olts Forward Current - 6.0Amperes



The curve above is for reference only.

Reverse Voltage - 50 to 1000 V olts Forward Current - 6.0Amperes



unit:mm

Item	Symbol	Tolerance	ABS
Carrier width	A	0.1	5.45
Carrier length	B	0.1	7.00
Carrier depth	C	0.1	1.60
Sprocket hole	d	0.05	1.55
13" Reel outside diameter	D	2.0	330.00
13" Reel inner diameter	D1	min	50.00
Feed hole diameter	D2	0.5	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	5.50
Punch hole pitch	P	0.1	8.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.25
Tape width	W	0.3	12.00
Reel width	W1	1.0	12.40

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (mm)	BOX (pcs)	INNER BOX (mm)	REEL DIA. (mm)	CARTON SIZE (mm)	CARTON (pcs)	APPROX. GROSS WEIGHT (kg)
ABS	13"	3,000	4.0	6000	xxx*XXX*XX	330	360*360*360	48000	11.0

Suggested Pad Layout