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拓電半導體

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電源管理



顯示驅動



二三極管



LDO穩壓器



觸摸芯片



MOS管



運算放大器



存儲芯片



MCU



串口通信

MSB40M-TD

產品規格說明書

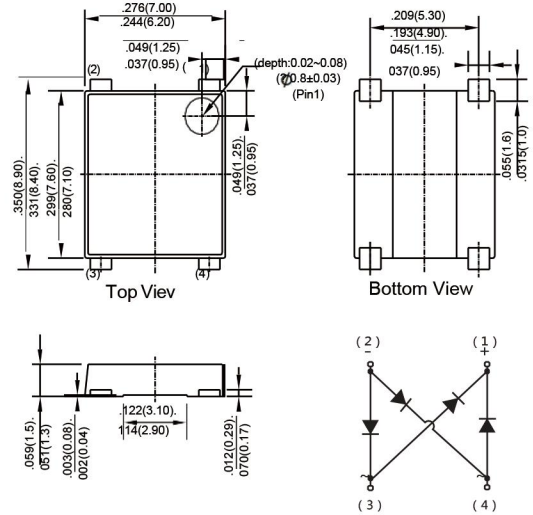
GLASS PASSIVATED SURFACE MOUNT BRIDGE RECTIFIERS

Features

- ◆ Glass Passivated Chip Junction
- ◆ Reverse Voltage - 100 to 1000 V
- ◆ Forward Current- 4 A
- ◆ High Surge Current Capability
- Designed for Surface Mount Application

UMSB

ROHS
COMPLIANT



Dimensions in inches and (millimeters)

Mechanical Data

Case : JEDEC UMSB molded plastic body
 Terminals : Solderable per MIL-STD-750, Method 2026
 Polarity : Polarity symbol marking on body
 Mounting Position : Any
 Weight : 0.00825 ounce, 0.234 grams

Maximum Ratings And Electrical Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

Parameter	Symbols	CRK MSB40D	CRK MSB40G	CRK MSB40J	CRK MSB40K	CRK MSB40M	Units
Marking Code							
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	200	400	600	800	1000	V
Maximum Average Forward Rectified Current	$I_{F(AV)}$	4					A
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method)	I_{FSM}	95					A
Maximum Forward Voltage at 4.0 A	V_F	1.1					V
Maximum DC Reverse Current @ $T_a=25\text{ }^{\circ}\text{C}$ @ at Rated DC Blocking Voltage $T_a=125\text{ }^{\circ}\text{C}$	I_R	5 100					μA
Typical Junction Capacitance per element Note1	C_j	40					pF
Operating and Storage Temperature Range	T_j, T_{stg}	-55 ~ +150					$^{\circ}\text{C}$

Note: 1. Measured at 1MHz and applied reverse voltage of 4 V D.C.

2. P.C.B. mounted with 4×1.5"×1.5" (3.81×3.81 cm) copper pad areas.

Voltage Range - 200 to 1000 Volts Current - 4.0Ampere

Fig.1 Average Rectified Output Current Derating Curve

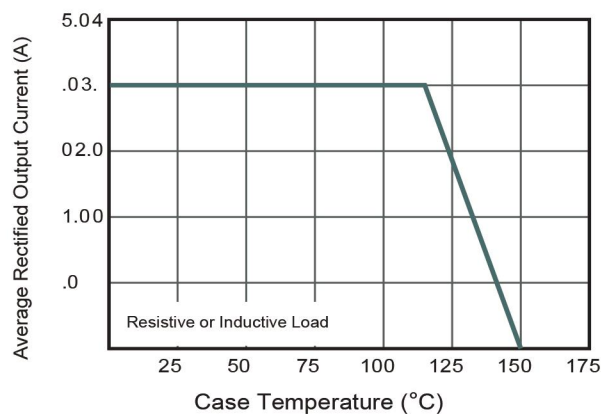


Fig.2 Typical Reverse Characteristics

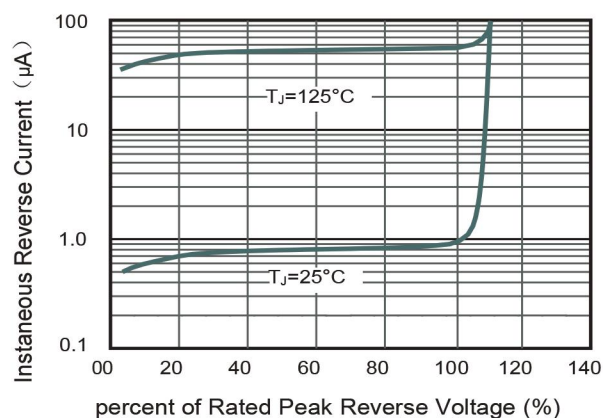


Fig.3 Typical Instantaneous Forward Characteristics

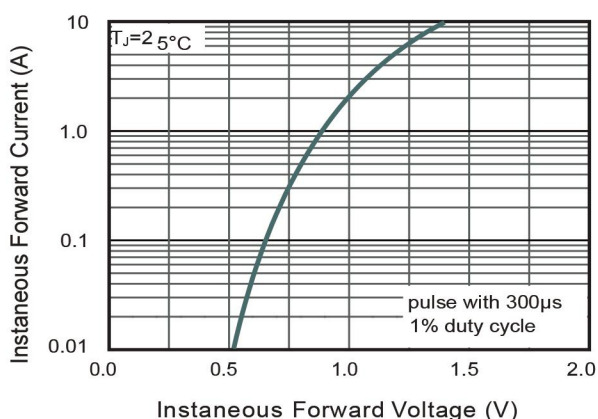


Fig.4 Typical Junction Capacitance

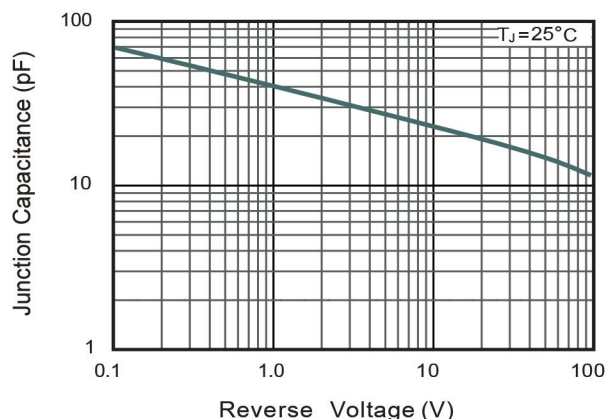
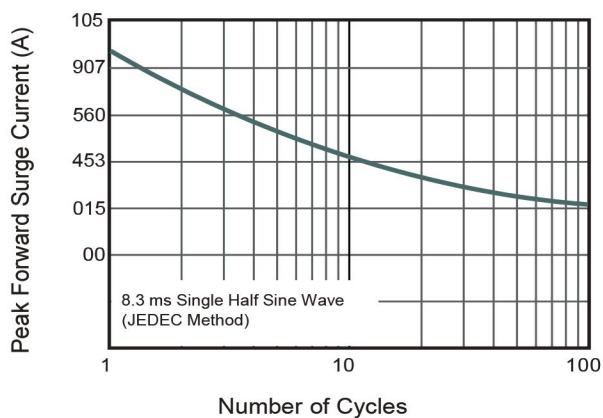
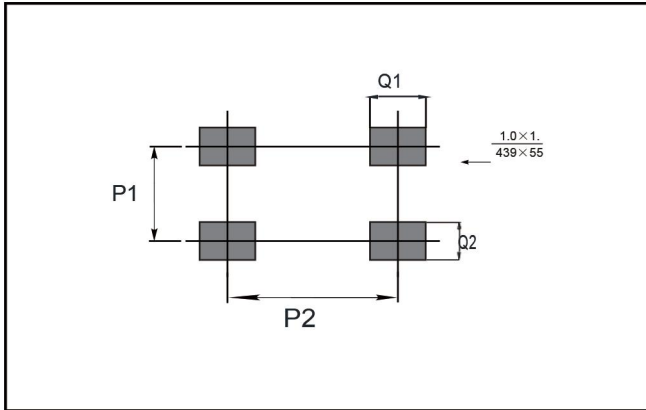


Fig.5 Maximum Non-Repetitive Peak Forward Surge Current





Dim	Min
P1	5.1
P2	7.1
Q1	1.8
Q2	1.3