

TDSEMIC

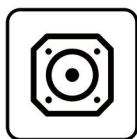
拓電半導體

自主封測 品質把控 售後保障

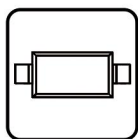
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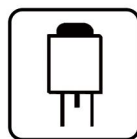
電源管理



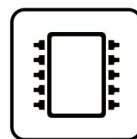
顯示驅動



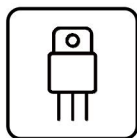
二三極管



LDO穩壓器



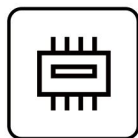
觸摸芯片



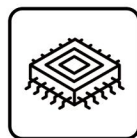
MOS管



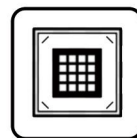
運算放大器



存儲芯片



MCU



串口通信

KBP310-TD

產品規格說明書

Reverse Voltage - 50 to 1000 Volts Forward Current - 3.0Amperes SINGLE BRIDGE RECTIFIERS

Features

- ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ◆ Glass passivated die construction
- ◆ Ideal for printed circuit boards
- ◆ Low reverse leakage
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed: 260°C/10 seconds

Mechanical Data

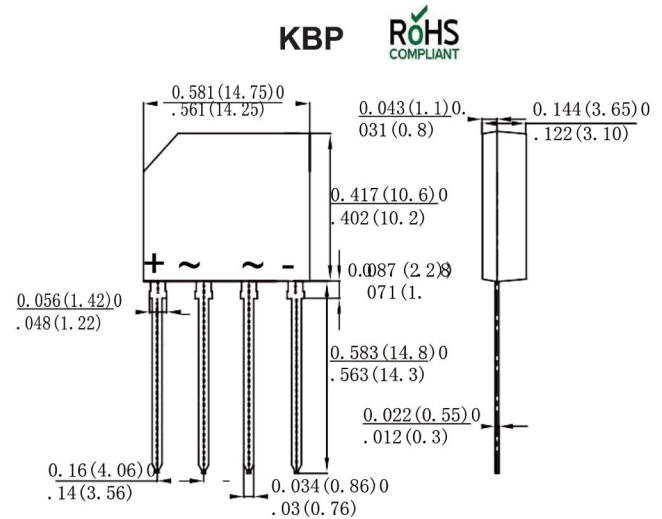
Case : JEDEC KBP Molded plastic body

Terminals : Solder plated, solderable per MIL-STD-750, Method 2026

Polarity : Polarity symbol marking on body

Mounting Position : Any

Weight : 0.050 ounce, 1.52 grams



Dimensions in inches and (millimeters)

Maximum Ratings And Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

Parameter	SYMBOLS	CRK KBP3005	CRK KBP301	CRK KBP302	CRK KBP304	CRK KBP307	CRK KBP308	CRK KBP310	UNITS
Marking Code									
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	50	100	200	400	600	800	1000	V
Maximum average forward output rectified current at T _c =100°C	I _(AV)	3.0							A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	55							A
Maximum instantaneous forward voltage drop per birdge element at 3.0A	V _F	1.15							V
Maximum DC reverse current T _A =25°C at rated DC blocking voltage T _A =125°C	I _R	.0							μA
		0.5							mA
I ² t Rating for fusing (3ms≤t ≤8.3ms)	I ² t	12.5							A ² S
Typical Junction Capacitance per element (Note 1)	C _j	40							pF
Typical Thermal Resistance (Note 2)	R _{θJA}	40							°C/W
	R _{θJC}	10							
	R _{θJL}	18							
Operating junction temperature range	T _J	-55 to +150							°C
Storage temperature range	T _{STG}	-55 to +150							°C

Note: (1) Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

(2) Thermal Resistance Junction to Case, Lead and Ambient.

Reverse Voltage - 50 to 1000 Volts Forward Current - 3.0Amperes

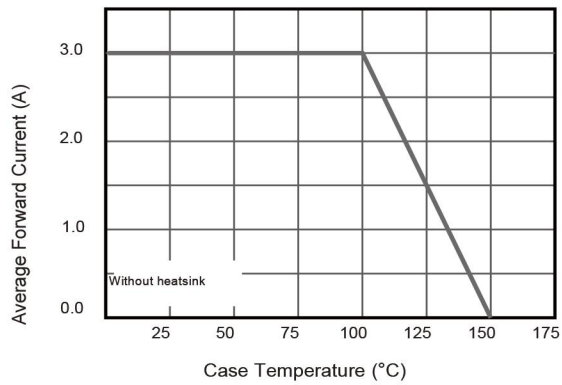


Fig.3 Typical Forward Characteristic

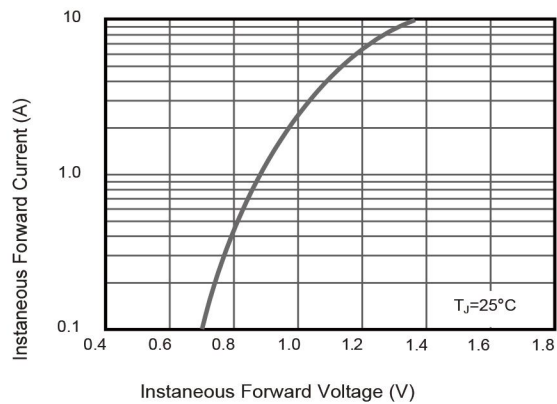


Fig.5 Maximum Non-Repetitive Peak Forward Surge Current

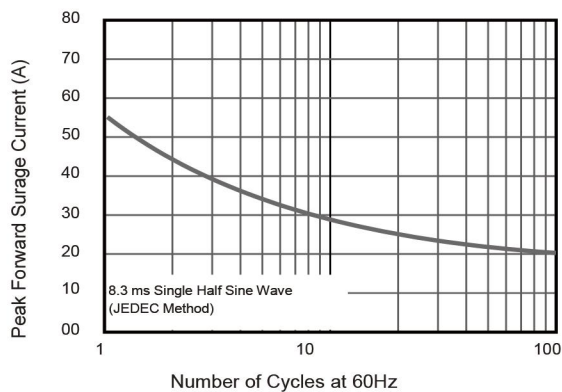


Fig.2 Typical Instantaneous Reverse Characteristics

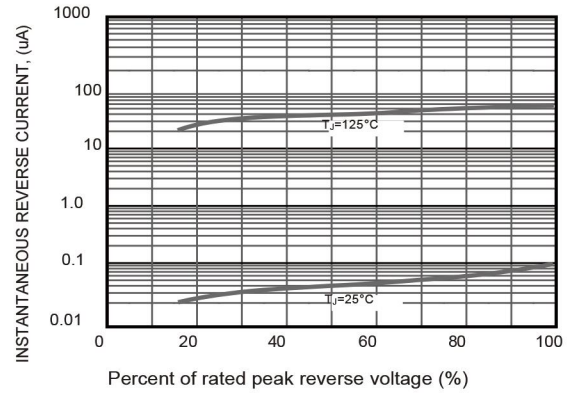


Fig.4 Typical Junction Capacitance

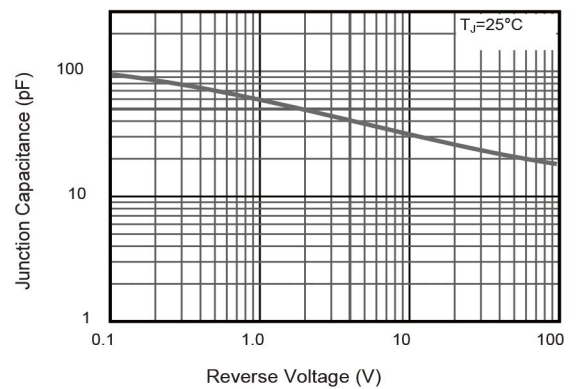


Fig.6- Typical Transient Thermal Impedance

