

TDSEMIC

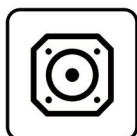
拓電半導體

自主封測 品質把控 售後保障

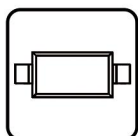
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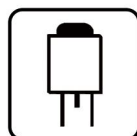
電源管理



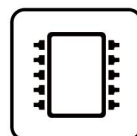
顯示驅動



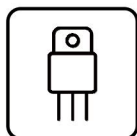
二三極管



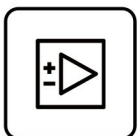
LDO穩壓器



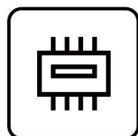
觸摸芯片



MOS管



運算放大器



存儲芯片



MCU



串口通信

KBP410-TD

產品規格說明書

Reverse Voltage - 50 to 1000 Volts Forward Current - 4.0 Amperes SINGLE BRIDGE RECTIFIERS

Features

- ◆ Glass Passivated Chip Junction
- ◆ Glass passivated die construction
- ◆ Low forward voltage drop
- ◆ High current capability
- ◆ High surge current capability
- ◆ The Plastic material-has UL flammability 94V-0

Mechanical Data

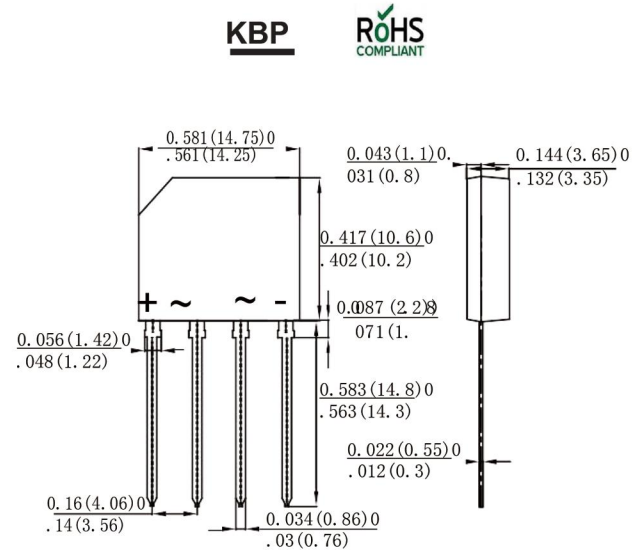
Case : JEDEC KBP Molded plastic body

Terminals : Solder plated, solderable per MIL-STD-750, Method 2026

Polarity : Polarity symbol marking on body

Mounting Position : Any

Weight : 0.050 ounce, 1.52 grams



Dimensions in inches and (millimeters)

Maximum Ratings And Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

Parameter	SYMBOLS	CRK KBP4005	CRK KBP401	CRK KBP402	CRK KBP404	CRK KBP406	CRK KBP408	CRK KBP410	UNITS
Marking Code									
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum average forward output rectified current at $T_c=100^\circ\text{C}$ (With heatsink) (Without heatsink)	I_{AV}	4.02 .0							A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	75.0							A
Maximum instantaneous forward voltage drop per bridge element at 4.0A	V_F	1.15							V
Maximum DC reverse current $T_A=25^\circ\text{C}$ at rated DC blocking voltage $T_A=125^\circ\text{C}$	I_R	.0 0.5							μA mA
I^2t Rating for fusing ($3\text{ms} \leq t \leq 8.3\text{ms}$)	I^2t	23.3							A^2S
Typical Junction Capacitance per element (Note 2)	C_j	50							pF
Typical Thermal Resistance (Note 2)	$R_{\theta JA}$ $R_{\theta JC}$ $R_{\theta JL}$	40 14 20							$^\circ\text{C/W}$
Operating junction temperature range	T_J	-55 to +150							$^\circ\text{C}$
Storage temperature range	T_{STG}	-55 to +150							$^\circ\text{C}$

Note:1. Mounted on glass epoxy PC board with 1.3mm² solder pad.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0V DC.

Reverse Voltage - 50 to 1000 Volts Forward Current - 4.0 Amperes

Fig.1 Forward Current Derating Curve

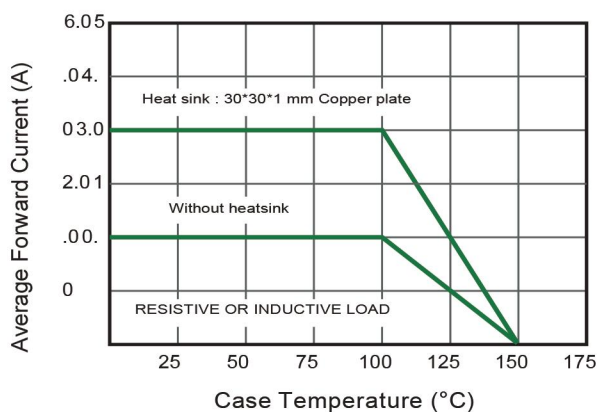


Fig.2 Typical Instantaneous Reverse Characteristics

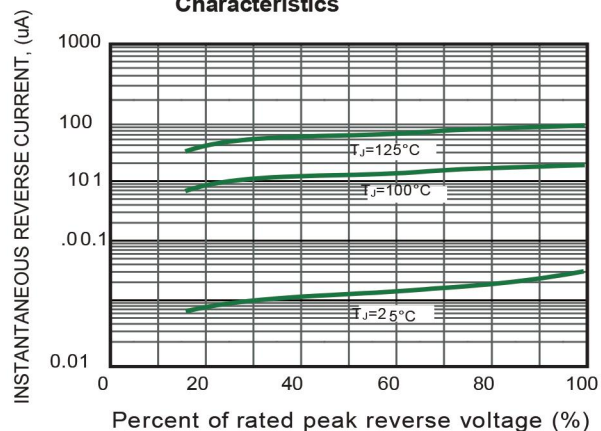


Fig.3 Typical Forward Characteristic

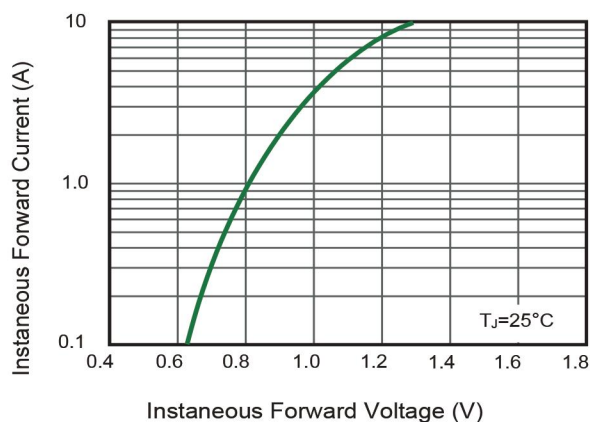


Fig.4 Typical Junction Capacitance

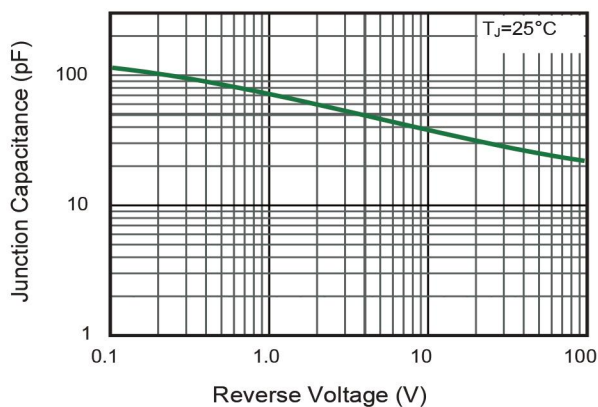


Fig.5 Maximum Non-Repetitive Peak Forward Surge Current

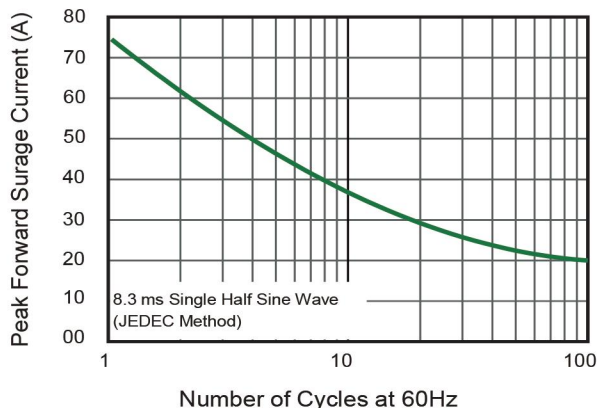
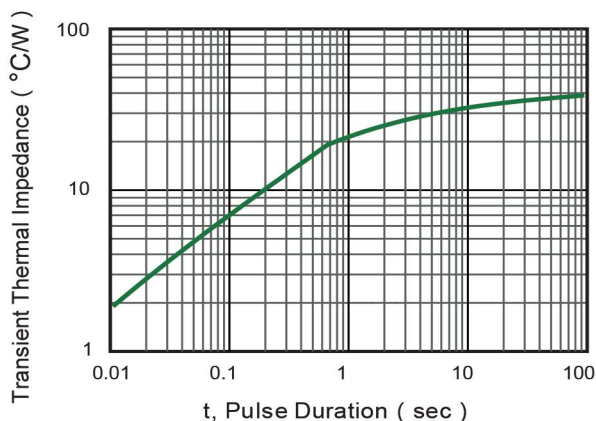


Fig.6- Typical Transient Thermal Impedance



The curve above is for reference only.