

自主封測 品質把控 售後保障

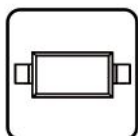
WEB | [WWW.TDSEMIC.COM](http://WWW.TDSEMIC.COM)



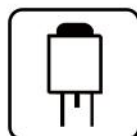
電源管理



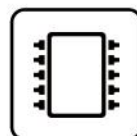
顯示驅動



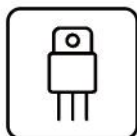
二三極管



LDO穩壓器



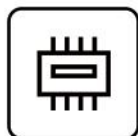
觸摸芯片



MOS管



運算放大器



存儲芯片



MCU



串口通信

## SD103AW S4

產品規格說明書

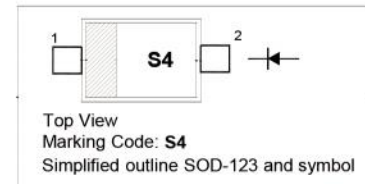
### SD103AW...SD103CW Surface Mount Schottky Barrier Diodes

#### Features

- Low Forward Voltage

#### PINNING

| PIN | DESCRIPTION |
|-----|-------------|
| 1   | Cathode     |
| 2   | Anode       |



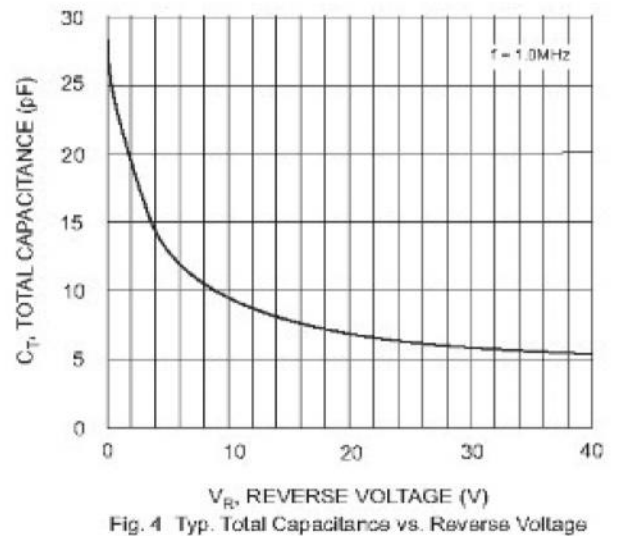
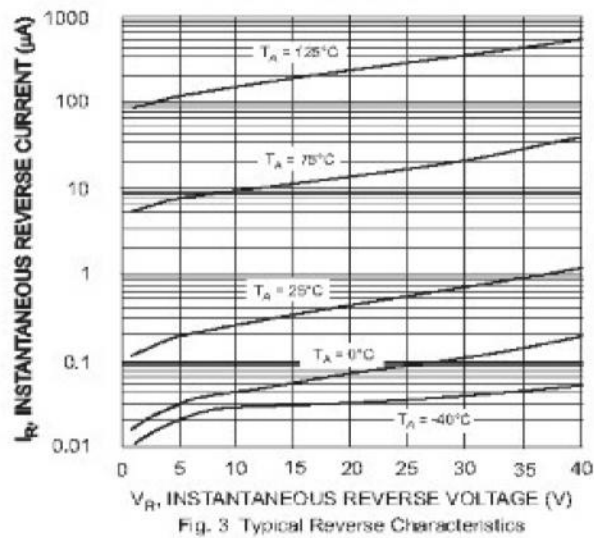
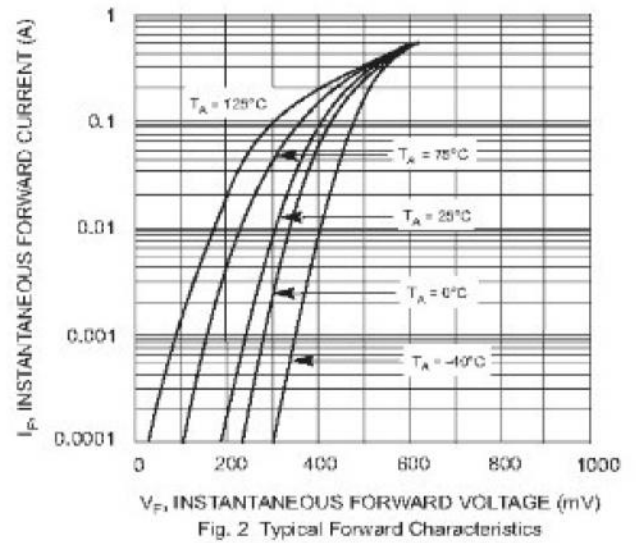
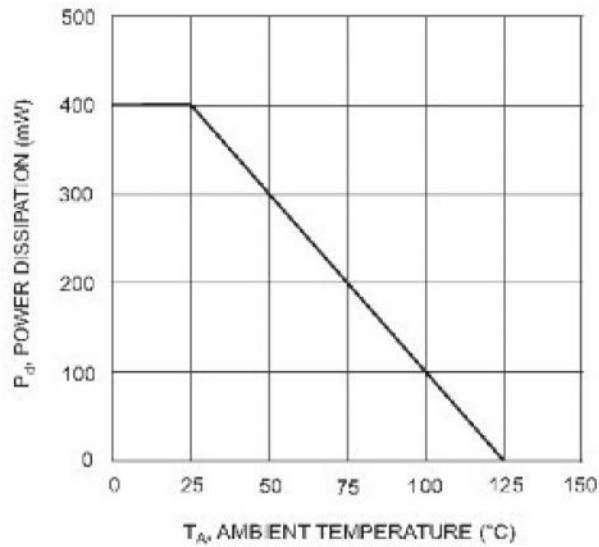
#### Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

| Parameter                                            |                               | Symbol         | Value          | Unit |
|------------------------------------------------------|-------------------------------|----------------|----------------|------|
| Peak Repetitive Reverse Voltage                      | SD103AW<br>SD103BW<br>SD103CW | $V_{RRM}$      | 40<br>30<br>20 | V    |
| Reverse Voltage                                      | SD103AW<br>SD103BW<br>SD103CW | $V_R$          | 40<br>30<br>20 | V    |
| Average Forward Rectified Current                    |                               | $I_{F(AV)}$    | 350            | mA   |
| Non-Repetitive Peak Forward Surge Current at t = 1 s |                               | $I_{FSM}$      | 2              | A    |
| Power Dissipation                                    |                               | $P_{tot}$      | 400            | mW   |
| Operating and Storage Temperature Range              |                               | $T_J, T_{stg}$ | - 65 to + 125  | °C   |

#### Characteristics at $T_a = 25^\circ\text{C}$

| Parameter                                                                                                | Symbol                        | Min.        | Typ.           | Max.        | Unit          |
|----------------------------------------------------------------------------------------------------------|-------------------------------|-------------|----------------|-------------|---------------|
| Reverse Breakdown Voltage<br>at $I_R = 10\text{ }\mu\text{A}$                                            | SD103AW<br>SD103BW<br>SD103CW | $V_{(BR)R}$ | 40<br>30<br>20 | -<br>-<br>- | V             |
| Reverse Leakage Current<br>at $V_R = 30\text{ V}$<br>at $V_R = 20\text{ V}$<br>at $V_R = 10\text{ V}$    | SD103AW<br>SD103BW<br>SD103CW | $I_R$       | -<br>-<br>-    | 5<br>5<br>5 | $\mu\text{A}$ |
| Forward Voltage<br>at $I_F = 20\text{ mA}$<br>at $I_F = 200\text{ mA}$                                   | $V_F$                         | -<br>-      | -<br>-         | 0.37<br>0.6 | V             |
| Total Capacitance<br>at $V_R = 0\text{ V}$ , $f = 1\text{ MHz}$                                          | $C_T$                         | -           | 50             | -           | pF            |
| Reverse Recovery Time<br>at $I_F = I_R = 200\text{ mA}$ , $I_{rr} = 0.1 I_R$ , $R_L = 100\text{ }\Omega$ | $t_{rr}$                      | -           | 10             | -           | ns            |

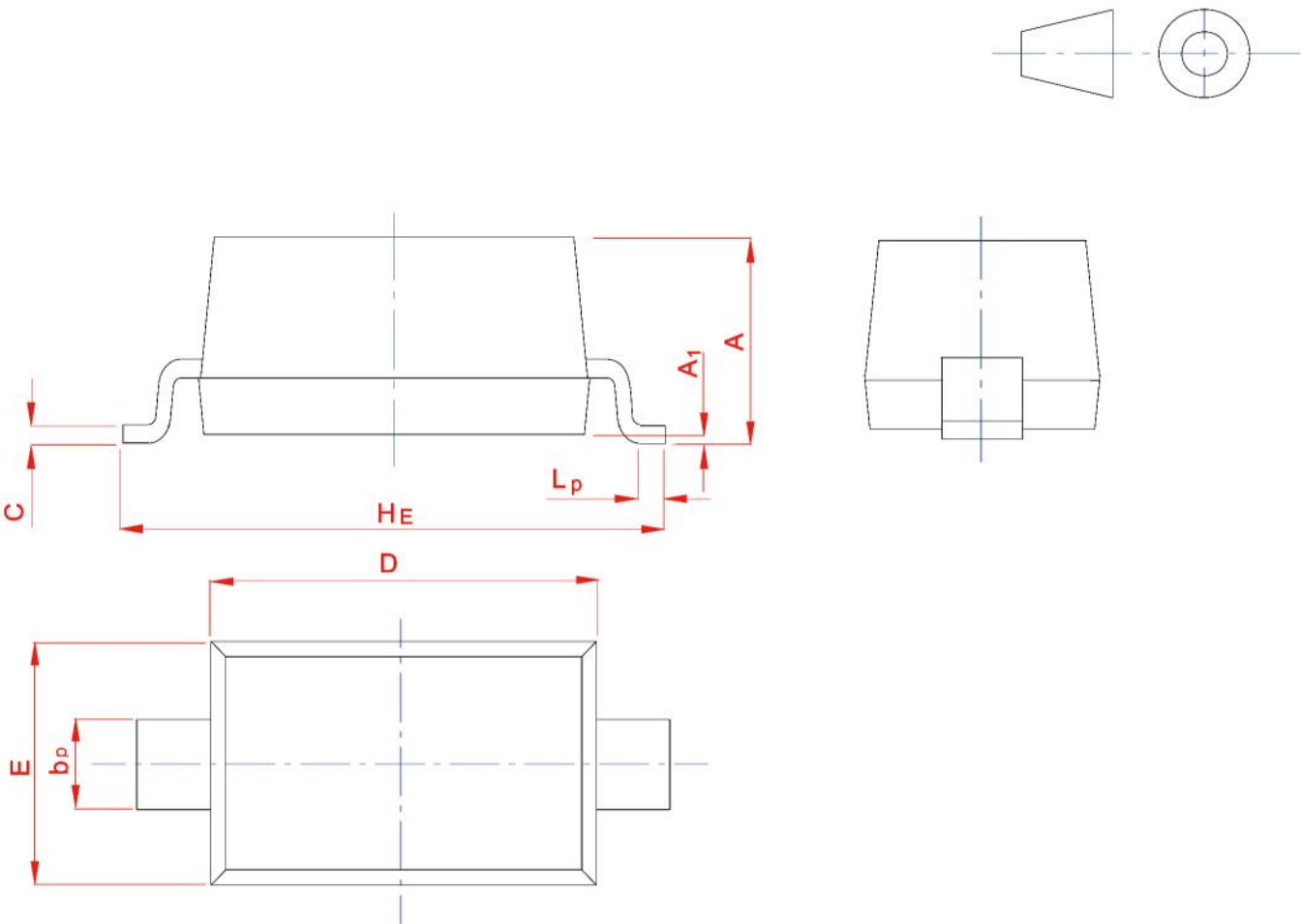
## SD103AW S4



PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SOD-123



| UNIT | A            | bp           | C              | D            | E            | HE           | A1           | Lp           |
|------|--------------|--------------|----------------|--------------|--------------|--------------|--------------|--------------|
| mm   | 1.20<br>0.90 | 0.60<br>0.50 | 0.135<br>0.100 | 2.75<br>2.55 | 1.65<br>1.55 | 3.85<br>3.55 | 0.10<br>0.01 | 0.50<br>0.20 |