



Descriptions

The NLAS2750MUTAG is a dual SPDT low on-resistance analog switch. It can operate from a single 1.5V to 5.5V power supply. The device offers low ON-state resistance and excellent ON-state resistance matching with break-before-make feature, to prevent signal distortion during the transferring of a signal from one channel to another. The device is capable of true isolation. Even when COMx overrides VCC, very little current will flow back to the supply.

Order Information

Package		Part Number	Top-Side Marking
QFN1418(XQFN-10(1.4x1.8))	Tape and Reel	NLAS2750MUTAG	A50 TYW

Features

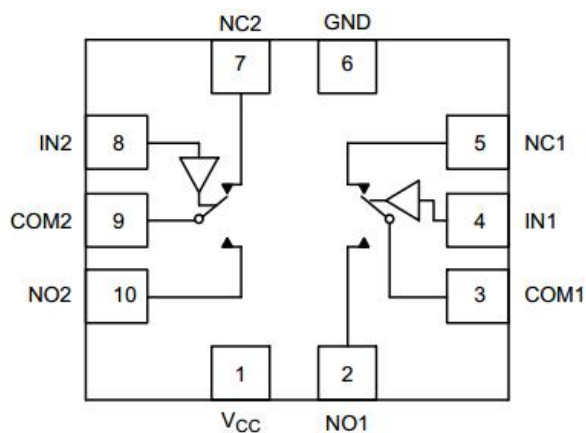
- Pin-to-Pin NLAS2750 and NX5L2750, QFN1418(XQFN-10(1.4x1.8)) Package
- Low On-resistance, $R_{on}=1.5\Omega$ when $COMx=5V$
- 1.8V Logic Compatible Control Pin
- COMx Overrides VCC to Achieve True Isolation Even When Supply Is Dead
- High Off-Isolation: -100dB @ 100KHz
- Low Channel-to-Channel Crosstalk: -97dB @ 100KHz
- High Bandwidth (-3dB @700MHz) Suitable For USB2.0 High-Speed Routing
- Low Quiescent Current (<2uA) With Very Wide Supply Range (1.5V ~ 5.5V)

Applications

- Audio, Video, UART, USB2.0 Signal and Supply Routing
- Cell phones and TWS headset
- USB Type-C Mic/Gnd Switch
- DC Motor Drive



Pin Configuration



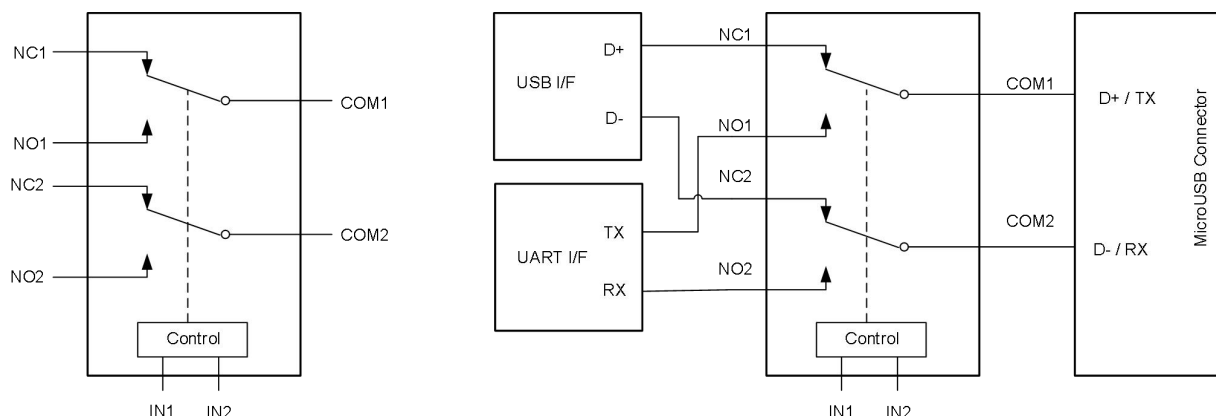
Functions and Pin Configuration

Pin Number	Symbol	Descriptions
1	VCC	Single Power Supply
2,10	NO _x	Analog/Digital Signal Ports (Normally open)
3,9	COM _x	Common Signal Ports
5,7	NC _x	Analog/Digital Signal Ports (Normally closed)
6	GND	Ground
4,8	IN _x	Logic Input Control

Function Descriptions

Logic Input(IN _x)	Function
0	NC1=COM1 and NC2=COM2
1	NO1=COM1 and NO2=COM2

Note: X= 1 or 2



Typical Application: Configured as USB2.0 Mux

Absolute Maximum Ratings ⁽¹⁾

Parameter	Symbol	Value	Unit
Supply Voltage	V_{CC}	-0.3 ~ 6.5	V
Control Input Voltage	V_{IN}	-0.3 ~ 6.5	V
Continuous Current Through NO, NC, COM		±100	mA
Peak Current Through NO, NC, COM (pulsed at 1ms 50% duty cycle)		±200	mA
Storage Temperature Range	T_{STG}	-55 ~ 150	°C
Junction Temperature under Bias	T_J	150	°C
Lead Temperature (Soldering, 10 seconds)	T_L	260	°C
Power Dissipation	P_D	250	mW

Recommend operating ratings ⁽²⁾

Parameter	Symbol	Value	Unit
Supply Voltage Operating	V_{CC}	1.5 ~ 5.5	V
Control Input Voltage	V_{IN}	-0.3 ~ 5.5	V
Input Signal Voltage	V_{COM}	-0.3 ~ 5.5	V
Operating Temperature	T_A	-40 ~ 85	°C
Thermal Resistance	$R_{\theta JA}$	360	°C/W

Note:

1. "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied.



DC Electronics Characteristics (Ta=25°C, VCC=3.3V, unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input logic high level	V_{IH}	VCC: 3.3 ~ 5.5V	1.6			V
		VCC: 1.5 ~ 3.3V	1.4			V
Input logic low level	V_{IL}	VCC: 3.3 ~ 5.5V			0.6	V
		VCC: 1.5 ~ 3.3V			0.4	V
Supply quiescent current	I_{CC}	$I_{COM}=0$, $V_{IN}=0$ or $V_{IN}=V_{CC}$			1.0	uA
Increase in I_{CC} per input	I_{CCT}	$I_{COM}=0$, VCC=4.5V $V_{IN}>1.8$ or $V_{IN}<0.5$			1.0	uA
Off state leakage from COM_x to NC_x (or NO_x)	I_{COMx}	$V_{COM} = 5.5V$, $V_{NC(or NO)} = 0V$			±2.0	uA
On-Resistance	R_{ON1}	$V_{COM}=0 \sim 0.5V$, $I_{COM}=30mA$		3.0	3.5	Ω
	R_{ON2}	$V_{COM}=0.5 \sim 2.0V$, $I_{COM}=30mA$		3.6	3.9	Ω
	R_{ON3}	$V_{COM}=2.0 \sim 4.0V$, $I_{COM}=30mA$		2.5	3.5	Ω
	R_{ON4}	$V_{COM}=4.0 \sim 5.5V$, $I_{COM}=30mA$		1.5	1.8	Ω
On-Resistance Flatness	R_{FLAT1}	$V_{COM}=0 \sim 0.5V$, $I_{COM}=30mA$		0.7		Ω
	R_{FLAT2}	$V_{COM}=0.5 \sim 2.0V$, $I_{COM}=30mA$		0.5		Ω
	R_{FLAT3}	$V_{COM}=2.0 \sim 4.0V$, $I_{COM}=30mA$		1.6		Ω
	R_{FLAT4}	$V_{COM}=4.0 \sim 5.5V$, $I_{COM}=30mA$		0.3		Ω
On-Resistance Matching Between Channels	ΔR_{ON}	$V_{COM}=0\sim 5.5V$, $I_{COM}=30mA$,		0.1	0.2	Ω



AC Electronics Characteristics (Ta=25°C, VCC=3.3V, unless otherwise noted)

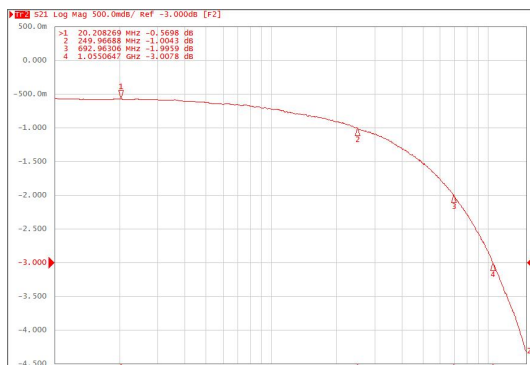
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Turn-On Time	T _{ON}	V _{COM} =1.5V, C _L =35pF, R _L =50Ω		200		ns
Turn-Off Time	T _{OFF}	V _{COM} =1.5V, C _L =35pF, R _L =50Ω		200		ns
Break-Before-Make time	T _{BBM}	V _{COM} =1.5V, C _L =35pF, R _L =50Ω		500		ns
-3dB Bandwidth	BW	R _L =50Ω, C _L =0pF		850		MHz
Off isolation	OIRR	F=1KHz, R _L =50Ω		-81		dB
		F=10KHz, R _L =50Ω		-80		dB
Crosstalk	Xtalk	F=1KHz, R _L =50Ω		-83		dB
		F=10KHz, R _L =50Ω		-82		dB
Total Harmonic Distortion	THD	F=20Hz to 20KHz V _{COM} =600mVp-p @R _L =32Ω,		-80		dB

Capacitance (Ta=25°C, VCC=3.3V, unless otherwise noted)

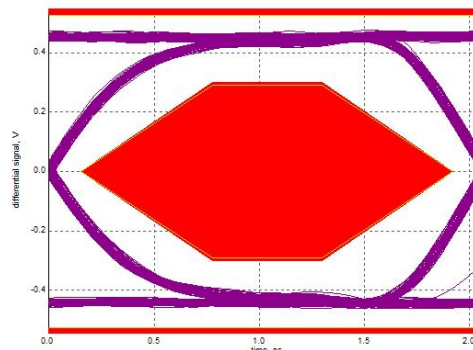
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Off capacitance	C _{OFF}	F=100KHz		5		pF
On capacitance	C _{ON}	F=100KHz		7		pF



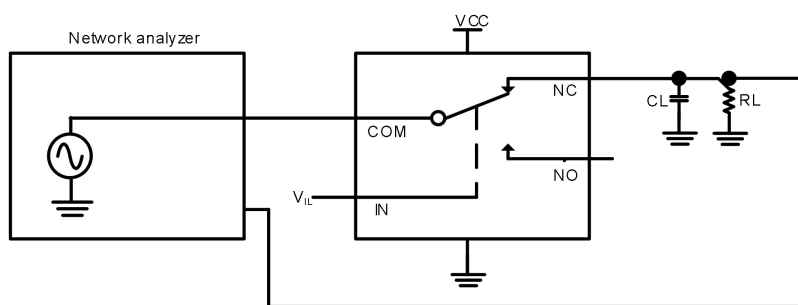
Typical Characteristics (Ta=25°C, VCC=3.3V, unless otherwise noted)



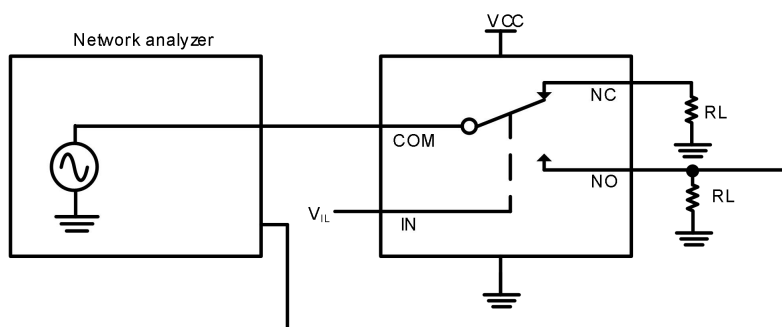
Bandwidth



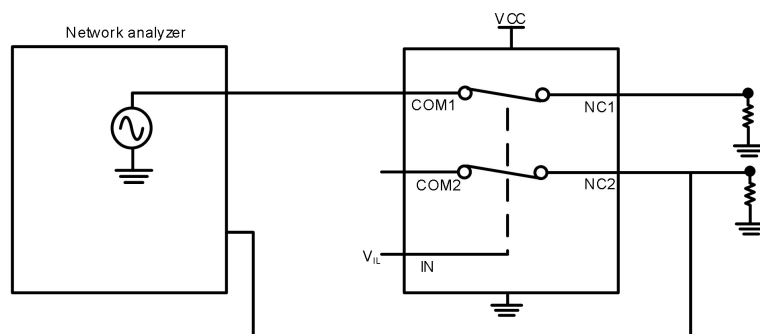
Eye Diagram (480Mbps)



Bandwidth



Off isolation

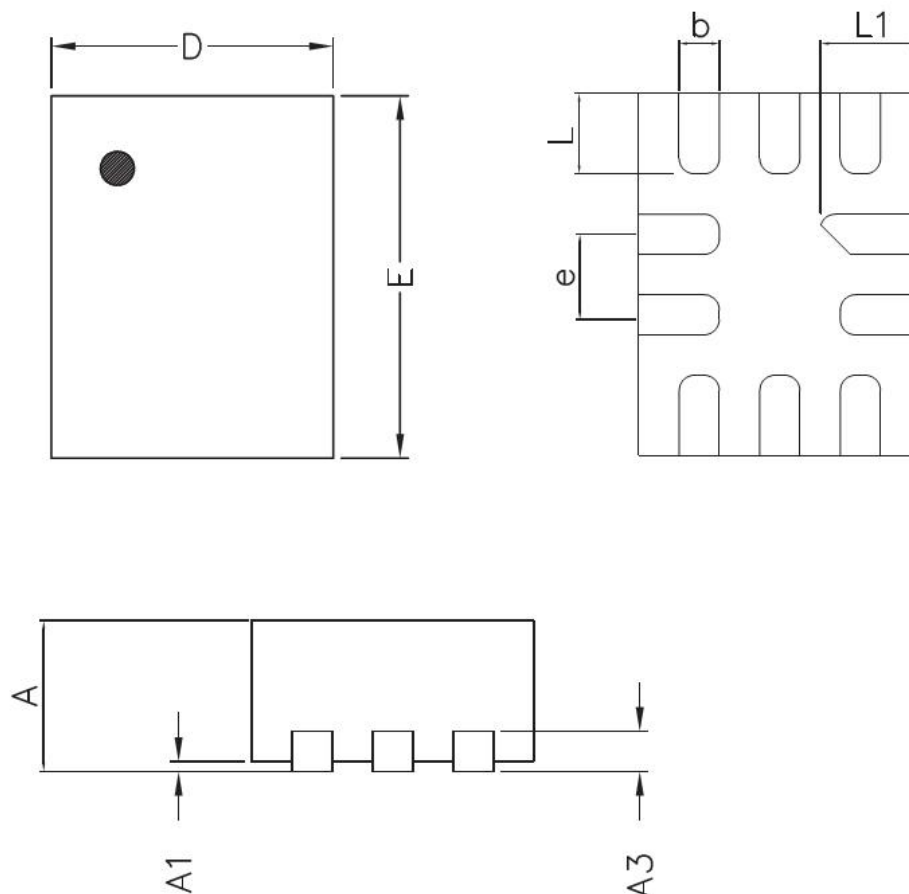


Crosstalk



Package Outline Dimensions

QFN1418(XQFN-10(1.4x1.8))



Symbol	Dimension in Millimeters	
	Min.	Max.
A	0.450	0.550
A1	0.000	0.050
A3	0.152 Ref.	
D	1.350	1.450
E	1.750	1.850
b	0.150	0.250
e	0.400 Typ.	
L	0.350	0.450
L1	0.450	0.550



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