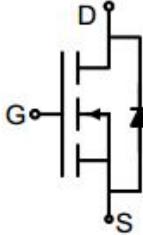


## N-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The GT013N04TLH uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math>, low gate charge. It can be used in a wide variety of applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS}</math> 40V</li> <li>● <math>I_D</math> (at <math>V_{GS} = 10V</math>) 285A</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = 10V</math>) &lt; 1.3m<math>\Omega</math></li> <li>● 100% Avalanche Tested</li> <li>● RoHS Compliant</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Power switch</li> <li>● DC/DC converters</li> </ul>	 <p>Schematic diagram</p>  <p>TOLL-8L</p>
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### Ordering Information

Device	Package	Marking	Packaging
GT013N04TLH	TOLL-8L	GT013N04H	2000pcs/Reel

### Absolute Maximum Ratings $T_C = 25^\circ C$ , unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	40	V
Continuous Drain Current	$I_D$	$T_C = 25^\circ C$	285
		$T_C = 100^\circ C$	180
Pulsed Drain Current (note1)	$I_{DM}$	1140	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Power Dissipation	$P_D$	156	W
Single pulse avalanche energy (note2)	$E_{AS}$	440	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	$^\circ C$

### Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	50	$^\circ C/W$
Maximum Junction-to-Case	$R_{thJC}$	0.8	$^\circ C/W$

Specifications $T_J = 25^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	40	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 40V, V_{GS} = 0V$	--	--	1	$\mu A$
Gate-Source Leakage	$I_{GSS}$	$V_{GS} = \pm 20V$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.0	3.0	4.0	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 50A$	--	1.0	1.3	m $\Omega$
Forward Transconductance	$g_{FS}$	$V_{GS} = 5V, I_D = 50A$	--	80	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V,$ $V_{DS} = 20V,$ $f = 0.6\text{MHz}$	--	5400	--	pF
Output Capacitance	$C_{oss}$		--	3600	--	
Reverse Transfer Capacitance	$C_{rss}$		--	150	--	
Total Gate Charge	$Q_g$	$V_{DD} = 20V,$ $I_D = 50A,$ $V_{GS} = 10V$	--	84	--	nC
Gate-Source Charge	$Q_{gs}$		--	28	--	
Gate-Drain Charge	$Q_{gd}$		--	17	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 20V,$ $I_D = 50A,$ $R_G = 1.6\Omega$	--	13	--	ns
Turn-on Rise Time	$t_r$		--	7	--	
Turn-off Delay Time	$t_{d(off)}$		--	53	--	
Turn-off Fall Time	$t_f$		--	9	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	285	A
Body Diode Voltage	$V_{SD}$	$T_J = 25^\circ\text{C}, I_{SD} = 50A, V_{GS} = 0V$	--	--	1.2	V
Reverse Recovery Charge	$Q_{rr}$	$I_F = 50A, V_{GS} = 0V$ $di/dt = 100A/\mu s$	--	115	--	nC
Reverse Recovery Time	$T_{rr}$		--	35	--	ns

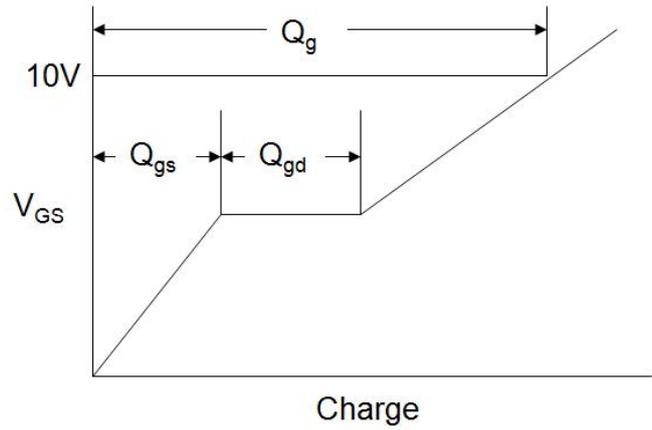
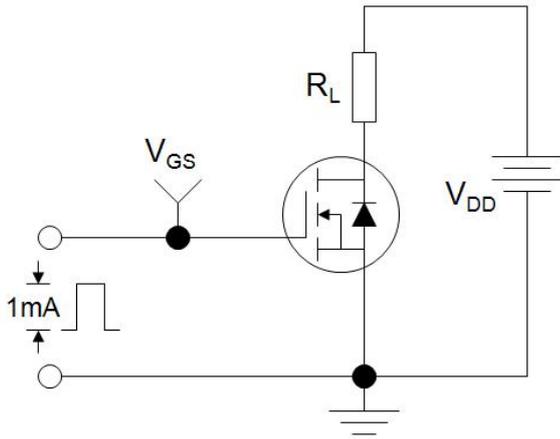
### Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. EAS condition :  $T_J = 25^\circ\text{C}, V_{DD} = 40V, V_{GS} = 10V, L = 0.5\text{mH}, R_G = 25\Omega$

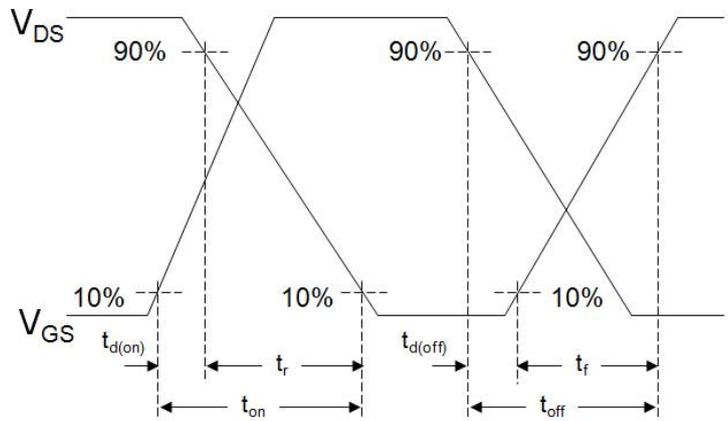
The table shows the minimum avalanche energy, which is 1225mJ when the device is tested until failure

3. Identical low side and high side switch with identical  $R_G$

### Gate Charge Test Circuit



### Switch Time Test Circuit

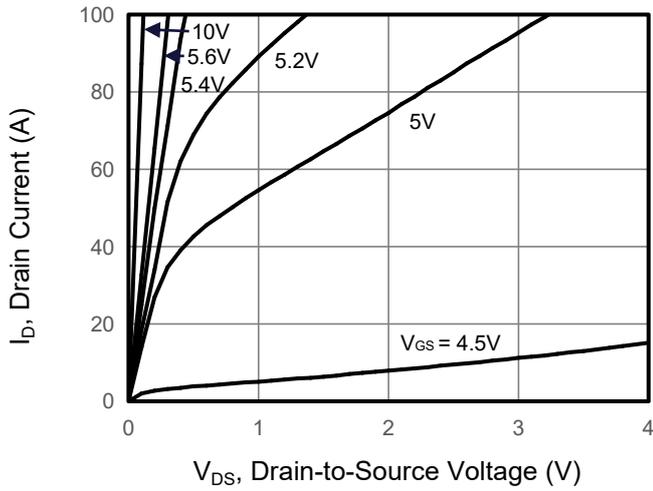


### EAS Test Circuit

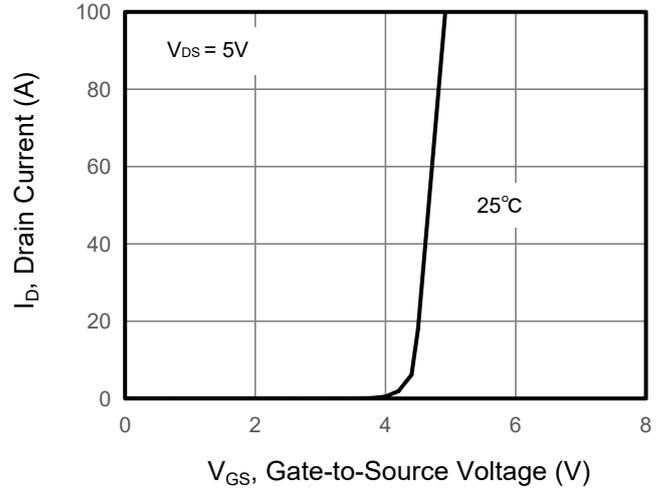


Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

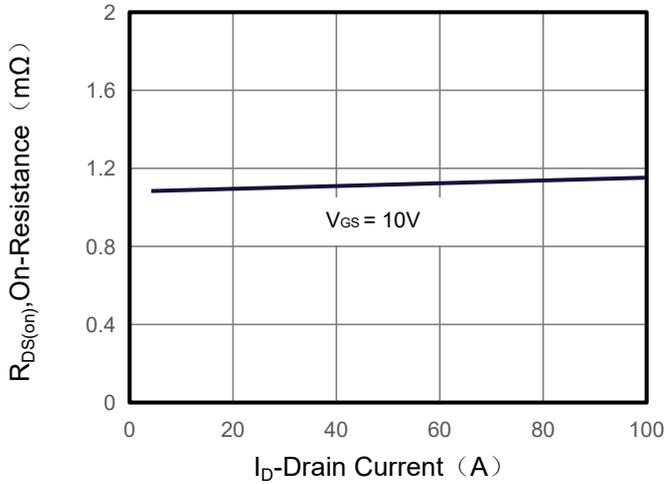
**Figure 1. Output Characteristics**



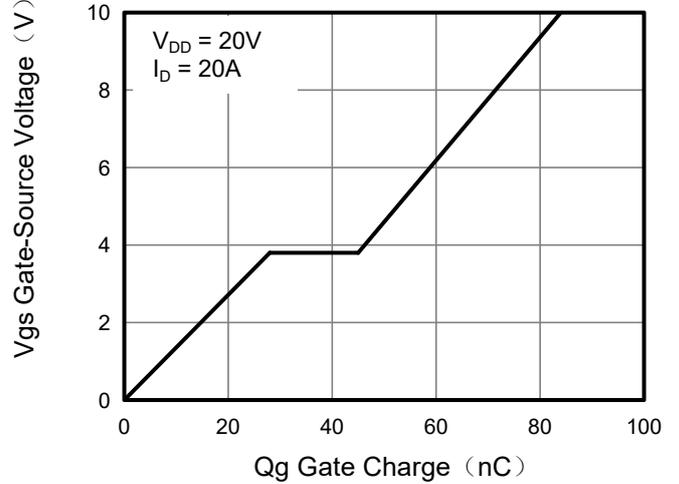
**Figure 2. Transfer Characteristics**



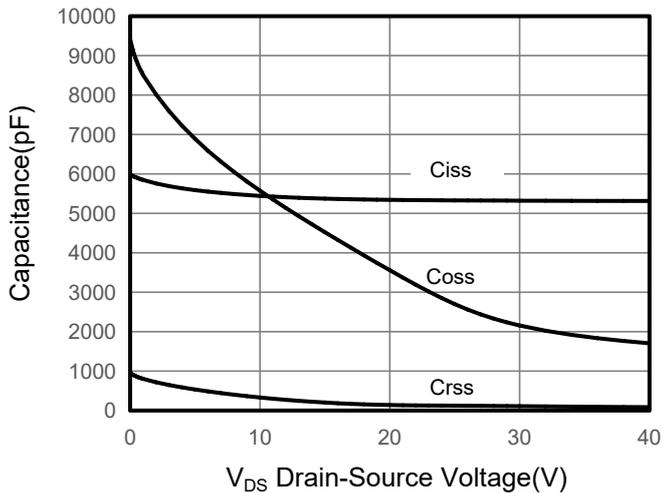
**Figure 3. Drain Source On Resistance**



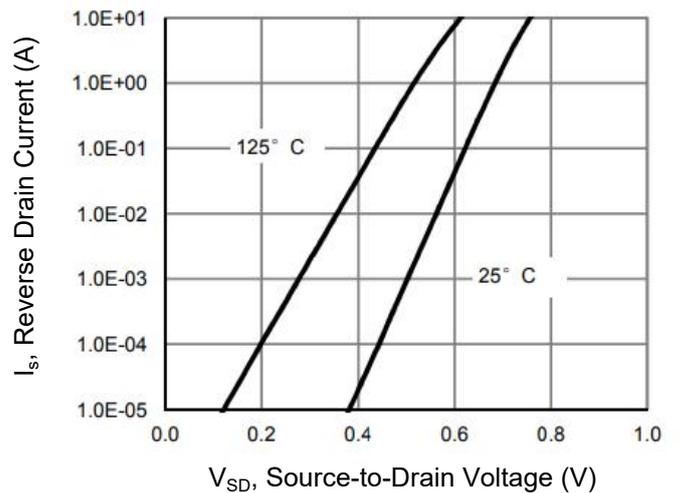
**Figure 4. Gate Charge**



**Figure 5. Capacitance**

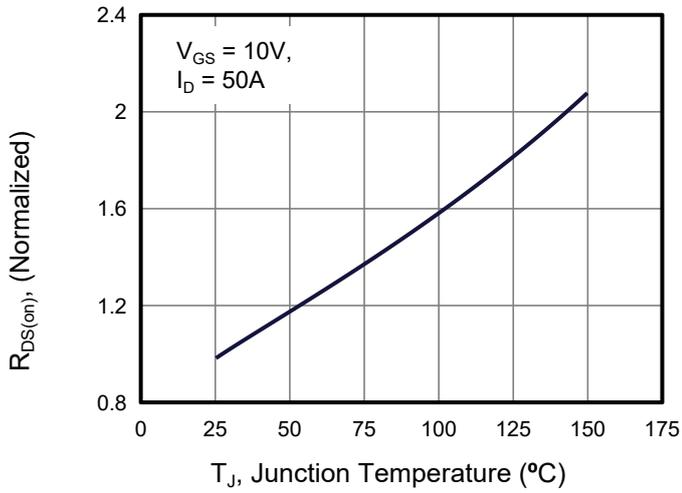


**Figure 6. Source-Drain Diode Forward**

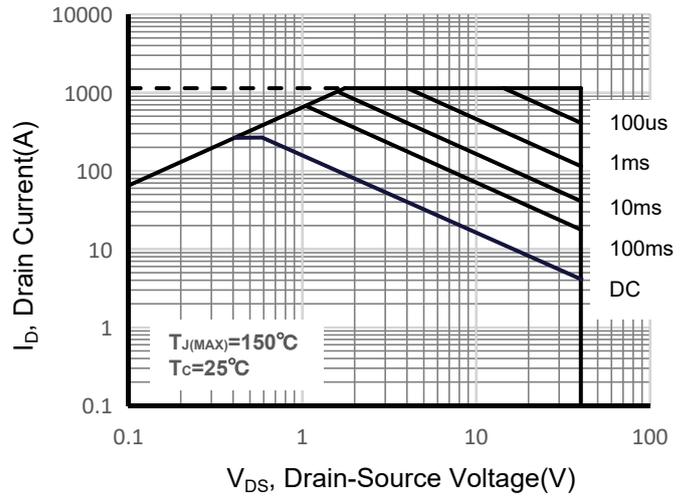


## Typical Characteristics $T_J = 25^\circ\text{C}$ , unless otherwise noted

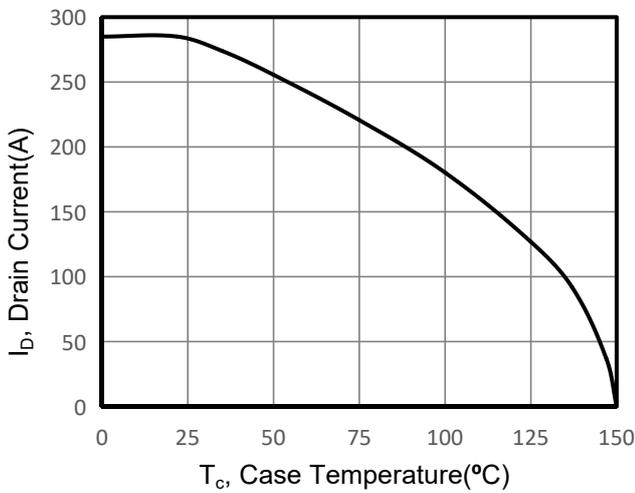
**Figure 7. Drain-Source On-Resistance**



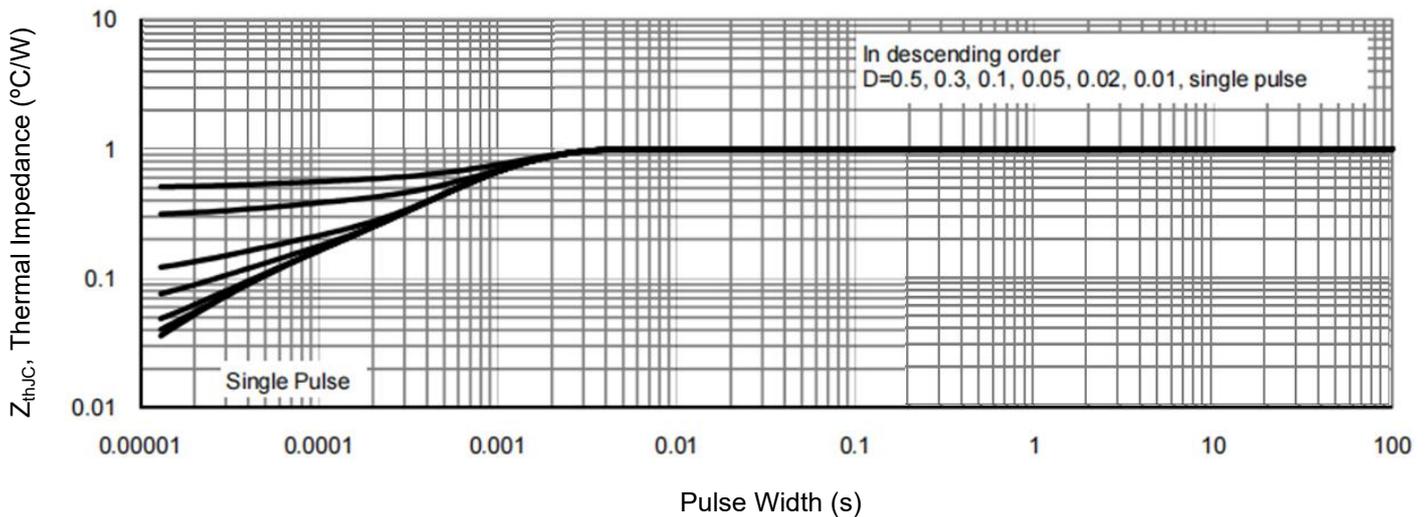
**Figure 8. Safe Operation Area**



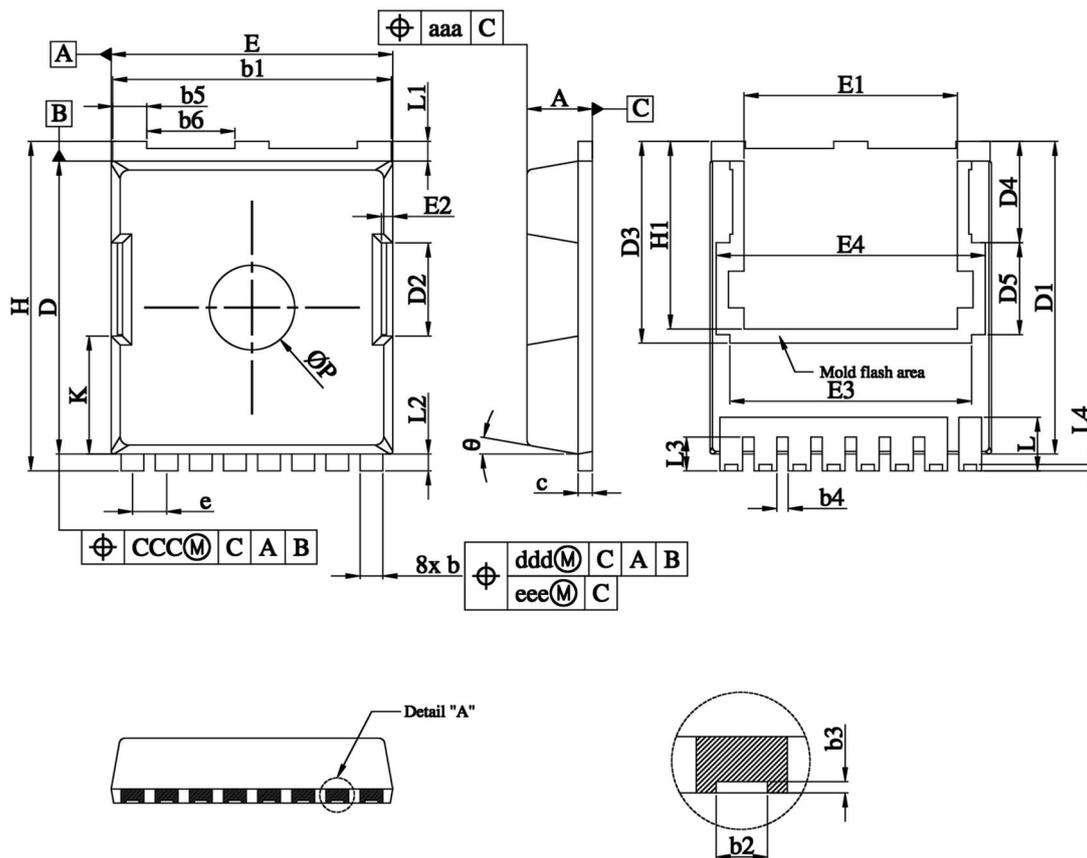
**Figure 9. Maximum Continuous Drain Current vs Case Temperature**



**Figure 10. Normalized Maximum Transient Thermal Impedance**



## TOLL-8L Package Information



SYMBOL	COMMON			SYMBOL	COMMON		
	MILLIMETER				MILLIMETER		
	MIN.	NOMINAL	MAX.		MIN.	NOMINAL	MAX.
A	2.20	2.30	2.40	E2	0.30	0.40	0.50
b	0.70	0.80	0.90	E3	8.50		
b1	9.70	9.80	9.90	E4	9.46		
b2	0.36	0.45	0.55	H	11.50	11.68	11.85
b3	0.05	0.100	/	H1	6.55	6.65	6.75
b4	0.30	0.40	0.50	K	4.08	4.18	4.28
b5	1.10	1.20	1.30	L	1.60	1.90	2.10
b6	3.00	3.10	3.20	L1	0.50	0.70	0.90
c	0.40	0.50	0.60	L2	0.50	0.60	0.70
D	10.28	10.38	10.55	L3	1.00	1.20	1.30
D1	10.98	11.08	11.18	L4	0.13	0.23	0.33
D2	3.20	3.30	3.40	P	2.85	3.00	3.15
D3	7.15			$\theta$	10° REF		
D4	3.59			aaa	0.20		
D5	3.26			ccc	0.20		
e	1.10	1.20	1.30	ddd	0.25		
E	9.80	9.90	10.00	eee	0.20		
E1	7.40	7.50	7.60				