



Descriptions

The NLAS3257AMU3TCG is a single SPDT low on-resistance analog switch. It can operate from a single 1.5V to 5.5V power supply. The device offers low ON-state resistance and excellent ON-state resistance matching with break-before-make feature to prevent signal distortion during the transferring of a signal from one channel to another. The device is capable of truly isolation. Even when A overrides VCC, very little current will flow back to the supply.

Order Information

Package		Part Number	Top-Side Marking
DFN1x1(UDFN-6(1x1))	Tape and Reel	NLAS3257AMU3TCG	A52 TYW

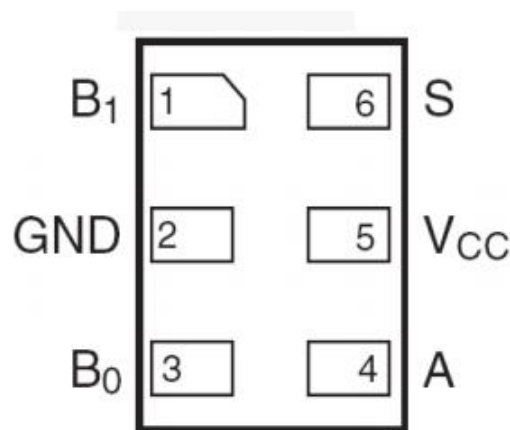
Features

- Pin-to-Pin NLAS3357AMU3TCG, SN74LVC1G3157DRL, DFN1X1(UDFN-6(1x1))Package
- Low On-resistance, $R_{on}=1.5\Omega$ when $A=5V$
- 1.8V Logic Compatible Control Pin
- A Overrides VCC to Achieve True Isolation Even When Supply Is Dead
- High Off-Isolation: -100dB @ 100KHz
- Low Channel-to-Channel Crosstalk: -97dB @ 100KHz
- High Bandwidth (-3dB @700MHz) Suitable For USB2.0 High-Speed Routing
- Low Quiescent Current (<2uA) With Very Wide Supply Range (1.5V ~ 5.5V)

Applications

- Audio, Video, UART, USB2.0 Signal and Supply Routing
- Cell phones and TWS headset

Pin Configuration





Functions and Pin Configuration

Pin Number	Symbol	Descriptions
1	B1	Analog/Digital Signal Port (Normally open)
2	GND	Ground
3	B0	Analog/Digital Signal Port (Normally closed)
4	A	Common Signal Port
5	VCC	Single Power Supply
6	S	Logic Input Control

Function Descriptions

Logic Input	Function
S=0	B0=A
S=1	B1=A

Absolute Maximum Ratings ⁽¹⁾

Parameter	Symbol	Value	Unit
Supply Voltage	V _{CC}	-0.3 ~ 6.5	V
Control Input Voltage	V _S	-0.3 ~ 6.5	V
Continuous Current Through A, B0, B1		±100	mA
Peak Current Through A, B0, B1 (pulsed at 1ms 50% duty cycle)		±200	mA
Storage Temperature Range	T _{STG}	-55 ~ 150	°C
Junction Temperature under Bias	T _J	150	°C
Lead Temperature (Soldering, 10 seconds)	T _L	260	°C
Thermal resistance	R _{θJA}	350	°C/W

Recommend operating ratings ⁽²⁾

Parameter	Symbol	Value	Unit
Supply Voltage Operating	V _{CC}	1.5 ~ 5.5	V
Control Input Voltage	V _S	-0.3 ~ 5.5	V
Input Signal Voltage	V _A	-0.3 ~ 5.5	V
Operating Temperature	T _A	-40 ~ 85	°C

Note:

1. "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied.



DC Electronics Characteristics (Ta=25°C, VCC=3.3V, unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input logic high level	V _{IH}	VCC: 3.3 ~ 5.5V	1.6			V
		VCC: 1.5 ~ 3.3V	1.4			V
Input logic low level	V _{IL}	VCC: 3.3 ~ 5.5V			0.6	V
		VCC: 1.5 ~ 3.3V			0.4	V
Supply quiescent current	I _{CC}	I _A =0, V _S =0 or V _S =VCC			1.0	uA
Increase in I _{CC} per input	I _{CCT}	I _A =0, VCC=4.5V V _S >1.8 or V _S <0.5			1.0	uA
Off state leakage from A to B0 (or B1)	I _A	V _A = 5.5V , V _{B0(or B1)} = 0V			±2.0	uA
On-Resistance	R _{ON1}	V _A =0 ~ 0.5V, I _A =30mA		3.0	3.5	Ω
	R _{ON2}	V _A =0.5 ~ 2.0V, I _A =30mA		3.6	3.9	Ω
	R _{ON3}	V _A =2.0 ~ 4.0V, I _A =30mA		2.5	3.5	Ω
	R _{ON4}	V _A =4.0 ~ 5.5V, I _A =30mA		1.5	1.8	Ω
On-Resistance Flatness	R _{FLAT1}	V _A =0 ~ 0.5V, I _A =30mA		0.7		Ω
	R _{FLAT2}	V _A =0.5 ~ 2.0V, I _A =30mA		0.5		Ω
	R _{FLAT3}	V _A =2.0 ~ 4.0V, I _A =30mA		1.6		Ω
	R _{FLAT4}	V _A =4.0 ~ 5.5V, I _A =30mA		0.3		Ω
On-Resistance Matching Between Channels	Δ R _{ON}	V _A =0~5.5V, I _A =30mA,		0.1	0.2	Ω

AC Electronics Characteristics (Ta=25°C, VCC=3.3V, unless otherwise noted)

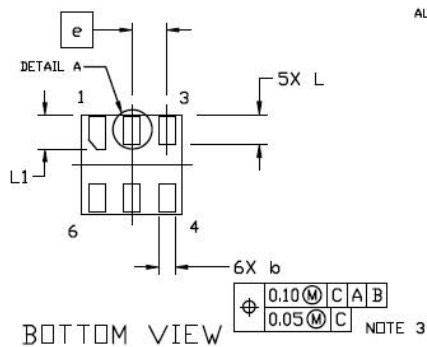
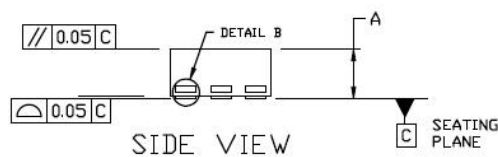
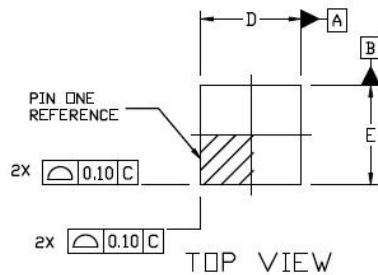
Parameter	Symbol	Conditions	Min.	Typ.	Max	Unit
Turn-On Time	T _{ON}	V _A =1.5V, C _L =35pF, R _L =50Ω		200		ns
Turn-Off Time	T _{OFF}	V _A =1.5V, C _L =35pF, R _L =50Ω		200		ns
Break-Before-Make time	T _{BBM}	V _A =1.5V, C _L =35pF, R _L =50Ω		500		ns
-3dB Bandwidth	BW	R _L =50Ω, C _L =0pF		700		MHz
Off isolation	OIRR	F=1KHz, R _L =50Ω		-81		dB
		F=10KHz, R _L =50Ω		-80		dB
Crosstalk	Xtalk	F=1KHz, R _L =50Ω		-83		dB
		F=10KHz, R _L =50Ω		-82		dB
Total Harmonic Distortion	THD	F=20Hz to 20KHz V _A =600mVp-p @R _L =32Ω,		-80		dB

Capacitance (Ta=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Off capacitance	C _{OFF}	F=100KHz, VCC=3.3		5		pF
On capacitance	C _{ON}	F=100KHz, VCC=3.3		7		pF

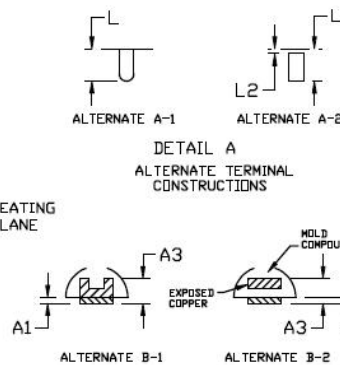


Package Outline Dimensions



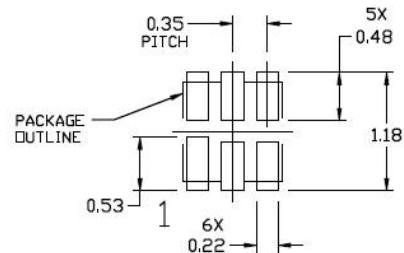
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION b APPLIES TO THE PLATED TERMINALS AND IS MEASURED BETWEEN 0.15 AND 0.20 FROM THE TERMINAL TIPS.
4. PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.



DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.45	0.50	0.55
A1	0.00	0.025	0.05
A3	0.13 REF		
b	0.12	0.17	0.22
D	0.90	1.00	1.10
E	0.90	1.00	1.10
e	0.35 BSC		
L	0.25	0.30	0.35
L1	0.30	0.35	0.40
L2	---	---	0.10

DETAIL B ALTERNATE CONSTRUCTION



RECOMMENDED MOUNTING FOOTPRINT*

* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



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